



# SPEC® CINT2006 Result

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H3C

H3C UniServer R4900 G3

**SPECint\_rate2006 = 1830**

**SPECint\_rate\_base2006 = 1740**

CPU2006 license: 9066

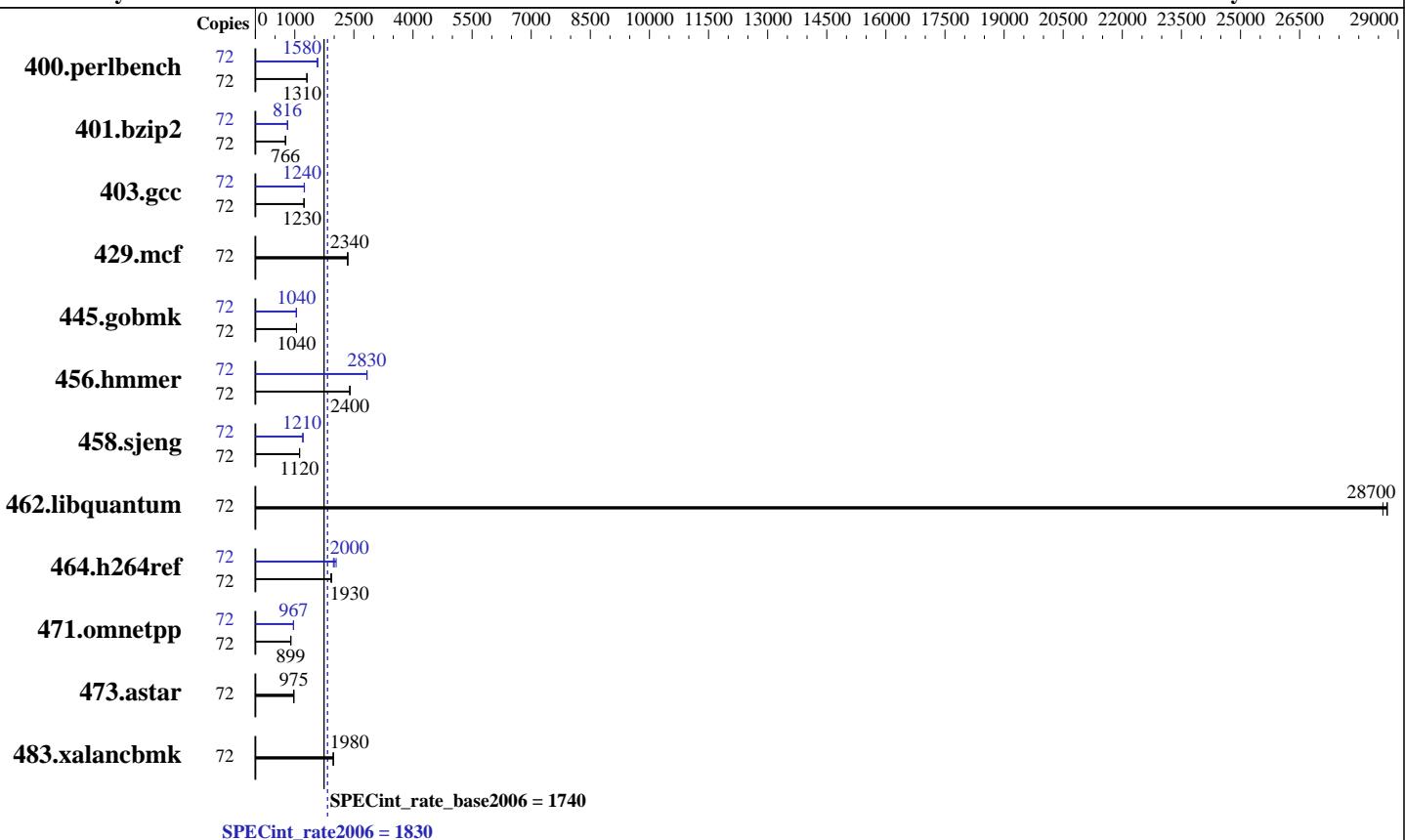
**Test date:** Jul-2017

Test sponsor: H3C

**Hardware Availability:** Jul-2017

Tested by: H3C

**Software Availability:** Jul-2017



## Hardware

CPU Name: Intel Xeon Gold 6140  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 24.75 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)  
 Disk Subsystem: 1 x 480 GB SATA SSD  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
 Compiler: 3.10.0-514.26.2.el7.x86\_64  
 C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	72	538	1310	<b>538</b>	<b>1310</b>	537	1310	72	445	1580	<b>445</b>	<b>1580</b>	448	1570
401.bzip2	72	912	762	904	768	<b>908</b>	<b>766</b>	72	851	816	<b>852</b>	<b>816</b>	853	814
403.gcc	72	<b>469</b>	<b>1230</b>	468	1240	470	1230	72	<b>467</b>	<b>1240</b>	468	1240	465	1250
429.mcf	72	<b>281</b>	<b>2340</b>	280	2350	281	2340	72	<b>281</b>	<b>2340</b>	280	2350	281	2340
445.gobmk	72	724	1040	723	1040	<b>724</b>	<b>1040</b>	72	726	1040	<b>726</b>	<b>1040</b>	726	1040
456.hammer	72	<b>280</b>	<b>2400</b>	281	2390	280	2400	72	<b>237</b>	<b>2830</b>	237	2840	237	2830
458.sjeng	72	<b>777</b>	<b>1120</b>	778	1120	776	1120	72	<b>723</b>	<b>1210</b>	722	1210	723	1200
462.libquantum	72	<b>52.0</b>	<b>28700</b>	51.9	28700	52.1	28600	72	<b>52.0</b>	<b>28700</b>	51.9	28700	52.1	28600
464.h264ref	72	823	1940	<b>826</b>	<b>1930</b>	832	1920	72	777	2050	<b>796</b>	<b>2000</b>	805	1980
471.omnetpp	72	501	898	<b>500</b>	<b>899</b>	500	900	72	466	966	<b>466</b>	<b>967</b>	464	970
473.astar	72	519	974	<b>518</b>	<b>975</b>	518	975	72	519	974	<b>518</b>	<b>975</b>	518	975
483.xalancbmk	72	251	1980	251	1980	<b>251</b>	<b>1980</b>	72	251	1980	251	1980	<b>251</b>	<b>1980</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Set SNC to Enabled

Set IMC Interleaving to 1 way

Set ENERGY\_PERF\_BIAS\_CFG Mode to Performance

Sysinfo program /home/spec/config/sysinfo.rev6993

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)

running on localhost.localdomain Thu Jul 13 19:48:50 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6140 CPU @ 2.30GHz

2 "physical id"s (chips)

72 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with

Continued on next page



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## Platform Notes (Continued)

```
caution.)  
    cpu cores : 18  
    siblings   : 36  
    physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27  
    physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27  
    cache size : 25344 kB  
  
From /proc/meminfo  
MemTotal:      394654100 kB  
HugePages_Total:        0  
Hugepagesize:     2048 kB  
  
From /etc/*release* /etc/*version*  
os-release:  
  NAME="Red Hat Enterprise Linux Server"  
  VERSION="7.3 (Maipo)"  
  ID="rhel"  
  ID_LIKE="fedora"  
  VERSION_ID="7.3"  
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"  
  ANSI_COLOR="0;31"  
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"  
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server  
  
uname -a:  
Linux localhost.localdomain 3.10.0-514.26.2.el7.x86_64 #1 SMP Tue Jul 4  
15:04:05 UTC 2017 x86_64 x86_64 x86_64 GNU/Linux  
  
run-level 3 Jul 13 19:13  
  
SPEC is set to: /home/spec  
Filesystem      Type  Size  Used Avail Use% Mounted on  
 /dev/sda3       xfs   439G   36G  403G   9% /  
Additional information from dmidecode:  
  
Warning: Use caution when you interpret this section. The 'dmidecode' program  
reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to  
hardware, firmware, and the "DMTF SMBIOS" standard.  
  
BIOS American Megatrends Inc. 1.00.14 07/10/2017  
Memory:  
 24x Micron 18ASF2G72PDZ-2G6D1 16 GB 2 rank 2666 MHz  
  
(End of data from sysinfo program)
```



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## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/spec/lib/ia32:/home/spec/lib/intel64:/home/spec/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run  
runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hammer: -D\_FILE\_OFFSET\_BITS=64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -D\_FILE\_OFFSET\_BITS=64

429.mcf: -D\_FILE\_OFFSET\_BITS=64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

464.h264ref: -D\_FILE\_OFFSET\_BITS=64

471.omnetpp: -D\_FILE\_OFFSET\_BITS=64

473.astar: -D\_FILE\_OFFSET\_BITS=64

483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -auto-ilp32 -qopt-mem-layout-trans=3

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## Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

403.gcc: -xCORE-AVX512 -ipo -O3 -no-prec-div  
-qopt-mem-layout-trans=3

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-mem-layout-trans=3

456.hmmr: -xCORE-AVX512 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32  
-qopt-mem-layout-trans=3

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll14 -auto-ilp32  
-qopt-mem-layout-trans=3

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll12 -qopt-mem-layout-trans=3

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2)  
-qopt-ra-region-strategy=block  
-qopt-mem-layout-trans=3 -Wl,-z,muldefs  
-L/sh10.2 -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/H3C-Platform-Settings-SKL-V1.1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/H3C-Platform-Settings-SKL-V1.1.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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