



# SPEC® CINT2006 Result

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## Huawei

**SPECint®2006 = 71.3**

Huawei CH121 V3 (Intel Xeon E5-2643 v4)

**SPECint\_base2006 = 68.1**

CPU2006 license: 3175

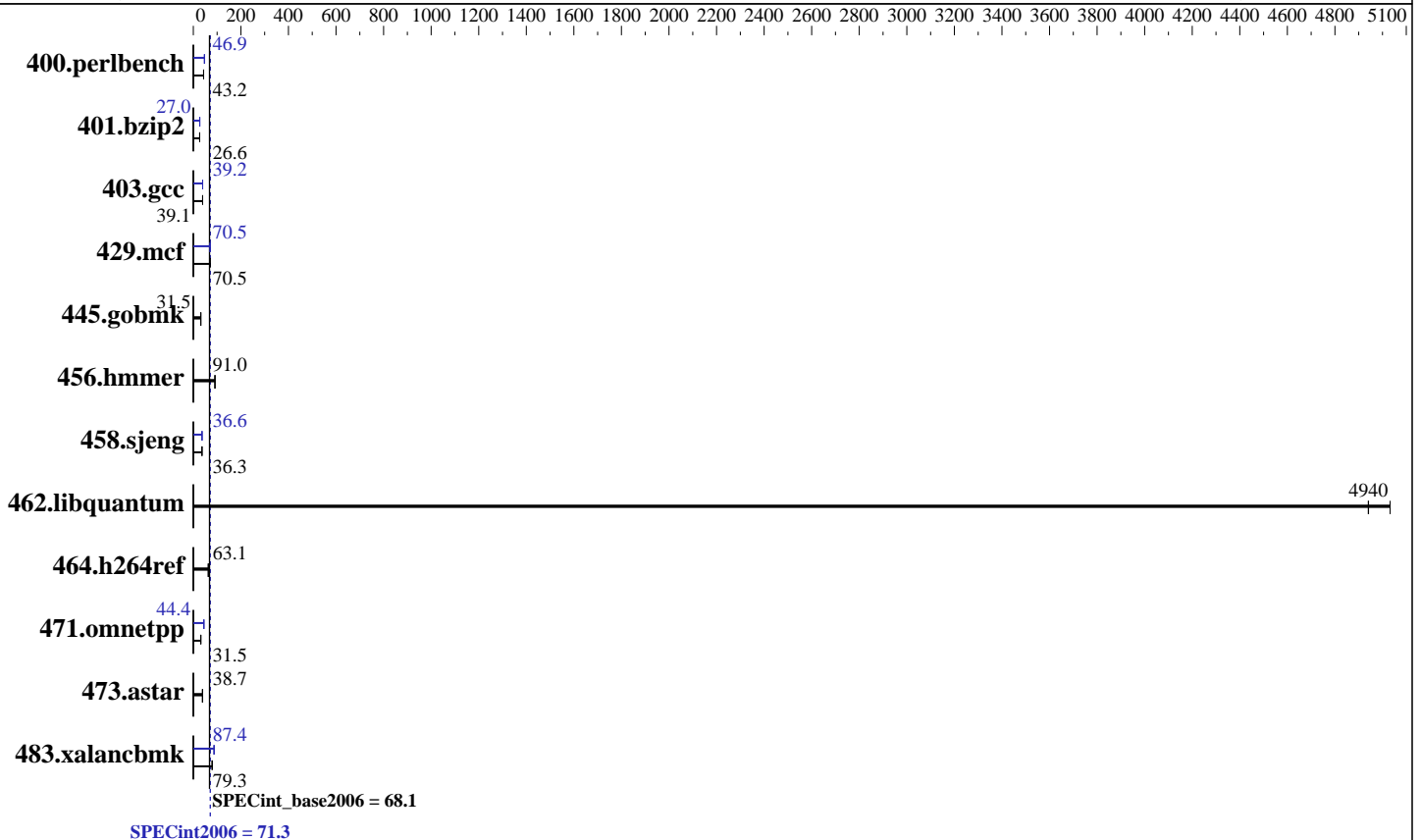
Test date: Nov-2016

Test sponsor: Huawei

Hardware Availability: Mar-2016

Tested by: Huawei

Software Availability: Nov-2015



### Hardware

CPU Name: Intel Xeon E5-2643 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)  
 Disk Subsystem: 1 x 1000 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.2 (Maipo)  
 3.10.0-327.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	225	43.4	227	43.1	<u>226</u>	<u>43.2</u>	208	47.0	<u>208</u>	<u>46.9</u>	209	46.7
401.bzip2	363	26.6	<u>363</u>	<u>26.6</u>	360	26.8	354	27.2	<u>357</u>	<u>27.0</u>	358	27.0
403.gcc	206	39.1	206	39.1	<u>206</u>	<u>39.1</u>	205	39.3	206	39.2	<u>205</u>	<u>39.2</u>
429.mcf	131	69.6	<u>129</u>	<u>70.5</u>	128	71.0	129	70.9	<u>129</u>	<u>70.5</u>	129	70.5
445.gobmk	334	31.5	<u>334</u>	<u>31.5</u>	334	31.4	334	31.5	<u>334</u>	<u>31.5</u>	334	31.4
456.hammer	102	91.1	103	91.0	<u>102</u>	<u>91.0</u>	102	91.1	103	91.0	<u>102</u>	<u>91.0</u>
458.sjeng	333	36.4	333	36.3	<u>333</u>	<u>36.3</u>	330	36.7	331	36.5	<u>331</u>	<u>36.6</u>
462.libquantum	<u>4.19</u>	<u>4940</u>	4.19	4940	4.12	5030	<u>4.19</u>	<u>4940</u>	4.19	4940	4.12	5030
464.h264ref	<u>351</u>	<u>63.1</u>	351	63.1	350	63.1	<u>351</u>	<u>63.1</u>	351	63.1	350	63.1
471.omnetpp	<u>198</u>	<u>31.5</u>	199	31.4	198	31.5	141	44.2	141	44.4	<u>141</u>	<u>44.4</u>
473.astar	182	38.6	181	38.7	<u>181</u>	<u>38.7</u>	182	38.6	181	38.7	<u>181</u>	<u>38.7</u>
483.xalancbmk	<u>87.0</u>	<u>79.3</u>	86.9	79.4	87.1	79.2	<u>78.9</u>	<u>87.4</u>	<u>78.9</u>	<u>87.4</u>	79.3	87.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Performance

Set Snoop Mode to ES mode

Set Patrol Scrub to Disable

Set Hyper-Threading to Disable

Baseboard Management Controller used to adjust the fan speed to 100%

Sysinfo program /spec16/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on localhost.localdomain Tue Nov 8 13:47:52 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2643 v4 @ 3.40GHz

2 "physical id"s (chips)

12 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The

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<b>CPU2006 license:</b> 3175	<b>Test date:</b> Nov-2016
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## Platform Notes (Continued)

following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```

cpu cores : 6
siblings  : 6
physical 0: cores 0 1 2 3 6 7
physical 1: cores 0 1 2 3 6 7
cache size : 20480 KB

```

```

From /proc/meminfo
MemTotal:      527795096 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

```

```

uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Nov 8 09:10

```

SPEC is set to: /spec16
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3       xfs   911G  154G  758G  17% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```

BIOS Insyde Corp. 3.32 09/14/2016
Memory:
8x NO DIMM NO DIMM
16x Samsung M393A4K40BB1-CRC 32 GB 2 rank 2400 MHz

```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,compact,1,0"  
LD\_LIBRARY\_PATH = "/spec16/libs/32:/spec16/libs/64:/spec16/sh"  
OMP\_NUM\_THREADS = "12"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
The Huawei CH121 V3 and Huawei CH222 V3 are electronically equivalent.  
The results have been measured on a Huawei CH121 V3 model

## Base Compiler Invocation

C benchmarks:  
icc -m64  
C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

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## Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-BDW-V1.0.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Wed Nov 30 10:46:46 2016 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 29 November 2016.