



# SPEC<sup>®</sup> CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp<sup>®</sup>\_rate2006 = 811

PowerEdge R530 (Intel Xeon E5-2650 v4, 2.20 GHz)

SPECfp\_rate\_base2006 = 793

CPU2006 license: 55

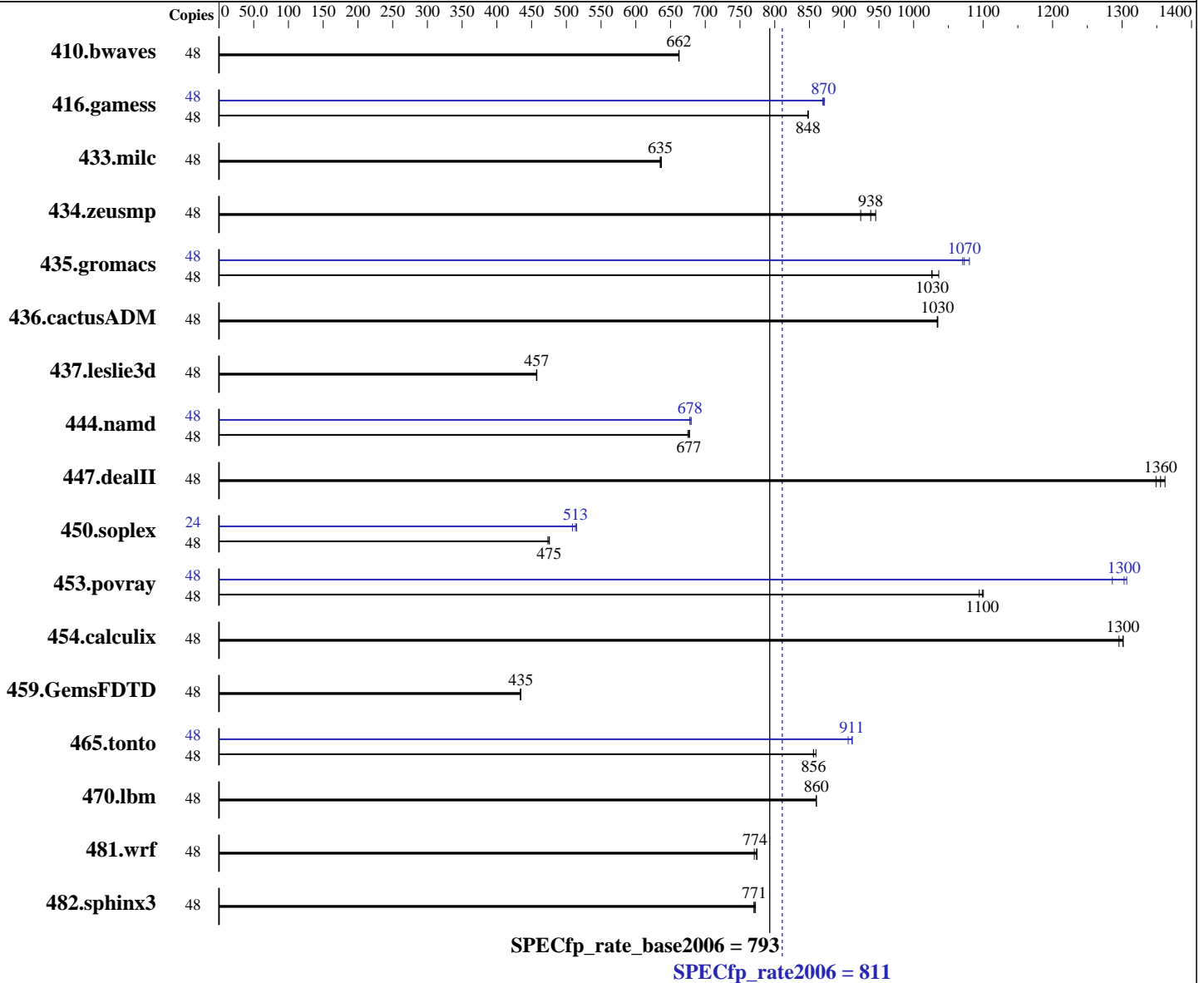
Test date: Jun-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Dec-2015



### Hardware

CPU Name: Intel Xeon E5-2650 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.2.181 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 811

PowerEdge R530 (Intel Xeon E5-2650 v4, 2.20 GHz)

SPECfp\_rate\_base2006 = 793

CPU2006 license: 55

Test date: Jun-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Dec-2015

L3 Cache: 30 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (8 x 32 GB 2Rx4 PC4-2400T-R)  
Disk Subsystem: 1 x 250 GB 7200 RPM SATA HDD  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	48	986	662	985	662	<b>985</b>	<b>662</b>	48	986	662	985	662	<b>985</b>	<b>662</b>
416.gamess	48	1109	848	1108	848	<b>1108</b>	<b>848</b>	48	1078	872	1082	869	<b>1080</b>	<b>870</b>
433.milc	48	694	635	692	637	<b>694</b>	<b>635</b>	48	694	635	692	637	<b>694</b>	<b>635</b>
434.zeusmp	48	462	945	<b>466</b>	<b>938</b>	473	924	48	462	945	<b>466</b>	<b>938</b>	473	924
435.gromacs	48	334	1030	<b>334</b>	<b>1030</b>	331	1040	48	320	1070	<b>319</b>	<b>1070</b>	317	1080
436.cactusADM	48	555	1030	<b>555</b>	<b>1030</b>	554	1030	48	555	1030	<b>555</b>	<b>1030</b>	554	1030
437.leslie3d	48	<b>986</b>	<b>457</b>	986	458	987	457	48	<b>986</b>	<b>457</b>	986	458	987	457
444.namd	48	568	677	<b>569</b>	<b>677</b>	570	675	48	568	678	566	680	<b>568</b>	<b>678</b>
447.dealII	48	407	1350	403	1360	<b>405</b>	<b>1360</b>	48	407	1350	403	1360	<b>405</b>	<b>1360</b>
450.soplex	48	<b>843</b>	<b>475</b>	846	473	842	476	24	393	509	389	515	<b>390</b>	<b>513</b>
453.povray	48	233	1090	232	1100	<b>232</b>	<b>1100</b>	48	199	1290	<b>196</b>	<b>1300</b>	195	1310
454.calculix	48	304	1300	306	1300	<b>304</b>	<b>1300</b>	48	304	1300	306	1300	<b>304</b>	<b>1300</b>
459.GemsFDTD	48	1174	434	1172	435	<b>1172</b>	<b>435</b>	48	1174	434	1172	435	<b>1172</b>	<b>435</b>
465.tonto	48	<b>552</b>	<b>856</b>	552	856	550	859	48	521	906	518	912	<b>518</b>	<b>911</b>
470.lbm	48	767	860	<b>767</b>	<b>860</b>	767	860	48	767	860	<b>767</b>	<b>860</b>	767	860
481.wrf	48	692	775	696	771	<b>693</b>	<b>774</b>	48	692	775	696	771	<b>693</b>	<b>774</b>
482.sphinx3	48	1211	772	1215	770	<b>1214</b>	<b>771</b>	48	1211	772	1215	770	<b>1214</b>	<b>771</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:  
Snoop Mode set to Cluster on Die  
Virtualization Technology disabled

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 811

PowerEdge R530 (Intel Xeon E5-2650 v4, 2.20 GHz)

SPECfp\_rate\_base2006 = 793

CPU2006 license: 55

Test date: Jun-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Dec-2015

## Platform Notes (Continued)

System Profile set to custom  
 CPU Power Management set to Hardware P States  
 C States set to Autonomous  
 C1E disabled  
 Energy Efficient Turbo disabled  
 Uncore Frequency set to Dynamic  
 Energy Efficiency Policy set to Balanced Performance  
 Memory Patrol Scrub disabled  
 Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914  
 \$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1  
 running on linux-g0aw Wed Jun 15 21:11:47 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name      : Intel(R) Xeon(R) CPU E5-2650 v4@ 2.20GHz
 2 "physical id"s (chips)
 48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 12
  siblings    : 24
  physical 0   : cores 0 1 2 3 4 5 8 9 10 11 12 13
  physical 1   : cores 0 1 2 3 4 5 8 9 10 11 12 13
cache size     : 15360 KB
```

```
From /proc/meminfo
MemTotal:      264567584 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
```

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 811

PowerEdge R530 (Intel Xeon E5-2650 v4, 2.20 GHz)

SPECfp\_rate\_base2006 = 793

CPU2006 license: 55

Test date: Jun-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Dec-2015

## Platform Notes (Continued)

CPE\_NAME="cpe:/o:suse:sles:12:sp1"

```
uname -a:
Linux linux-g0aw 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jun 15 09:26
```

```
SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext4  221G  8.7G  212G   4% /
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS Dell Inc. 2.0.1 04/11/2016
Memory:
8x 00CE00B300CE M393A4K40BB1-CRC 32 GB 2 rank 2400 MHz
4x Not Specified Not Specified
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

```
Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB
memory using RedHat EL 7.2 glibc 2.17
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 811

PowerEdge R530 (Intel Xeon E5-2650 v4, 2.20 GHz)

SPECfp\_rate\_base2006 = 793

CPU2006 license: 55

Test date: Jun-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Dec-2015

## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:

icc -m64

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 811

PowerEdge R530 (Intel Xeon E5-2650 v4, 2.20 GHz)

SPECfp\_rate\_base2006 = 793

CPU2006 license: 55

Test date: Jun-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Dec-2015

## Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deallI: -DSPEC\_CPU\_LP64  
 450.soplex: -D\_FILE\_OFFSET\_BITS=64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
 -prof-use(pass 2) -fno-alias -auto-ilp32

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 811

PowerEdge R530 (Intel Xeon E5-2650 v4, 2.20 GHz)

SPECfp\_rate\_base2006 = 793

CPU2006 license: 55

Test date: Jun-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Dec-2015

## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll4 -ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 811

PowerEdge R530 (Intel Xeon E5-2650 v4, 2.20 GHz)

SPECfp\_rate\_base2006 = 793

CPU2006 license: 55

Test date: Jun-2016

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Dec-2015

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Tue Aug 9 17:04:34 2016 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 9 August 2016.