SPEC® CINT2006 Result

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HITACHI

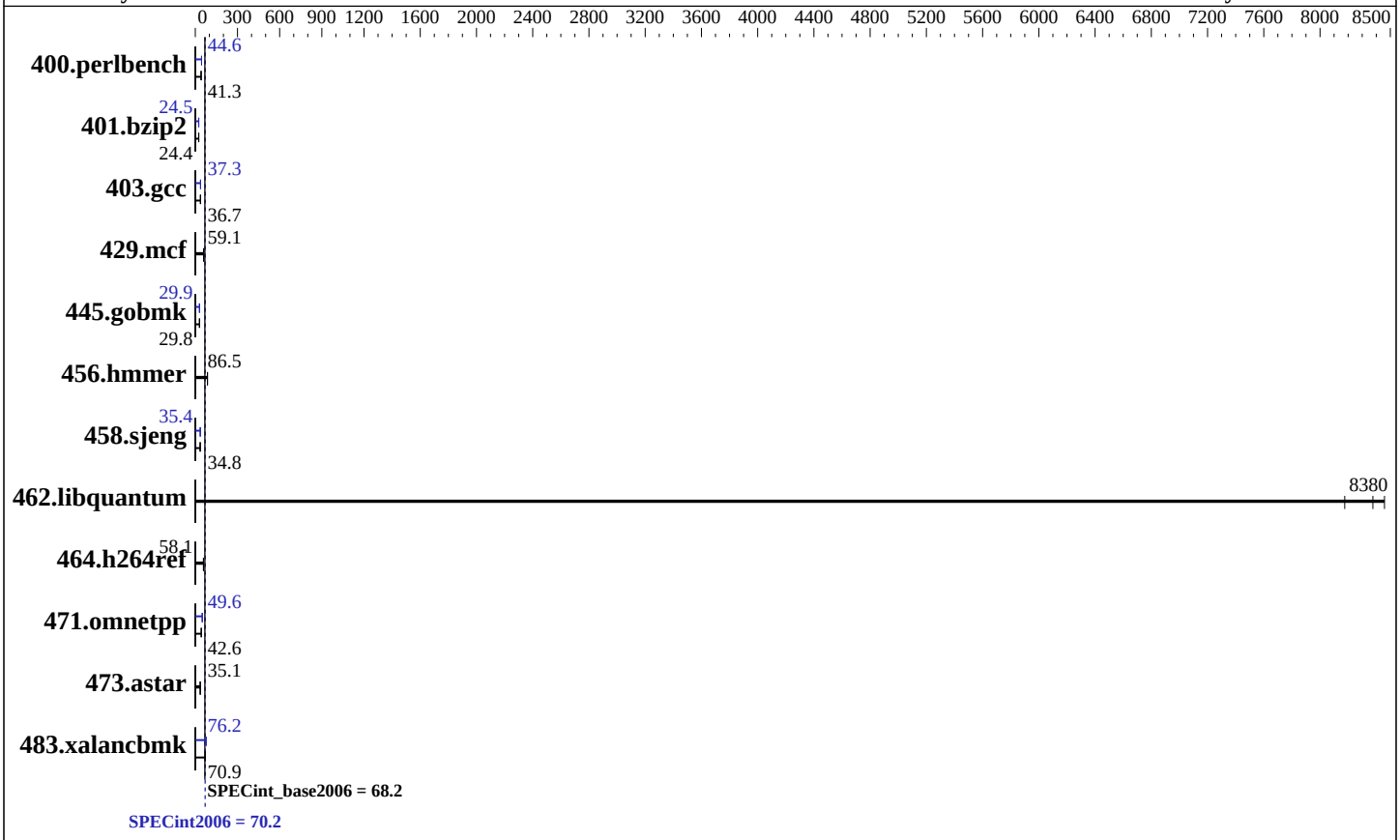
BladeSymphony BS520H (Intel Xeon E5-2697 v4)

SPECint®2006 = 70.2

SPECint_base2006 = 68.2

CPU2006 license: 35
Test sponsor: HITACHI
Tested by: HITACHI

Test date: Jun-2016
Hardware Availability: Jun-2016
Software Availability: Nov-2015



Hardware

CPU Name:	Intel Xeon E5-2697 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.60 GHz
CPU MHz:	2300
FPU:	Integrated
CPU(s) enabled:	36 cores, 2 chips, 18 cores/chip, 2 threads/core
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	45 MB I+D on chip per chip
Other Cache:	None
Memory:	512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)
Disk Subsystem:	2 x 300 GB SAS, 15000 RPM, RAID1
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 7.2 (Maipo) 3.10.0-327.el7.x86_64
Compiler:	C/C++: Version 16.0.0.0 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.2

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**237**	**41.3**	238	41.1	235	41.6	221	44.2	**219**	**44.6**	219	44.7
401.bzip2	393	24.6	**395**	**24.4**	396	24.4	396	24.3	**394**	**24.5**	393	24.5
403.gcc	220	36.7	219	36.8	**219**	**36.7**	**216**	**37.3**	215	37.4	216	37.2
429.mcf	151	60.4	**154**	**59.1**	156	58.5	151	60.4	**154**	**59.1**	156	58.5
445.gobmk	351	29.9	**352**	**29.8**	353	29.7	352	29.8	349	30.0	**351**	**29.9**
456.hmmer	**108**	**86.5**	108	86.0	107	86.9	**108**	**86.5**	108	86.0	107	86.9
458.sjeng	348	34.8	347	34.9	**348**	**34.8**	341	35.4	**342**	**35.4**	342	35.3
462.libquantum	2.53	8180	**2.47**	**8380**	2.45	8460	2.53	8180	**2.47**	**8380**	2.45	8460
464.h264ref	380	58.2	381	58.0	**381**	**58.1**	380	58.2	381	58.0	**381**	**58.1**
471.omnetpp	147	42.6	**147**	**42.6**	147	42.5	126	49.8	127	49.4	**126**	**49.6**
473.astar	200	35.1	199	35.3	**200**	**35.1**	200	35.1	199	35.3	**200**	**35.1**
483.xalancbmk	97.3	70.9	**97.4**	**70.9**	97.6	70.7	90.0	76.7	**90.6**	**76.2**	91.0	75.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
```

Platform Notes

```
BIOS configuration:
 Patrol Scrub = Disable
 Per Core P-state = Disable

 Sysinfo program /home/cpu2006/config/sysinfo.rev6914
 $Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
 running on rhel722 Wed Jun  8 06:46:34 2016

 This section contains SUT (System Under Test) info as seen by
 some common utilities.  To remove or add to this section, see:
   http://www.spec.org/cpu2006/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2697 v4 @ 2.30GHz
       2 "physical id"s (chips)
       72 "processors"
    cores, siblings (Caution: counting these is hw and system dependent.  The
    following excerpts from /proc/cpuinfo might not be reliable.  Use with
    caution.)
```

Continued on next page

Platform Notes (Continued)

```
        cpu cores : 18
        siblings  : 36
        physical 0: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
        physical 1: cores 0 1 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
     cache size : 46080 KB

  From /proc/meminfo
     MemTotal:       527317492 kB
     HugePages_Total:       0
     Hugepagesize:       2048 kB

  From /etc/*release* /etc/*version*
     os-release:
        NAME="Red Hat Enterprise Linux Server"
        VERSION="7.2 (Maipo)"
        ID="rhel"
        ID_LIKE="fedora"
        VERSION_ID="7.2"
        PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
        ANSI_COLOR="0;31"
        CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
     redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
     system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
     system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

  uname -a:
     Linux rhel722 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
     x86_64 x86_64 x86_64 GNU/Linux

  run-level 3 Jun 8 06:42

  SPEC is set to: /home/cpu2006
     Filesystem            Type  Size  Used Avail Use% Mounted on
     /dev/mapper/rhel-home xfs   225G   23G  202G  11% /home
  Additional information from dmidecode:

     Warning: Use caution when you interpret this section. The 'dmidecode' program
     reads system data which is "intended to allow hardware to be accurately
     determined", but the intent may not be met, as there are frequent changes to
     hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS HITACHI 10-00 01/29/2016
    Memory:
     8x NO DIMM Unknown
     16x Samsung M393A4K40BB1-CRC 32 GB 2 rank 2400 MHz

  (End of data from sysinfo program)
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"
OMP_NUM_THREADS = "36"

 Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB
 memory using RedHat EL 7.1
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
 BladeSymphony BS520H, Hitachi Compute Blade 520H and BladeSymphony BS2500 are electronically equivalent.
 The results have been measured on a Hitachi Compute Blade 520H.
```

Base Compiler Invocation

C benchmarks:
```
icc  -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
  401.bzip2: -DSPEC_CPU_LP64
    403.gcc: -DSPEC_CPU_LP64
    429.mcf: -DSPEC_CPU_LP64
  445.gobmk: -DSPEC_CPU_LP64
  456.hmmer: -DSPEC_CPU_LP64
  458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
  464.h264ref: -DSPEC_CPU_LP64
  471.omnetpp: -DSPEC_CPU_LP64
    473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
```

C++ benchmarks:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc -m64`

400.perlbench: `icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

445.gobmk: `icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

C++ benchmarks (except as noted below):
`icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin`

473.astar: `icpc -m64`

Peak Portability Flags

400.perlbench: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
403.gcc: `-DSPEC_CPU_LP64`
429.mcf: `-DSPEC_CPU_LP64`
445.gobmk: `-D_FILE_OFFSET_BITS=64`
456.hmmer: `-DSPEC_CPU_LP64`
458.sjeng: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`
464.h264ref: `-DSPEC_CPU_LP64`
471.omnetpp: `-D_FILE_OFFSET_BITS=64`
473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xCORE-AVX2`(pass 2) `-prof-gen:threadsafe`(pass 1)
`-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`(pass 2)
`-par-num-threads=1`(pass 1) `-prof-use`(pass 2) `-opt-prefetch`
`-ansi-alias`

401.bzip2: `-xCORE-AVX2`(pass 2) `-prof-gen:threadsafe`(pass 1)
`-ipo`(pass 2) `-O3`(pass 2) `-no-prec-div`
`-par-num-threads=1`(pass 1) `-prof-use`(pass 2) `-auto-ilp32`

Continued on next page

Peak Optimization Flags (Continued)

401.bzip2 (continued):
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html
http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.6.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml
http://www.spec.org/cpu2006/flags/PlatformHitachi-V1.6.xml

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