



# SPEC® CINT2006 Result

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Hewlett Packard Enterprise  
(Test Sponsor: HPE)

ProLiant BL460c Gen9  
(2.10 GHz, Intel Xeon E5-2620 v4)

**SPECint\_rate2006 = 665**

**SPECint\_rate\_base2006 = 632**

CPU2006 license: 3

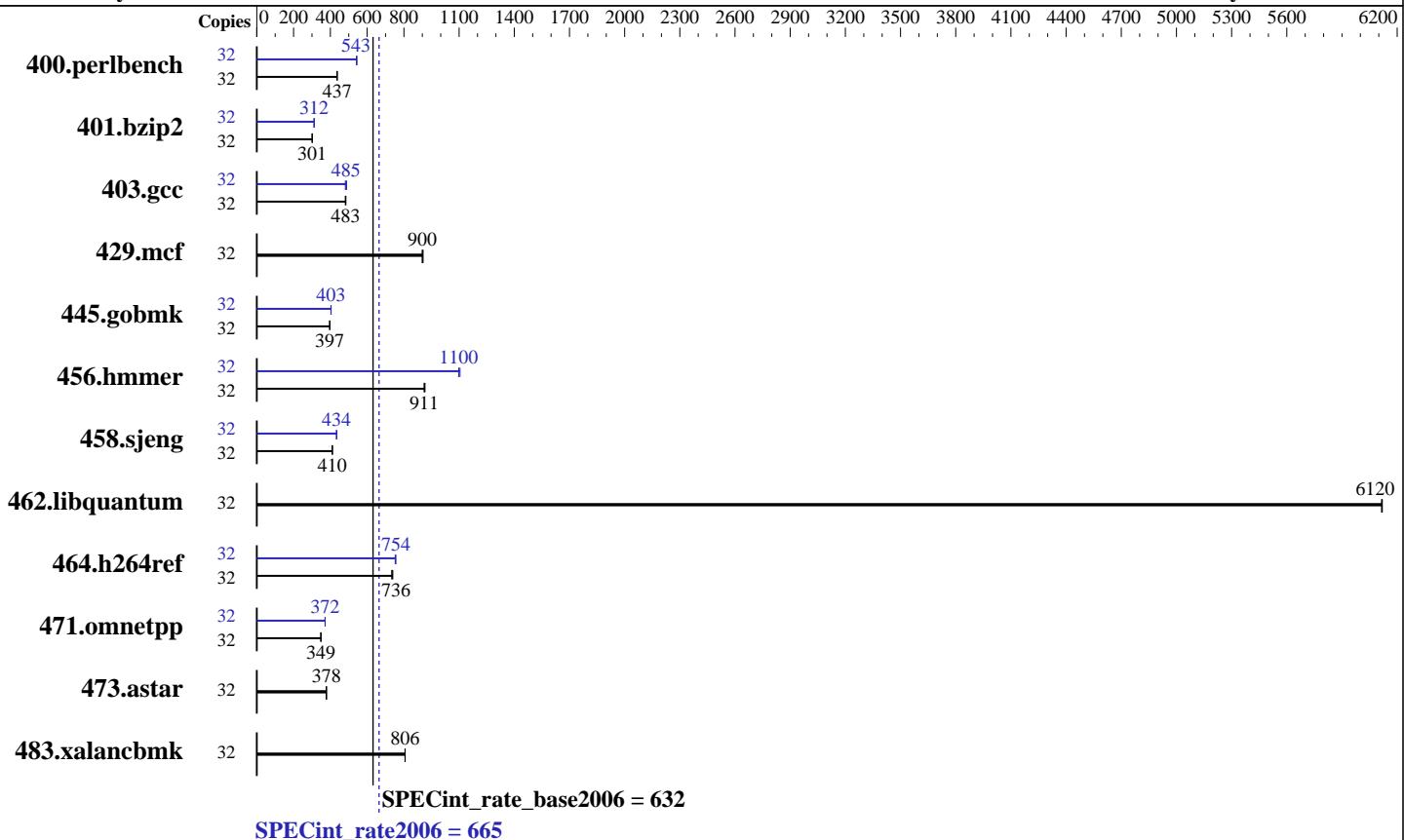
Test sponsor: HPE

Tested by: HPE

Test date: Mar-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015



## Hardware

CPU Name: Intel Xeon E5-2620 v4  
CPU Characteristics: Intel Turbo Boost Technology up to 3.00 GHz  
CPU MHz: 2100  
FPU: Integrated  
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
CPU(s) orderable: 1,2 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core  
L3 Cache: 20 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (8 x 32 GB 2Rx4 PC4-2400T-R, running at 2133 MHz)  
Disk Subsystem: 2 x 500 GB SAS HDD 10 K, RAID 1  
Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 12 (x86\_64) SP1, Kernel 3.12.49-11-default  
Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
Auto Parallel: No  
File System: xfs  
System State: Run level 5 (multi-user, w/GUI)  
Base Pointers: 32-bit  
Peak Pointers: 32/64-bit  
Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	717	436	<b>716</b>	<b>437</b>	715	437	32	577	542	<b>576</b>	<b>543</b>	575	544
401.bzip2	32	<b>1025</b>	<b>301</b>	1024	302	1026	301	32	991	312	<b>990</b>	<b>312</b>	988	312
403.gcc	32	535	481	532	485	<b>533</b>	<b>483</b>	32	535	481	528	488	<b>531</b>	<b>485</b>
429.mcf	32	<b>324</b>	<b>900</b>	323	904	324	900	32	<b>324</b>	<b>900</b>	323	904	324	900
445.gobmk	32	847	396	<b>846</b>	<b>397</b>	846	397	32	831	404	833	403	<b>832</b>	<b>403</b>
456.hammer	32	326	915	<b>328</b>	<b>911</b>	328	910	32	<b>272</b>	<b>1100</b>	270	1100	272	1100
458.sjeng	32	<b>943</b>	<b>410</b>	942	411	944	410	32	<b>892</b>	<b>434</b>	892	434	893	433
462.libquantum	32	<b>108</b>	<b>6120</b>	108	6120	108	6120	32	<b>108</b>	<b>6120</b>	108	6120	108	6120
464.h264ref	32	<b>962</b>	<b>736</b>	958	740	965	734	32	941	753	937	756	<b>939</b>	<b>754</b>
471.omnetpp	32	574	348	573	349	<b>573</b>	<b>349</b>	32	539	371	538	372	<b>538</b>	<b>372</b>
473.astar	32	<b>594</b>	<b>378</b>	593	379	594	378	32	<b>594</b>	<b>378</b>	593	379	594	378
483.xalancbmk	32	<b>274</b>	<b>806</b>	274	806	274	807	32	<b>274</b>	<b>806</b>	274	806	274	807

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Platform Notes

BIOS Configuration:  
Intel Hyperthreading Option set to Enabled  
Power Profile set to Custom  
Power Regulator set to Static High Performance Mode  
Minimum Processor Idle Power Core C-State set to C6 State  
Minimum Processor Idle Power Package C-State set to No Package State  
Collaborative Power Control set to Disabled  
QPI Snoop Configuration set to Home Snoop  
Thermal Configuration set to Maximum Cooling  
Processor Power and Utilization Monitoring set to Disabled  
Memory Refresh Rate set to 1x Refresh

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## Platform Notes (Continued)

```
Sysinfo program /home/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$
running on bl460c2-gen9-b Thu May 26 22:41:58 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-2620 v4 @ 2.10GHz
        2 "physical id"s (chips)
        32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 8
    siblings   : 16
    physical 0: cores 0 1 2 3 4 5 6 7
    physical 1: cores 0 1 2 3 4 5 6 7
    cache size : 20480 KB
```

```
From /proc/meminfo
    MemTotal:      264328212 kB
    HugePages_Total:       0
    Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 1
    # This file is deprecated and will be removed in a future service pack or
    release.
    # Please check /etc/os-release for details about this release.
os-release:
    NAME="SLES"
    VERSION="12-SP1"
    VERSION_ID="12.1"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
Linux bl460c2-gen9-b 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 5 May 26 22:03
```

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## Platform Notes (Continued)

SPEC is set to: /home/cpu2006

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda4	xfs	424G	171G	253G	41%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP I36 04/12/2016

Memory:

8x UNKNOWN NOT AVAILABLE

8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have one line reading as:  
8x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz, configured at 2133 MHz

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Base Portability Flags

```
400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hammer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```

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## Base Portability Flags (Continued)

464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

```

403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hammer: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
458.sjeng: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LP64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
               -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
               -par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
            -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -prof-use(pass 2) -par-num-threads=1(pass 1) -ansi-alias
            -opt-mem-layout-trans=3

456.hammer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
            -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
            -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
            -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
               -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
               -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
               -ansi-alias

```

C++ benchmarks:

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## Peak Optimization Flags (Continued)

```
471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -prof-use(pass 2) -ansi-alias
             -opt-ra-region-strategy=block -Wl,-z,muldefs
             -L/sh -lsmartheap
```

```
473.astar: basepeak = yes
```

```
483.xalancbmk: basepeak = yes
```

## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=__alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>  
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>  
<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>

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