



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]2006 = 93.3

PowerEdge R630 (Intel Xeon E5-2650L v4, 1.70 GHz)

SPECfp_base2006 = 87.9

CPU2006 license: 55

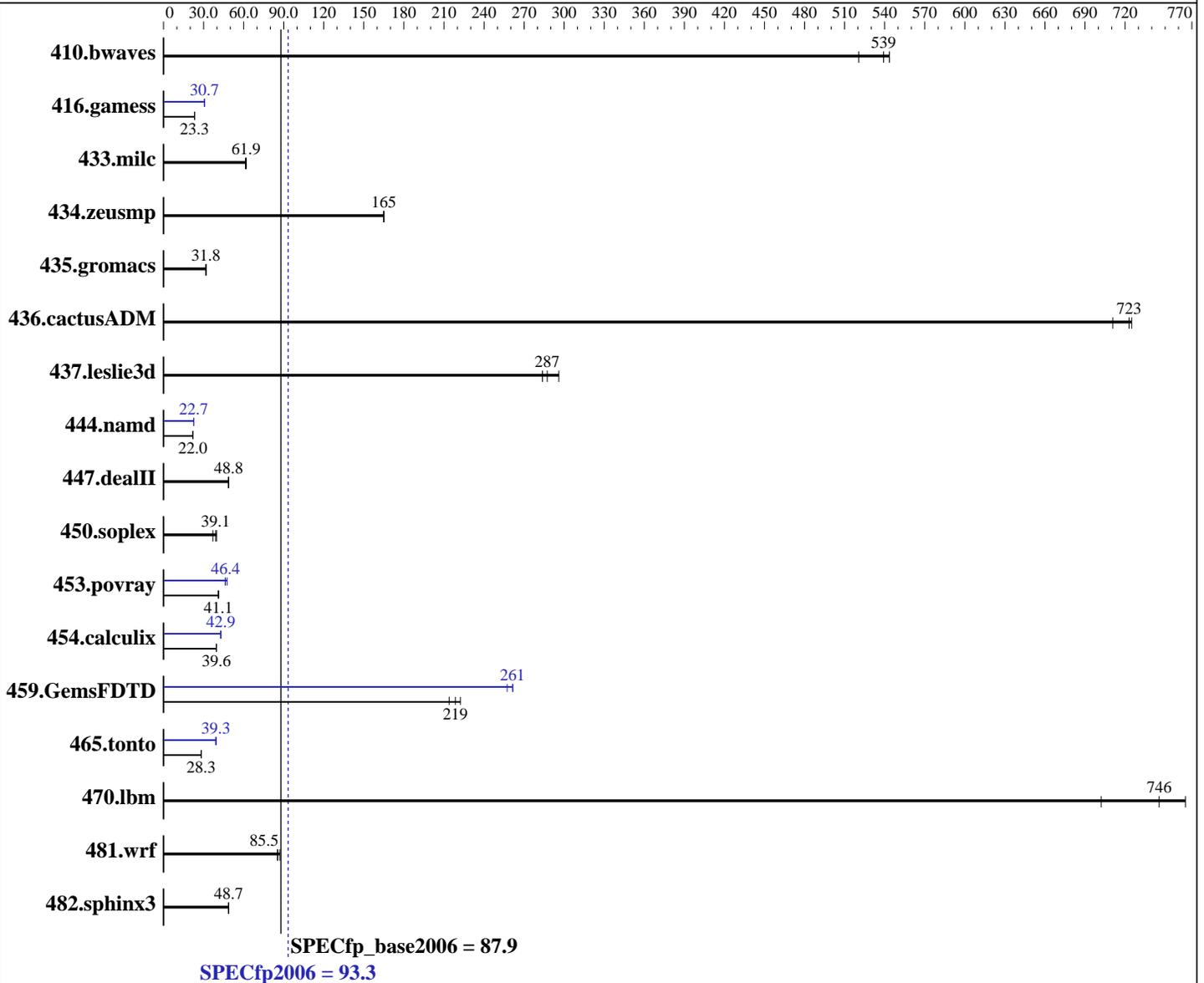
Test date: Mar-2016

Test sponsor: Dell Inc.

Hardware Availability: Mar-2016

Tested by: Dell Inc.

Software Availability: Mar-2016



Hardware	
CPU Name:	Intel Xeon E5-2650L v4
CPU Characteristics:	Intel Turbo Boost Technology up to 2.50 GHz
CPU MHz:	1700
FPU:	Integrated
CPU(s) enabled:	28 cores, 2 chips, 14 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

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Software	
Operating System:	Red Hat Enterprise Linux Server release 7.2 (Maipo)
Compiler:	3.10.0-327.el7.x86_64 C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	xf

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L3 Cache: 35 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)
 Disk Subsystem: 1 x 500 GB 7200 RPM SATA
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
410.bwaves	26.1	521	<u>25.2</u>	<u>539</u>	25.0	544	26.1	521	<u>25.2</u>	<u>539</u>	25.0	544
416.gamess	837	23.4	<u>840</u>	<u>23.3</u>	842	23.3	636	30.8	<u>637</u>	<u>30.7</u>	638	30.7
433.milc	148	62.0	<u>148</u>	<u>61.9</u>	150	61.3	148	62.0	<u>148</u>	<u>61.9</u>	150	61.3
434.zeusmp	55.3	165	55.1	165	<u>55.1</u>	<u>165</u>	55.3	165	55.1	165	<u>55.1</u>	<u>165</u>
435.gromacs	224	31.9	<u>224</u>	<u>31.8</u>	224	31.8	224	31.9	<u>224</u>	<u>31.8</u>	224	31.8
436.cactusADM	<u>16.5</u>	<u>723</u>	16.8	711	16.5	725	<u>16.5</u>	<u>723</u>	16.8	711	16.5	725
437.leslie3d	33.1	284	<u>32.7</u>	<u>287</u>	31.8	296	33.1	284	<u>32.7</u>	<u>287</u>	31.8	296
444.namd	<u>364</u>	<u>22.0</u>	364	22.0	364	22.0	353	22.7	354	22.7	<u>354</u>	<u>22.7</u>
447.dealII	<u>235</u>	<u>48.8</u>	234	48.9	236	48.6	<u>235</u>	<u>48.8</u>	234	48.9	236	48.6
450.soplex	<u>213</u>	<u>39.1</u>	210	39.8	226	36.9	<u>213</u>	<u>39.1</u>	210	39.8	226	36.9
453.povray	129	41.1	129	41.2	<u>129</u>	<u>41.1</u>	115	46.2	112	47.7	<u>115</u>	<u>46.4</u>
454.calculix	<u>208</u>	<u>39.6</u>	208	39.7	209	39.5	<u>192</u>	<u>42.9</u>	193	42.8	192	42.9
459.GemsFDTD	47.7	222	49.6	214	<u>48.5</u>	<u>219</u>	41.2	257	40.6	262	<u>40.6</u>	<u>261</u>
465.tonto	349	28.2	348	28.3	<u>348</u>	<u>28.3</u>	250	39.3	<u>250</u>	<u>39.3</u>	250	39.3
470.lbm	<u>18.4</u>	<u>746</u>	19.6	702	18.0	765	<u>18.4</u>	<u>746</u>	19.6	702	18.0	765
481.wrf	131	85.3	128	87.2	<u>131</u>	<u>85.5</u>	131	85.3	128	87.2	<u>131</u>	<u>85.5</u>
482.sphinx3	399	48.8	<u>400</u>	<u>48.7</u>	400	48.7	399	48.8	<u>400</u>	<u>48.7</u>	400	48.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
 Snoop Mode set to Opportunistic Snoop Broadcast
 Virtualization Technology disabled
 System Profile set to Custom
 CPU Power Management set to Maximum Performance
 Energy Efficient Turbo disabled
 Memory Patrol Scrub disabled
 Cstates autonomous/C1E enabled
 Energy Efficient Policy set to Performance
 Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914

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Platform Notes (Continued)

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on localhost.localdomain Fri Mar 18 10:03:59 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2650L v4@ 1.70GHz
 2 "physical id"s (chips)
 56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 14
  siblings  : 28
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 35840 KB
```

```
From /proc/meminfo
MemTotal:      264040556 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.2 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.2"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server
```

```
uname -a:
Linux localhost.localdomain 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29
EDT 2015 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Mar 18 03:18
```

```
SPEC is set to: /root/cpu2006-1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       xfs   256G  12G  245G   5% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
Continued on next page



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Platform Notes (Continued)

determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 2.0.1 02/12/2016

Memory:

16x 002C0632002C 18ASF2G72PDZ-2G3B1 16 GB 2 rank 2400 MHz

8x Not Specified Not Specified

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact,1,0"

LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

OMP_NUM_THREADS = "28"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64

416.gamess: -DSPEC_CPU_LP64

433.milc: -DSPEC_CPU_LP64

434.zeusmp: -DSPEC_CPU_LP64

435.gromacs: -DSPEC_CPU_LP64 -nofor_main

436.cactusADM: -DSPEC_CPU_LP64 -nofor_main

437.leslie3d: -DSPEC_CPU_LP64

444.namd: -DSPEC_CPU_LP64

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Base Portability Flags (Continued)

```

447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

Base Optimization Flags

```

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch
-ansi-alias

```

Peak Compiler Invocation

```

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

```

Peak Portability Flags

Same as Base Portability Flags



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Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

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Peak Optimization Flags (Continued)

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml>

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Tested with SPEC CPU2006 v1.2.
Report generated on Tue Apr 5 14:55:50 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 5 April 2016.