



# SPEC<sup>®</sup> CFP2006 Result

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## Hypertechnologies Ciara, Inc Orion HF320-G3

SPECfp<sup>®</sup>\_rate2006 = 412

SPECfp\_rate\_base2006 = 400

CPU2006 license: 4531

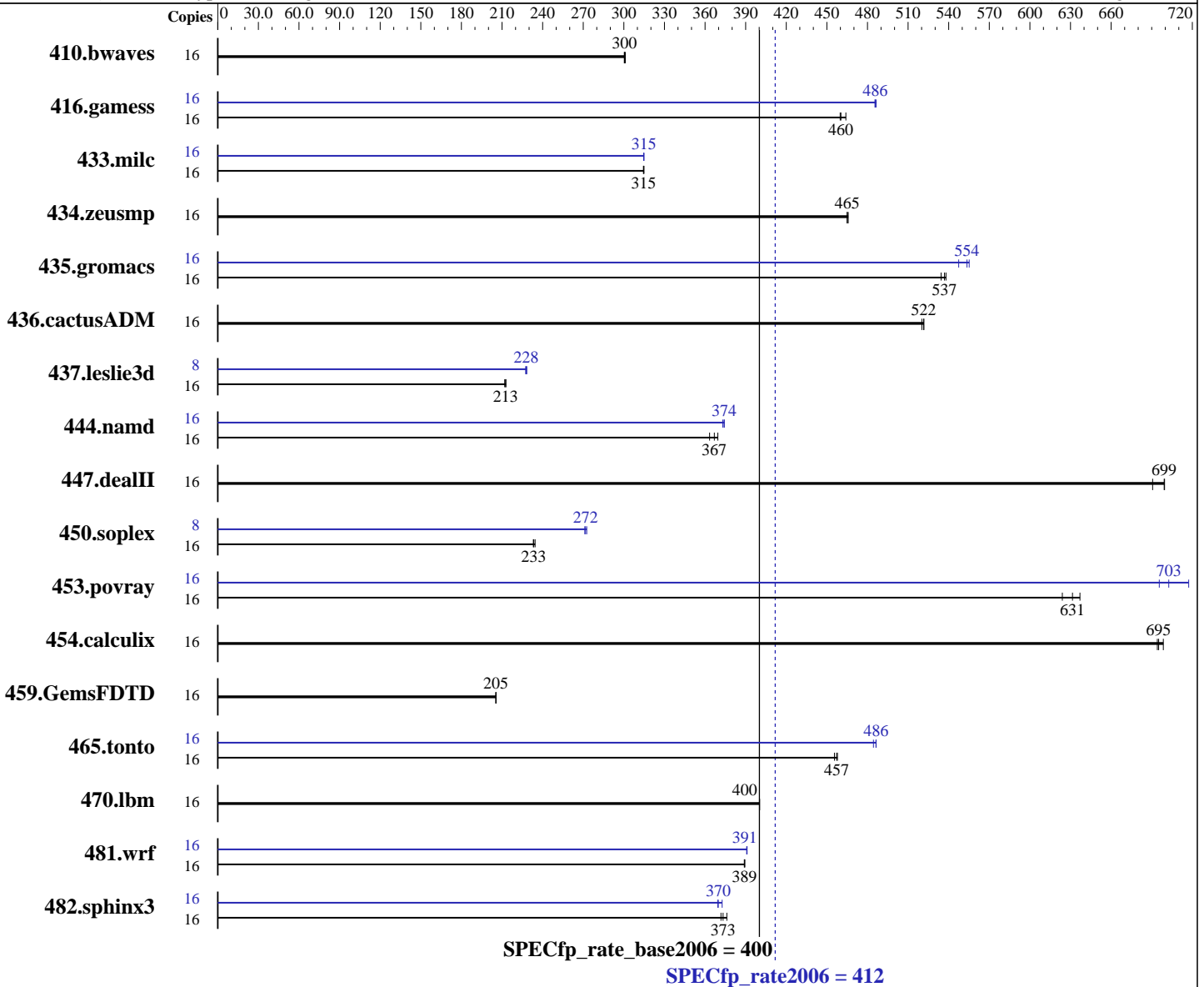
Test sponsor: Hypertechnologies Ciara, Inc

Tested by: Hypertechnologies Ciara, Inc

Test date: Apr-2015

Hardware Availability: Feb-2015

Software Availability: Aug-2015



### Hardware

CPU Name: Intel Core i7-5960X  
 CPU Characteristics: Intel Turbo Boost Technology disabled  
 CPU MHz: 4300  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 1 chip, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 3.10.0-123.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;  
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs

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L3 Cache: 20 MB I+D on chip per chip  
Other Cache: None  
Memory: 64 GB (8 x 8 GB 2Rx8 PC4-2666P-U)  
Disk Subsystem: 2x 240 GB SATA SSD, RAID 0  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	722	301	724	300	<b>724</b>	<b>300</b>	16	722	301	724	300	<b>724</b>	<b>300</b>
416.gamess	16	675	464	<b>680</b>	<b>460</b>	681	460	16	644	486	<b>645</b>	<b>486</b>	645	486
433.milc	16	466	315	<b>467</b>	<b>315</b>	467	315	16	<b>467</b>	<b>315</b>	467	315	467	315
434.zeusmp	16	313	466	<b>313</b>	<b>465</b>	313	465	16	313	466	<b>313</b>	<b>465</b>	313	465
435.gromacs	16	<b>213</b>	<b>537</b>	214	534	212	538	16	<b>206</b>	<b>554</b>	209	547	206	555
436.cactusADM	16	<b>367</b>	<b>522</b>	367	522	368	520	16	<b>367</b>	<b>522</b>	367	522	368	520
437.leslie3d	16	<b>707</b>	<b>213</b>	706	213	710	212	8	329	228	<b>330</b>	<b>228</b>	331	227
444.namd	16	353	363	347	369	<b>350</b>	<b>367</b>	16	<b>343</b>	<b>374</b>	343	374	344	373
447.dealII	16	265	691	<b>262</b>	<b>699</b>	262	699	16	265	691	<b>262</b>	<b>699</b>	262	699
450.soplex	16	569	234	<b>572</b>	<b>233</b>	572	233	8	245	273	246	271	<b>246</b>	<b>272</b>
453.povray	16	<b>135</b>	<b>631</b>	134	637	136	624	16	122	696	119	717	<b>121</b>	<b>703</b>
454.calculix	16	<b>190</b>	<b>695</b>	190	694	189	699	16	<b>190</b>	<b>695</b>	190	694	189	699
459.GemsFDTD	16	<b>826</b>	<b>205</b>	826	205	826	206	16	<b>826</b>	<b>205</b>	826	205	826	206
465.tonto	16	344	458	346	456	<b>344</b>	<b>457</b>	16	324	486	325	484	<b>324</b>	<b>486</b>
470.lbm	16	549	400	549	400	<b>549</b>	<b>400</b>	16	549	400	549	400	<b>549</b>	<b>400</b>
481.wrf	16	<b>459</b>	<b>389</b>	459	389	459	389	16	457	391	<b>457</b>	<b>391</b>	457	391
482.sphinx3	16	<b>835</b>	<b>373</b>	829	376	839	372	16	837	373	<b>843</b>	<b>370</b>	844	369

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /usr/cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on localhost.localdomain Tue Apr 7 08:34:59 2015

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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Core(TM) i7-5960X CPU @ 3.00GHz
 1 "physical id"s (chips)
 16 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 8
siblings   : 16
physical 0: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

From /proc/meminfo

```
MemTotal:      65710396 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server
```

uname -a:

```
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014 x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Apr 7 08:34 last=5

SPEC is set to: /usr/cpu2006

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-root xfs  434G  141G  294G  33% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

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## Platform Notes (Continued)

BIOS American Megatrends Inc. 8804 01/27/2015

Memory:

8x Undefined CMD32GX4M4A2666C15 8 GB 2 rank 2133 MHz, configured at 2666 MHz

(End of data from sysinfo program)

## General Notes

Processor accelerated to 4.30 GHz

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB

memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

```

435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Peak Optimization Flags

C benchmarks:

```

433.milc: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
         -auto-ilp32

```

```

470.lbm: basepeak = yes

```

```

482.sphinx3: -xCORE-AVX2 -prof-gen(pass 1) -ipo -O3 -no-prec-div
            -prof-use(pass 2) -unroll2

```

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
         -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
         -fno-alias -auto-ilp32

```

```

447.dealII: basepeak = yes

```

```

450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
           -opt-malloc-options=3

```

```

453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
           -ansi-alias

```

Fortran benchmarks:

```

410.bwaves: basepeak = yes

```

```

416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
           -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
           -inline-level=0 -scalar-rep-

```

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## Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

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Tested with SPEC CPU2006 v1.2.

Report generated on Wed Jun 17 10:47:41 2015 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 16 June 2015.