# SPEC® CINT2006 Result
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## Fujitsu

PRIMERGY TX1330 M1, Intel Xeon E3-1271 v3, 3.60 GHz

$SPECint^{®}\_rate2006 =$ 223

$SPECint\_rate\_base2006 =$ 216

| | | | |
|---|---|---|---|
| **CPU2006 license:** | 19 | **Test date:** | Jul-2014 |
| **Test sponsor:** | Fujitsu | **Hardware Availability:** | Jul-2014 |
| **Tested by:** | Fujitsu | **Software Availability:** | Nov-2013 |



**Copies**

- 400.perlbench — 8 / 218 — 8 / 180
- 401.bzip2 — 8 / 112 — 8 / 108
- 403.gcc — 8 / 166
- 429.mcf — 8 / 258
- 445.gobmk — 8 / 156 — 8 / 153
- 456.hmmer — 8 / 315
- 458.sjeng — 8 / 172 — 8 / 166
- 462.libquantum — 8 / 1910
- 464.h264ref — 8 / 299 — 8 / 274
- 471.omnetpp — 8 / 111 — 8 / 110
- 473.astar — 8 / 116
- 483.xalancbmk — 8 / 231

SPECint_rate_base2006 = 216

SPECint_rate2006 = 223

## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon E3-1271 v3 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 4.00 GHz |
| CPU MHz: | 3600 |
| FPU: | Integrated |
| CPU(s) enabled: | 4 cores, 1 chip, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1 chip |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 256 KB I+D on chip per core |
| L3 Cache: | 8 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 32 GB (4 x 8 GB 2Rx8 PC3L-12800E-11, ECC) |
| Disk Subsystem: | 1 x SATA, 500 GB, 7200 RPM |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | Red Hat Enterprise Linux Server release 6.5 (Santiago) 2.6.32-431.el6.x86_64 |
| Compiler: | C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux |
| Auto Parallel: | No |
| File System: | ext4 |
| System State: | Run level 5 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | 32/64-bit |
| Other Software: | Microquill SmartHeap V10.0 |

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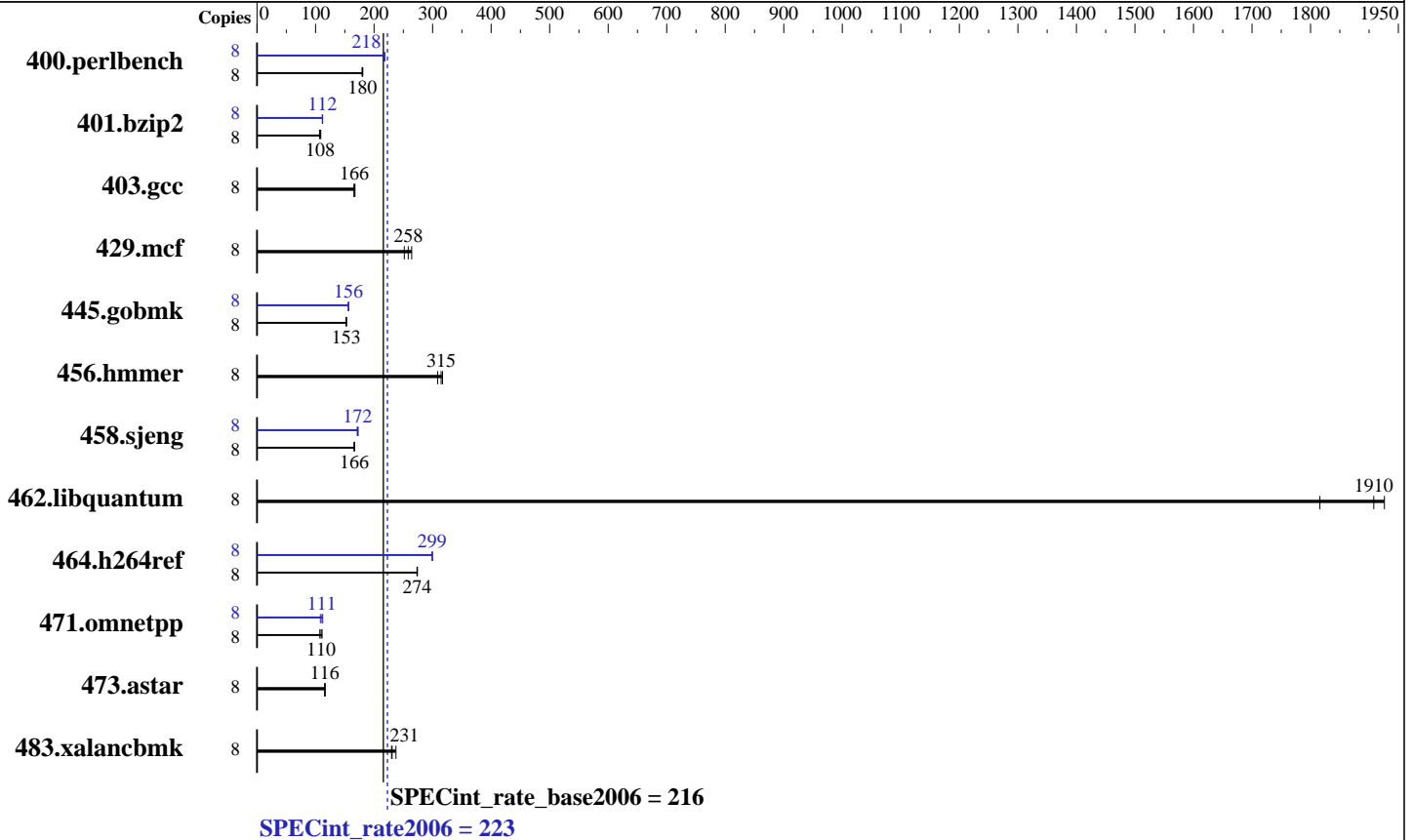
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## Results Table

| Benchmark | Base Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Peak Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| 400.perlbench | 8 | **434** | **180** | 432 | 181 | 436 | 179 | 8 | 361 | 217 | 358 | 218 | **359** | **218** |
| 401.bzip2 | 8 | **715** | **108** | 723 | 107 | 709 | 109 | 8 | **690** | **112** | 692 | 112 | 689 | 112 |
| 403.gcc | 8 | **387** | **166** | 385 | 167 | 389 | 166 | 8 | **387** | **166** | 385 | 167 | 389 | 166 |
| 429.mcf | 8 | 290 | 252 | 276 | 264 | **282** | **258** | 8 | 290 | 252 | 276 | 264 | **282** | **258** |
| 445.gobmk | 8 | 549 | 153 | **550** | **153** | 551 | 152 | 8 | 536 | 157 | **538** | **156** | 538 | 156 |
| 456.hmmer | 8 | 235 | 317 | 242 | 309 | **237** | **315** | 8 | 235 | 317 | 242 | 309 | **237** | **315** |
| 458.sjeng | 8 | **582** | **166** | 583 | 166 | 581 | 167 | 8 | 566 | 171 | 560 | 173 | **563** | **172** |
| 462.libquantum | 8 | **86.9** | **1910** | 91.3 | 1820 | 86.1 | 1930 | 8 | **86.9** | **1910** | 91.3 | 1820 | 86.1 | 1930 |
| 464.h264ref | 8 | **646** | **274** | 646 | 274 | 648 | 273 | 8 | 591 | 299 | **591** | **299** | 591 | 300 |
| 471.omnetpp | 8 | **457** | **110** | 450 | 111 | 466 | 107 | 8 | 445 | 112 | **451** | **111** | 462 | 108 |
| 473.astar | 8 | **484** | **116** | 482 | 116 | 486 | 116 | 8 | **484** | **116** | 482 | 116 | 486 | 116 |
| 483.xalancbmk | 8 | 233 | 237 | **239** | **231** | 240 | 230 | 8 | 233 | 237 | **239** | **231** | 240 | 230 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64:/SPECcpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RedHat EL 6.4
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1>      /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
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## Base Compiler Invocation

C benchmarks:
    icc  -m32

C++ benchmarks:
    icpc -m32

## Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

## Base Optimization Flags

C benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
    -opt-mem-layout-trans=3

C++ benchmarks:
    -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
    -opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

    403.gcc: -Dalloca=_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):
    icc  -m32

    400.perlbench: icc -m64

    401.bzip2: icc -m64

    458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

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# Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
    401.bzip2: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

# Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -auto-ilp32

    401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -opt-prefetch -auto-ilp32 -ansi-alias

      403.gcc: basepeak = yes

      429.mcf: basepeak = yes

    445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
               -ansi-alias -opt-mem-layout-trans=3

    456.hmmer: basepeak = yes

    458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

   464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -unroll2 -ansi-alias
```

C++ benchmarks:

```
   471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
               -L/sh -lsmartheap

     473.astar: basepeak = yes

 483.xalancbmk: basepeak = yes
```

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# Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64-revB.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.xml

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For other inquiries, please contact webmaster@spec.org.

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