



# SPEC<sup>®</sup> CFP2006 Result

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## Fujitsu

PRIMERGY RX1330 M1, Intel Xeon E3-1220 v3, 3.10 GHz

SPECfp<sup>®</sup>\_rate2006 = 139

SPECfp\_rate\_base2006 = 135

CPU2006 license: 19

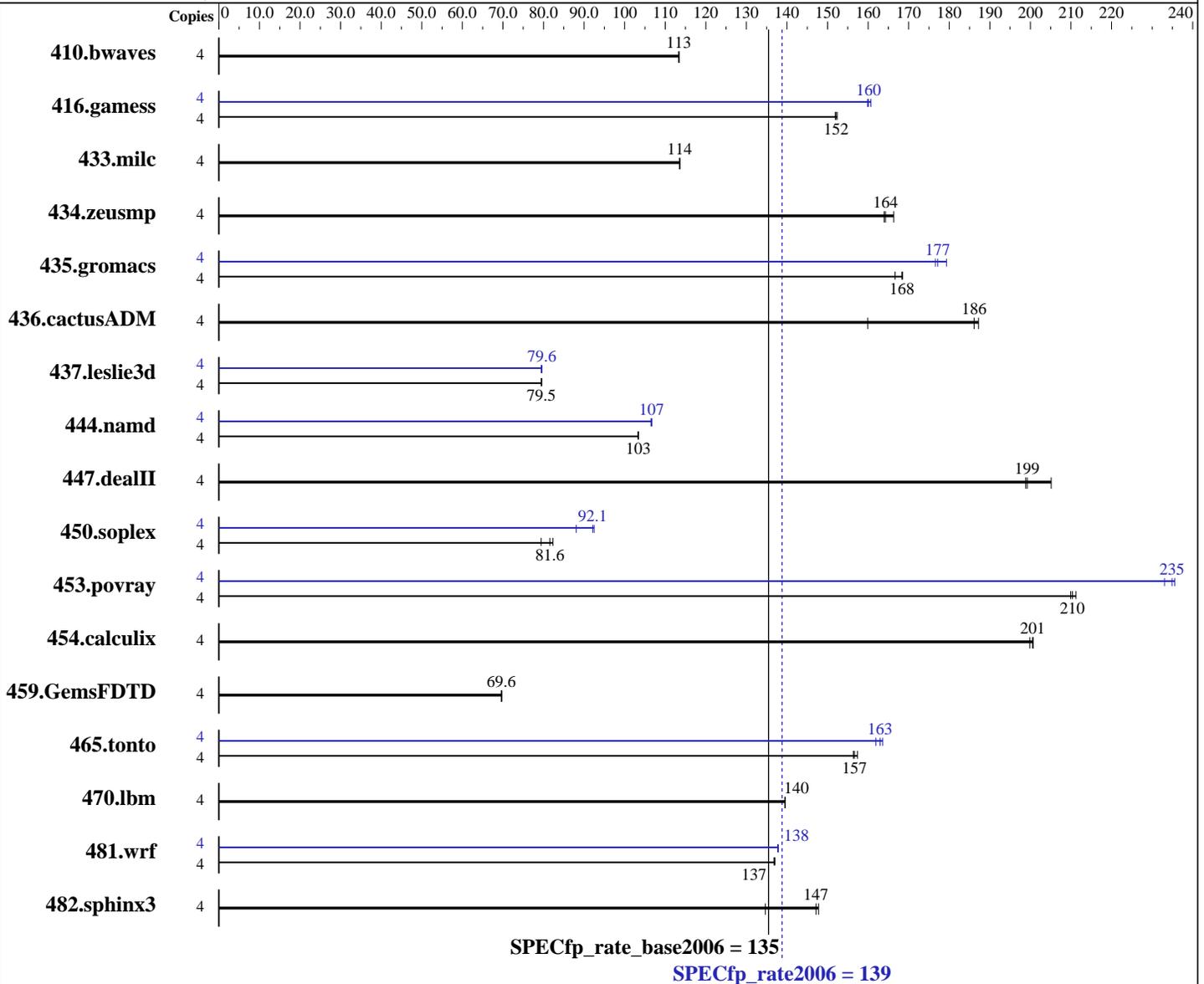
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2014

Hardware Availability: Jul-2014

Software Availability: Nov-2013



### Hardware

CPU Name: Intel Xeon E3-1220 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz  
 CPU MHz: 3100  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux;  
 Fortran: Version 14.0.0.080 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4

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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 32 GB (4 x 8 GB 2Rx8 PC3L-12800E-11, ECC)  
Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
Other Hardware: None

System State: Run level 5 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	479	113	480	113	<b>480</b>	<b>113</b>	4	479	113	480	113	<b>480</b>	<b>113</b>
416.gamess	4	<b>515</b>	<b>152</b>	514	152	516	152	4	487	161	490	160	<b>489</b>	<b>160</b>
433.milc	4	<b>323</b>	<b>114</b>	323	114	323	114	4	<b>323</b>	<b>114</b>	323	114	323	114
434.zeusmp	4	222	164	<b>222</b>	<b>164</b>	219	166	4	222	164	<b>222</b>	<b>164</b>	219	166
435.gromacs	4	169	169	<b>170</b>	<b>168</b>	171	167	4	159	179	<b>161</b>	<b>177</b>	162	177
436.cactusADM	4	255	187	299	160	<b>257</b>	<b>186</b>	4	255	187	299	160	<b>257</b>	<b>186</b>
437.leslie3d	4	473	79.4	473	79.5	<b>473</b>	<b>79.5</b>	4	472	79.6	<b>472</b>	<b>79.6</b>	473	79.4
444.namd	4	311	103	<b>310</b>	<b>103</b>	310	103	4	301	107	<b>301</b>	<b>107</b>	301	106
447.dealII	4	<b>230</b>	<b>199</b>	230	199	223	205	4	<b>230</b>	<b>199</b>	230	199	223	205
450.soplex	4	<b>409</b>	<b>81.6</b>	420	79.4	405	82.3	4	361	92.5	<b>362</b>	<b>92.1</b>	379	88.1
453.povray	4	<b>101</b>	<b>210</b>	101	211	101	210	4	<b>90.6</b>	<b>235</b>	90.3	236	91.3	233
454.calculix	4	164	201	<b>165</b>	<b>201</b>	165	200	4	164	201	<b>165</b>	<b>201</b>	165	200
459.GemsFDTD	4	610	69.6	609	69.7	<b>609</b>	<b>69.6</b>	4	610	69.6	609	69.7	<b>609</b>	<b>69.6</b>
465.tonto	4	250	157	252	156	<b>251</b>	<b>157</b>	4	<b>242</b>	<b>163</b>	241	164	243	162
470.lbm	4	394	139	394	140	<b>394</b>	<b>140</b>	4	394	139	394	140	<b>394</b>	<b>140</b>
481.wrf	4	326	137	327	137	<b>326</b>	<b>137</b>	4	324	138	324	138	<b>324</b>	<b>138</b>
482.sphinx3	4	527	148	<b>530</b>	<b>147</b>	579	135	4	527	148	<b>530</b>	<b>147</b>	579	135

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64:/SPECcpu2006/sh"

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### General Notes (Continued)

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1> /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
For information about Fujitsu please visit: <http://www.fujitsu.com>

### Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

### Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

### C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

### C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

### Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

### Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

### C benchmarks:

icc -m64

### C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

### Fortran benchmarks:

ifort -m64

### Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

### C benchmarks:

433.milc: basepeak = yes  
470.lbm: basepeak = yes  
482.sphinx3: basepeak = yes

### C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32  
447.dealIII: basepeak = yes  
450.soplex: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3  
453.povray: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2) -unroll4  
-ansi-alias

### Fortran benchmarks:

410.bwaves: basepeak = yes  
416.gamess: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-  
434.zeusmp: basepeak = yes  
437.leslie3d: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
459.GemsFDTD: basepeak = yes

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## Peak Optimization Flags (Continued)

465.tonto: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
-auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2)  
-opt-mem-layout-trans=3(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>  
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>  
<http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20130924.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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