



# SPEC® CINT2006 Result

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## Hewlett-Packard Company

**SPECint®\_rate2006 = 633**

ProLiant DL380e Gen8  
(2.50 GHz, Intel Xeon E5-2450 v2)

**SPECint\_rate\_base2006 = 612**

CPU2006 license: 3

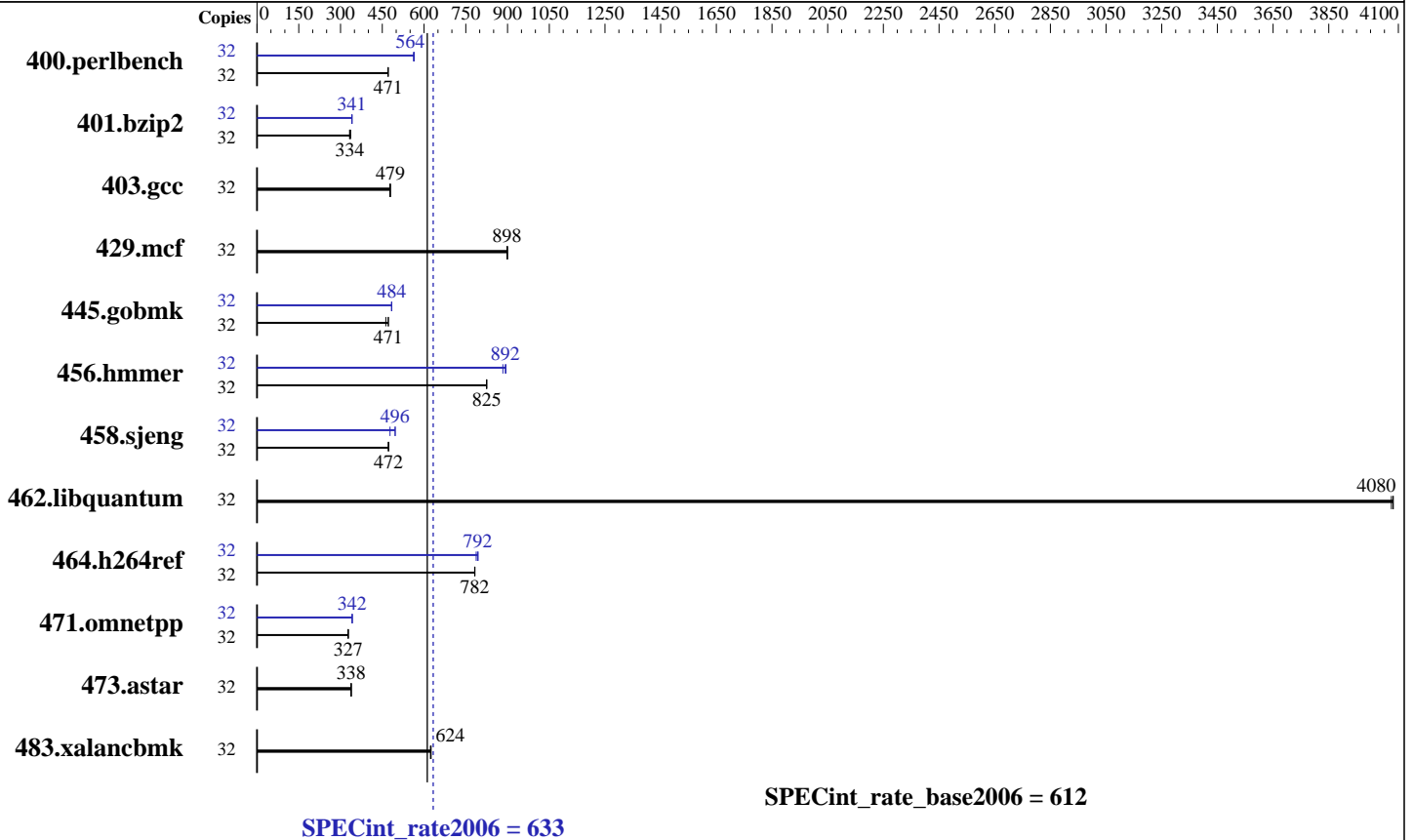
Test date: Jan-2014

Test sponsor: Hewlett-Packard Company

Hardware Availability: Jan-2014

Tested by: Hewlett-Packard Company

Software Availability: Sep-2013



### Hardware

CPU Name: Intel Xeon E5-2450 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2500  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 1 TB 7.2 K SATA, RAID 0  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)  
 Kernel 2.6.32-358.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	32	665	470	663	471	<b>664</b>	<b>471</b>	32	554	565	<b>554</b>	<b>564</b>	556	562
401.bzip2	32	918	336	<b>926</b>	<b>334</b>	929	332	32	<b>906</b>	<b>341</b>	904	341	907	340
403.gcc	32	540	477	<b>538</b>	<b>479</b>	538	479	32	540	477	<b>538</b>	<b>479</b>	538	479
429.mcf	32	325	898	324	900	<b>325</b>	<b>898</b>	32	325	898	324	900	<b>325</b>	<b>898</b>
445.gobmk	32	711	472	<b>712</b>	<b>471</b>	725	463	32	693	484	695	483	<b>694</b>	<b>484</b>
456.hammer	32	<b>362</b>	<b>825</b>	362	825	362	825	32	338	883	334	894	<b>335</b>	<b>892</b>
458.sjeng	32	<b>820</b>	<b>472</b>	821	472	819	473	32	810	478	<b>780</b>	<b>496</b>	780	496
462.libquantum	32	<b>162</b>	<b>4080</b>	162	4080	163	4070	32	<b>162</b>	<b>4080</b>	162	4080	163	4070
464.h264ref	32	906	782	<b>905</b>	<b>782</b>	905	783	32	892	794	<b>894</b>	<b>792</b>	899	787
471.omnetpp	32	609	328	<b>611</b>	<b>327</b>	611	327	32	585	342	<b>585</b>	<b>342</b>	585	342
473.astar	32	663	339	666	337	<b>664</b>	<b>338</b>	32	663	339	666	337	<b>664</b>	<b>338</b>
483.xalancbmk	32	353	625	<b>354</b>	<b>624</b>	354	623	32	353	625	<b>354</b>	<b>624</b>	354	623

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Filesystem page cache cleared with:  
echo 1 > /proc/sys/vm/drop\_caches  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>  
Disabled unused Linux services through "stop\_services.sh" before running.

## Platform Notes

BIOS Configuration:  
HP Power Profile set to Maximum Performance  
Memory Power Savings Mode set to Maximum Performance  
Thermal Configuration set to Maximum Cooling  
Collaborative Power Control set to Disabled  
Dynamic Power Capping Functionality set to Disabled  
Processor Power and Utilization Monitoring set to Disabled  
Memory Refresh Rate set to 1x

Sysinfo program /cpu2006/config/sysinfo.rev6818  
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### Platform Notes (Continued)

\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on DL380e-Gen8-012 Tue Jan 21 17:42:21 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2450 v2 @ 2.50GHz
 2 "physical id"s (chips)
 32 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 8
  siblings  : 16
  physical 0: cores 0 1 2 3 4 5 6 7
  physical 1: cores 0 1 2 3 4 5 6 7
cache size : 20480 KB
```

```
From /proc/meminfo
MemTotal:      99032836 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux DL380e-Gen8-012 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST
2013 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jan 21 17:41
```

```
SPEC is set to: /cpu2006
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda3        ext4      915G  25G  844G   3% /
```

```
Additional information from dmidecode:
BIOS HP P73 11/12/2013
Memory:
12x HP 689911-071 8 GB 1600 MHz 2 rank
```

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RedHat EL 6.4

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m32

400.perlbench: icc -m64

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## Peak Compiler Invocation (Continued)

401.bzip2: `icc -m64`

456.hmmer: `icc -m64`

458.sjeng: `icc -m64`

C++ benchmarks:

`icpc -m32`

## Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64`  
401.bzip2: `-DSPEC_CPU_LP64`  
456.hmmer: `-DSPEC_CPU_LP64`  
458.sjeng: `-DSPEC_CPU_LP64`  
462.libquantum: `-DSPEC_CPU_LINUX`  
483.xalancbmk: `-DSPEC_CPU_LINUX`

## Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`  
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`  
`-auto-ilp32`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`  
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`  
`-opt-prefetch -auto-ilp32 -ansi-alias`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)`  
`-ansi-alias -opt-mem-layout-trans=3`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32`

458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`  
`-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`  
`-unroll4 -auto-ilp32`

462.libquantum: `basepeak = yes`

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## Peak Optimization Flags (Continued)

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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