



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 1018D-73MTF
(X10SL7-F, Intel Xeon E3-1270 v3)

SPECint®_rate2006 = 217

SPECint_rate_base2006 = 210

CPU2006 license: 001176

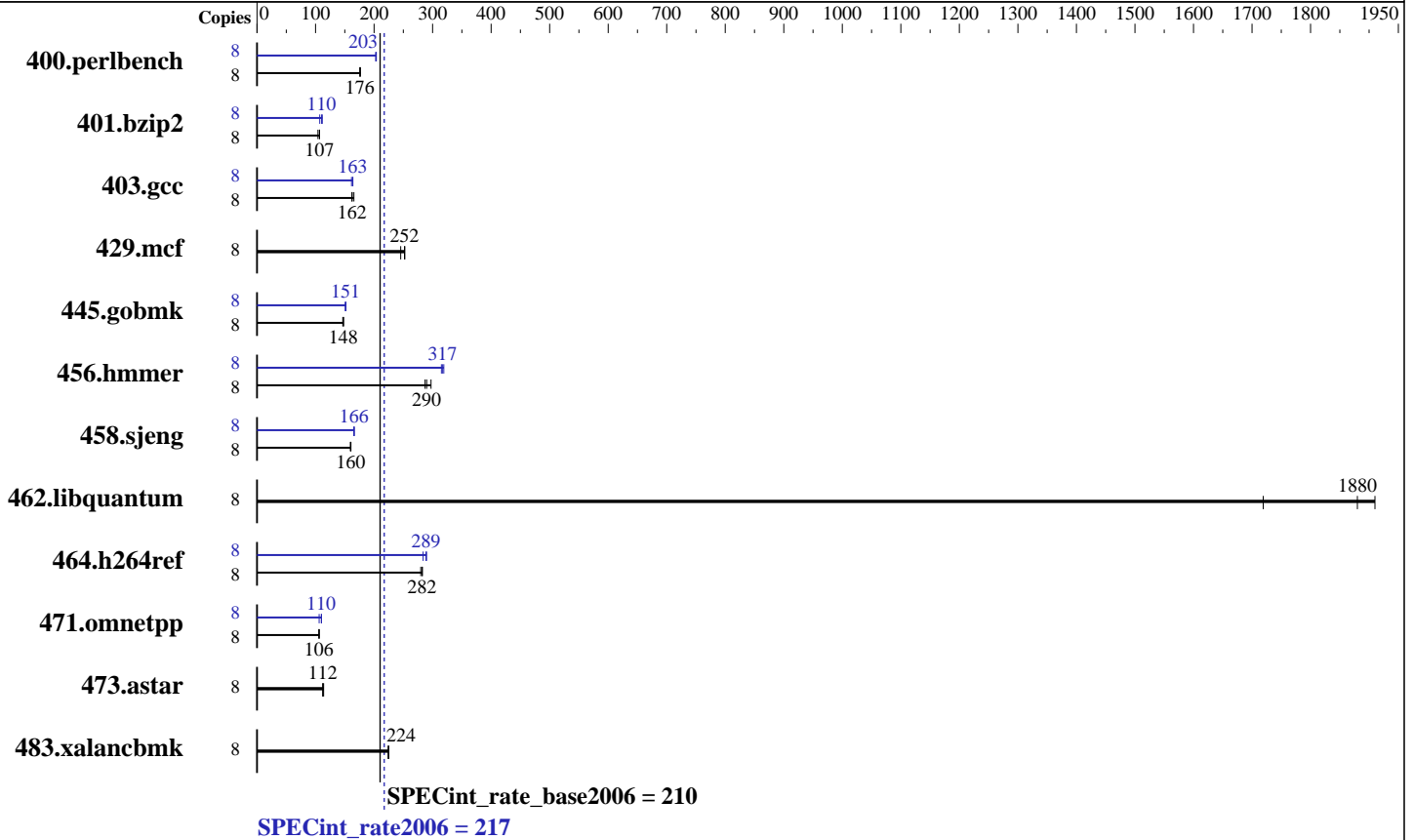
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jun-2013

Hardware Availability: Jun-2013

Software Availability: May-2013



Hardware

CPU Name: Intel Xeon E3-1270 v3
 CPU Characteristics: Intel Turbo Boost Technology up to 3.90 GHz
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
 Disk Subsystem: 2 x 600 GB SAS 6Gbps, RAID 1, 10000 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 Kernel 2.6.32-358.el6.x86_64
 Compiler: C/C++: Version 13.1.1.163 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 1018D-73MTF
(X10SL7-F, Intel Xeon E3-1270 v3)

SPECint_rate2006 = 217

SPECint_rate_base2006 = 210

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jun-2013
Hardware Availability: Jun-2013
Software Availability: May-2013

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	442	177	445	176	444	176	8	384	203	383	204	386	203
401.bzip2	8	741	104	724	107	723	107	8	699	110	720	107	692	112
403.gcc	8	396	162	389	166	397	162	8	393	164	394	163	399	161
429.mcf	8	297	246	289	253	290	252	8	297	246	289	253	290	252
445.gobmk	8	573	147	569	148	568	148	8	552	152	557	151	554	151
456.hammer	8	260	287	258	290	251	297	8	237	315	234	319	235	317
458.sjeng	8	607	159	603	160	604	160	8	583	166	583	166	585	165
462.libquantum	8	96.4	1720	86.8	1910	88.2	1880	8	96.4	1720	86.8	1910	88.2	1880
464.h264ref	8	633	280	628	282	627	283	8	613	289	624	284	611	290
471.omnetpp	8	472	106	470	106	472	106	8	455	110	454	110	471	106
473.astar	8	493	114	499	112	500	112	8	493	114	499	112	500	112
483.xalancbmk	8	246	224	246	224	245	225	8	246	224	246	224	245	225

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Sysinfo program /home/cpu2006_IC13.1/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on localhost.localdomain Mon Jun 3 21:36:34 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E3-1270 v3 @ 3.50GHz
1 "physical id"s (chips)
8 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 4
siblings : 8
```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 1018D-73MTF
(X10SL7-F, Intel Xeon E3-1270 v3)

SPECint_rate2006 = 217

SPECint_rate_base2006 = 210

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jun-2013
Hardware Availability: Jun-2013
Software Availability: May-2013

Platform Notes (Continued)

```
physical 0: cores 0 1 2 3
cache size : 8192 KB
```

```
From /proc/meminfo
MemTotal:      16266532 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)
```

```
From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

```
uname -a:
Linux localhost.localdomain 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41
EST 2013 x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jun 3 21:32
```

```
SPEC is set to: /home/cpu2006_IC13.1
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_home
                ext4      492G  7.9G  459G   2% /home
```

```
Additional information from dmidecode:
BIOS American Megatrends Inc. 1.0a 05/09/2013
Memory:
2x      8 GB
2x Hynix/Hyundai HMT41GU7MFR8C-PB 8 GB 1600 MHz 2 rank
1x Micron/Numonyx 25Q Series 16 MB
2x [Empty] [Empty]
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006_IC13.1/libs/32:/home/cpu2006_IC13.1/libs/64:/home/cpu2006_IC13.1/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 1018D-73MTF
(X10SL7-F, Intel Xeon E3-1270 v3)

SPECint_rate2006 = 217

SPECint_rate_base2006 = 210

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jun-2013
Hardware Availability: Jun-2013
Software Availability: May-2013

Base Compiler Invocation

C benchmarks:
icc -m32

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

400.perlbench: icc -m64
401.bzip2: icc -m64
456.hmmer: icc -m64
458.sjeng: icc -m64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 1018D-73MTF
(X10SL7-F, Intel Xeon E3-1270 v3)

SPECint_rate2006 = 217

SPECint_rate_base2006 = 210

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jun-2013
Hardware Availability: Jun-2013
Software Availability: May-2013

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-auto-ilp32
401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div
429.mcf: basepeak = yes
445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -opt-mem-layout-trans=3
456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
462.libquantum: basepeak = yes
464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Supermicro

SuperServer 1018D-73MTF
(X10SL7-F, Intel Xeon E3-1270 v3)

SPECint_rate2006 = 217

SPECint_rate_base2006 = 210

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Jun-2013
Hardware Availability: Jun-2013
Software Availability: May-2013

Peak Optimization Flags (Continued)

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/sh -lsmartheap

473.aster: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.html>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.20130702.xml>
<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revB.20130719.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 16:09:03 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 24 September 2013.