



# SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## IBM Corporation

**SPECint®2006 = 38.6**

IBM System x3500 M4 (Intel Xeon E5-2658)

**SPECint\_base2006 = 36.0**

CPU2006 license: 11

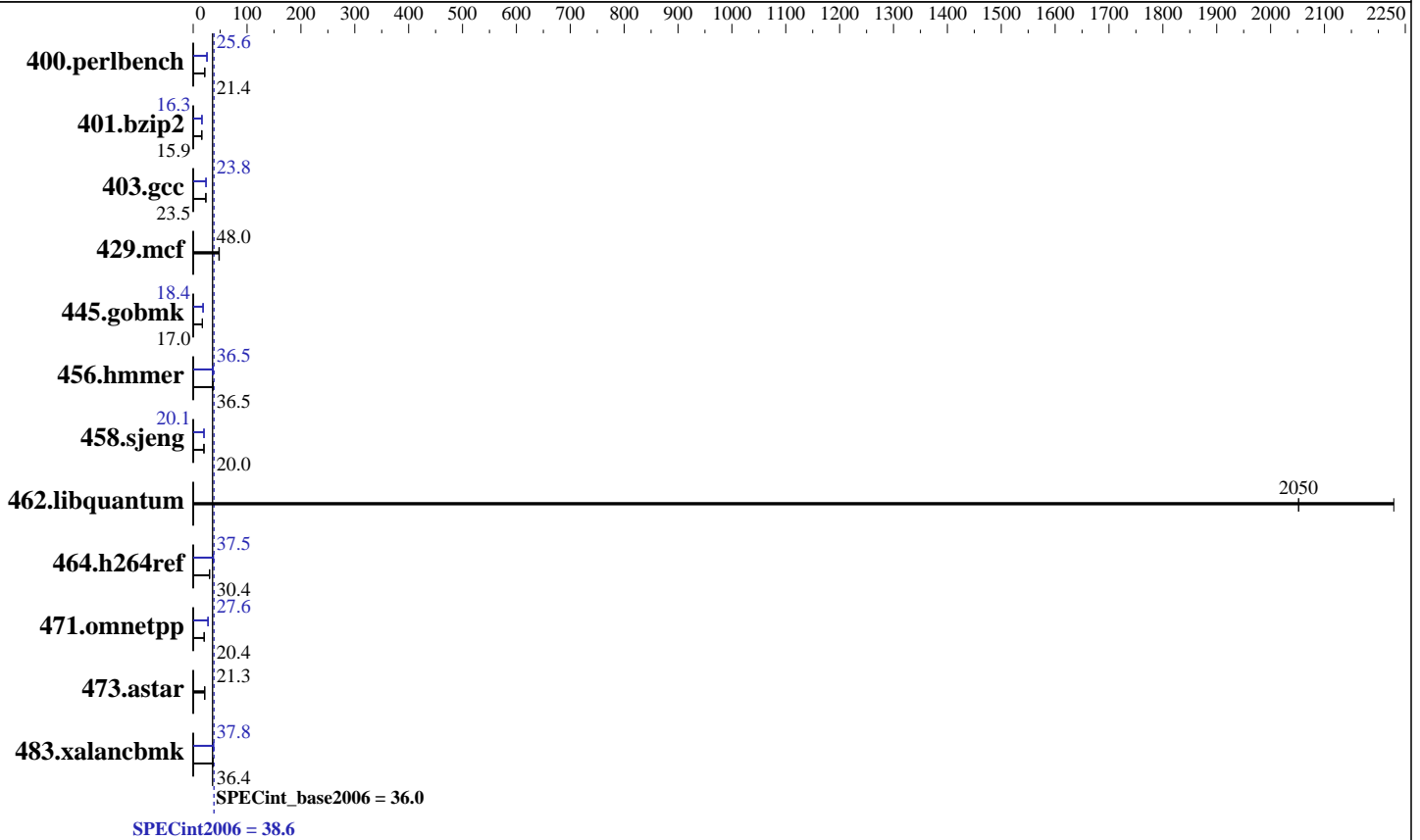
Test date: Nov-2012

Test sponsor: IBM Corporation

Hardware Availability: Mar-2012

Tested by: IBM Corporation

Software Availability: Jun-2012



### Hardware

CPU Name: Intel Xeon E5-2658  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.40 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 20 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (16 x 8 GB 2Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 1 TB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.3 (Santiago)  
 2.6.32-279.el6.x86\_64  
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 38.6

IBM System x3500 M4 (Intel Xeon E5-2658)

SPECint\_base2006 = 36.0

CPU2006 license: 11

Test date: Nov-2012

Test sponsor: IBM Corporation

Hardware Availability: Mar-2012

Tested by: IBM Corporation

Software Availability: Jun-2012

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	455	21.5	457	21.4	<u>457</u>	<u>21.4</u>	380	25.7	<u>382</u>	<u>25.6</u>	382	25.5
401.bzip2	605	15.9	606	15.9	<u>606</u>	<u>15.9</u>	594	16.2	<u>594</u>	<u>16.3</u>	593	16.3
403.gcc	<u>343</u>	<u>23.5</u>	343	23.5	342	23.5	<u>338</u>	<u>23.8</u>	338	23.8	338	23.8
429.mcf	191	47.9	<u>190</u>	<u>48.0</u>	188	48.5	191	47.9	<u>190</u>	<u>48.0</u>	188	48.5
445.gobmk	615	17.0	616	17.0	<u>616</u>	<u>17.0</u>	571	18.4	571	18.4	<u>571</u>	<u>18.4</u>
456.hammer	256	36.5	<u>256</u>	<u>36.5</u>	256	36.5	255	36.5	256	36.5	<u>255</u>	<u>36.5</u>
458.sjeng	<u>606</u>	<u>20.0</u>	606	20.0	606	20.0	603	20.1	603	20.1	<u>603</u>	<u>20.1</u>
462.libquantum	<u>10.1</u>	<u>2050</u>	10.1	2050	9.30	2230	<u>10.1</u>	<u>2050</u>	10.1	2050	9.30	2230
464.h264ref	<u>728</u>	<u>30.4</u>	725	30.5	734	30.2	592	37.4	<u>590</u>	<u>37.5</u>	587	37.7
471.omnetpp	306	20.5	<u>306</u>	<u>20.4</u>	306	20.4	226	27.7	<u>227</u>	<u>27.6</u>	227	27.5
473.astar	<u>330</u>	<u>21.3</u>	330	21.3	327	21.5	<u>330</u>	<u>21.3</u>	330	21.3	327	21.5
483.xalancbmk	<u>190</u>	<u>36.4</u>	189	36.5	190	36.3	183	37.8	183	37.7	<u>183</u>	<u>37.8</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Zone reclaim mode enabled with:  
echo 1 > /proc/sys/vm/zone\_reclaim\_mode

## Platform Notes

BIOS Settings:  
Operating Mode set to Maximum Performance  
Sysinfo program /home/SPECcpu-v1.2/config/sysinfo.rev6800  
\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3  
running on x3500M4 Sun Nov 4 08:37:44 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) CPU E5-2658 0 @ 2.10GHz  
2 "physical id"s (chips)  
32 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 8  
siblings : 16  
physical 0: cores 0 1 2 3 4 5 6 7  
physical 1: cores 0 1 2 3 4 5 6 7

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 38.6

IBM System x3500 M4 (Intel Xeon E5-2658)

SPECint\_base2006 = 36.0

CPU2006 license: 11

Test date: Nov-2012

Test sponsor: IBM Corporation

Hardware Availability: Mar-2012

Tested by: IBM Corporation

Software Availability: Jun-2012

## Platform Notes (Continued)

cache size : 20480 KB

From /proc/meminfo

```
MemTotal:      132230936 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

/usr/bin/lsb\_release -d

Red Hat Enterprise Linux Server release 6.3 (Santiago)

From /etc/\*release\* /etc/\*version\*

```
redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux x3500M4 2.6.32-279.el6.x86_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012
x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Nov 2 16:03

SPEC is set to: /home/SPECcpu-v1.2

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/vg_x3500m4-lv_home
                ext4      862G   61G  758G   8% /home
```

Additional information from dmidecode:

Memory:

16x Samsung M393B1K70DH0-CK0 8 GB 1600 MHz 2 rank

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact,1,0"

LD\_LIBRARY\_PATH = "/home/SPECcpu-v1.2/libs/32:/home/SPECcpu-v1.2/libs/64"

OMP\_NUM\_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

memory using RHEL5.5

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 3



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 38.6

IBM System x3500 M4 (Intel Xeon E5-2658)

SPECint\_base2006 = 36.0

CPU2006 license: 11

Test date: Nov-2012

Test sponsor: IBM Corporation

Hardware Availability: Mar-2012

Tested by: IBM Corporation

Software Availability: Jun-2012

## Base Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
C++ benchmarks:  
-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs  
-L/smartheap -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):  
icc -m64  
400.perlbench: icc -m32  
445.gobmk: icc -m32

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 38.6

IBM System x3500 M4 (Intel Xeon E5-2658)

SPECint\_base2006 = 36.0

CPU2006 license: 11

Test date: Nov-2012

Test sponsor: IBM Corporation

Hardware Availability: Mar-2012

Tested by: IBM Corporation

Software Availability: Jun-2012

## Peak Compiler Invocation (Continued)

464.h264ref: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
 401.bzip2: -DSPEC\_CPU\_LP64  
 403.gcc: -DSPEC\_CPU\_LP64  
 429.mcf: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
 -ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch  
 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc  
 -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
 -ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

Continued on next page



# SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

SPECint2006 = 38.6

IBM System x3500 M4 (Intel Xeon E5-2658)

SPECint\_base2006 = 36.0

CPU2006 license: 11

Test date: Nov-2012

Test sponsor: IBM Corporation

Hardware Availability: Mar-2012

Tested by: IBM Corporation

Software Availability: Jun-2012

## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias  
-Wl,-z,muldefs -L/smartheap -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/IBM-Platform-Flags-V1.2-SNB-C.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Thu Jul 24 13:32:39 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 4 December 2012.