



SPEC® CINT2006 Result

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Supermicro

SPECint®2006 = 53.1

SuperServer 1017C-TF (X9SCL-F, Intel E3-1270)

SPECint_base2006 = 50.0

CPU2006 license: 001176

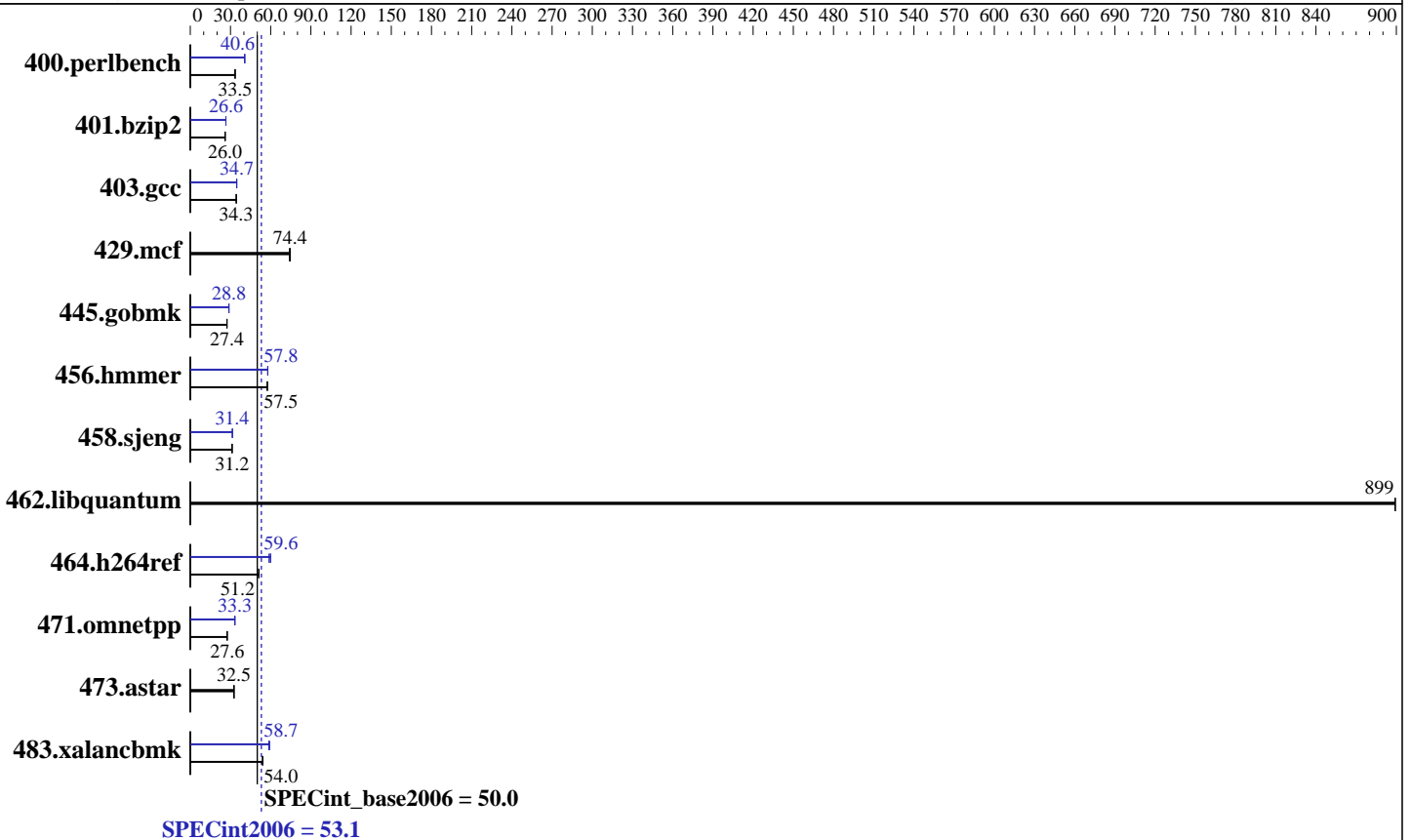
Test date: May-2012

Test sponsor: Supermicro

Hardware Availability: Apr-2011

Tested by: Supermicro

Software Availability: Oct-2011



Hardware

CPU Name: Intel Xeon E3-1270
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
 CPU MHz: 3400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server Release 6.1, Kernel 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++; Version 12.1.0.225 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	291	33.5	293	33.3	<u>292</u>	<u>33.5</u>	241	40.5	240	40.7	<u>240</u>	<u>40.6</u>
401.bzip2	371	26.0	<u>371</u>	<u>26.0</u>	371	26.0	<u>363</u>	<u>26.6</u>	363	26.6	363	26.6
403.gcc	234	34.3	<u>235</u>	<u>34.3</u>	235	34.3	232	34.6	<u>232</u>	<u>34.7</u>	232	34.7
429.mcf	122	74.6	123	73.9	<u>123</u>	<u>74.4</u>	122	74.6	123	73.9	<u>123</u>	<u>74.4</u>
445.gobmk	<u>383</u>	<u>27.4</u>	383	27.4	383	27.4	364	28.9	<u>364</u>	<u>28.8</u>	364	28.8
456.hammer	<u>162</u>	<u>57.5</u>	163	57.4	162	57.5	161	57.8	161	57.8	<u>161</u>	<u>57.8</u>
458.sjeng	387	31.2	388	31.2	<u>388</u>	<u>31.2</u>	385	31.4	386	31.4	<u>385</u>	<u>31.4</u>
462.libquantum	23.0	899	<u>23.0</u>	<u>899</u>	23.0	899	23.0	899	<u>23.0</u>	<u>899</u>	23.0	899
464.h264ref	432	51.3	<u>432</u>	<u>51.2</u>	434	51.0	369	60.0	377	58.7	<u>371</u>	<u>59.6</u>
471.omnetpp	<u>227</u>	<u>27.6</u>	228	27.4	226	27.6	<u>188</u>	<u>33.3</u>	188	33.3	188	33.3
473.astar	214	32.8	216	32.5	<u>216</u>	<u>32.5</u>	214	32.8	216	32.5	<u>216</u>	<u>32.5</u>
483.xalancbmk	128	53.9	128	54.1	<u>128</u>	<u>54.0</u>	<u>118</u>	<u>58.7</u>	118	58.6	116	59.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"
OMP_NUM_THREADS = "4"
```

```
Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
```

Base Compiler Invocation

```
C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64
```



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Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Base Optimization Flags

C benchmarks:

`-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32`

C++ benchmarks:

`-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -Wl,-z,muldefs -L/smartheap -lsmartheap64`

Base Other Flags

C benchmarks:

`403.gcc: -Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):

`icc -m64`

`400.perlbench: icc -m32`

`445.gobmk: icc -m32`

`464.h264ref: icc -m32`

C++ benchmarks (except as noted below):

`icpc -m32`

`473.astar: icpc -m64`



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
               -ansi-alias

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
            -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
          -opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
            -ansi-alias

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
            -ansi-alias

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
            -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
              -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
              -no-prec-div(pass 2) -prof-use(pass 2)
              -opt-ra-region-strategy=block -ansi-alias
              -Wl,-z,muldefs -L/smartheap -lsmartheap

```

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Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact webmaster@spec.org.

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