



SPEC[®] CFP2006 Result

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Supermicro

SPECfp[®]2006 = **77.8**

SuperServer 1017GR-TF (X9SRG-F, Intel E5-1620)

SPECfp_base2006 = **75.9**

CPU2006 license: 001176

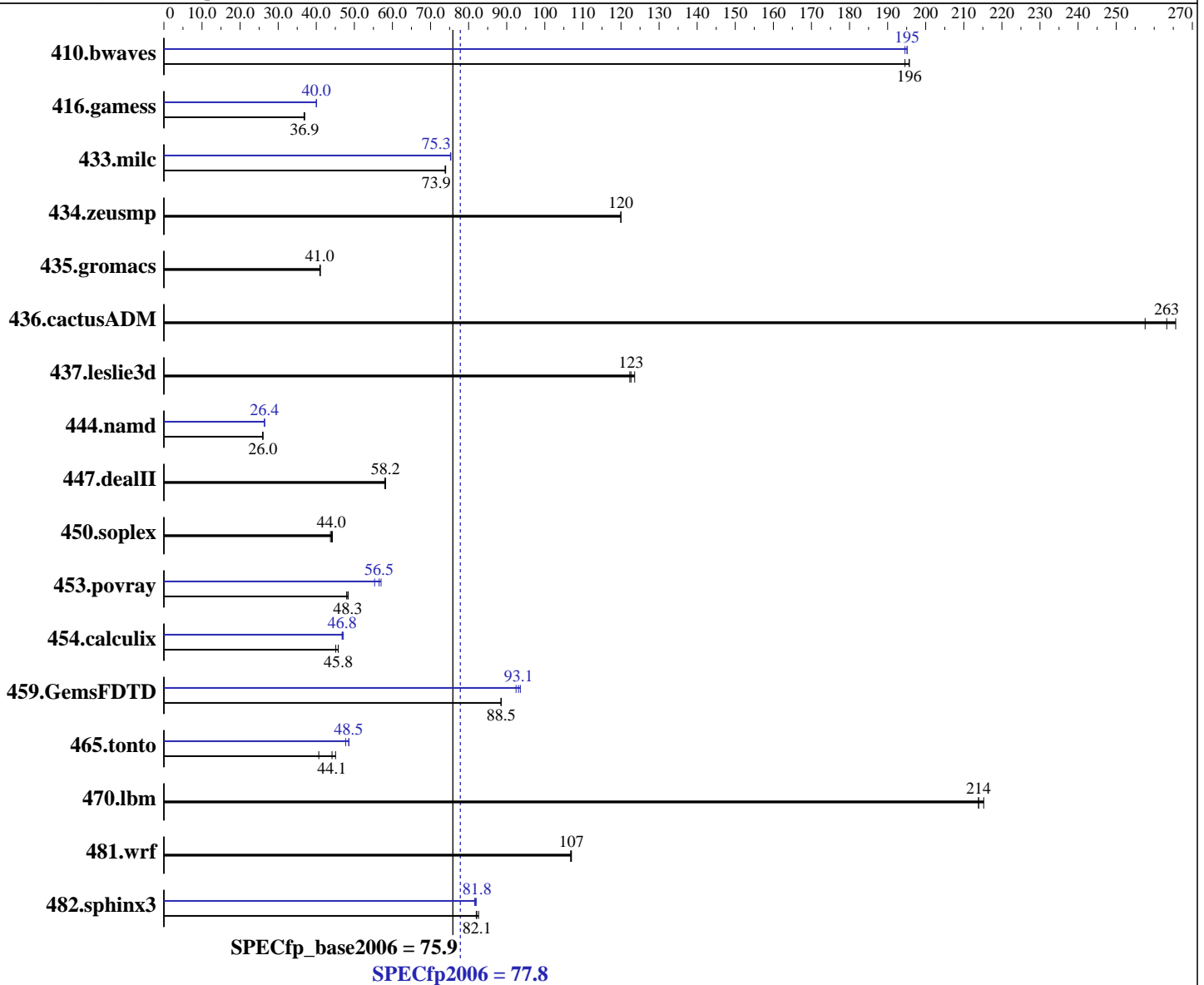
Test date: Apr-2012

Test sponsor: Supermicro

Hardware Availability: Mar-2012

Tested by: Supermicro

Software Availability: Dec-2011



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Hardware

CPU Name: Intel Xeon E5-1620
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
 CPU MHz: 3600
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: Red Hat Enterprise Linux Server Release 6.2 (Santiago), Kernel 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux; Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

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L3 Cache: 10 MB I+D on chip per chip
 Other Cache: None
 Memory: 64 GB (8 x 8 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x 160 GB SATA II, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	69.4	196	69.8	195	69.4	196	69.9	195	69.7	195	69.7	195
416.gamess	530	36.9	530	36.9	532	36.8	489	40.0	489	40.0	489	40.1
433.milc	124	74.0	124	73.9	124	73.8	122	75.3	122	75.2	122	75.3
434.zeusmp	75.8	120	75.8	120	75.8	120	75.8	120	75.8	120	75.8	120
435.gromacs	174	41.0	174	41.1	174	41.0	174	41.0	174	41.1	174	41.0
436.cactusADM	45.0	266	45.4	263	46.4	258	45.0	266	45.4	263	46.4	258
437.leslie3d	76.9	122	76.1	124	76.7	123	76.9	122	76.1	124	76.7	123
444.namd	309	26.0	309	26.0	309	26.0	304	26.4	304	26.4	304	26.4
447.dealII	197	58.2	197	58.0	197	58.2	197	58.2	197	58.0	197	58.2
450.soplex	190	44.0	189	44.2	191	43.7	190	44.0	189	44.2	191	43.7
453.povray	110	48.3	111	48.0	110	48.4	94.2	56.5	96.2	55.3	93.3	57.0
454.calculix	180	45.8	183	45.1	180	45.8	176	46.8	176	46.8	175	47.1
459.GemsFDTD	120	88.5	120	88.5	120	88.5	113	93.6	115	92.4	114	93.1
465.tonto	223	44.1	218	45.1	242	40.7	203	48.5	203	48.6	206	47.7
470.lbm	64.2	214	64.2	214	63.8	215	64.2	214	64.2	214	63.8	215
481.wrf	105	107	104	107	104	107	105	107	104	107	104	107
482.sphinx3	238	82.0	237	82.1	236	82.6	238	81.8	239	81.6	238	82.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:
 KMP_AFFINITY = "granularity=fine,scatter"
 LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"
 OMP_NUM_THREADS = "4"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
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Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

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Peak Optimization Flags (Continued)

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 04:17:14 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 24 April 2012.