



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint®2006 = 41.3

Acer AC100 (Intel Xeon E3-1260L, 2.40 GHz)

SPECint_base2006 = 38.2

CPU2006 license: 97

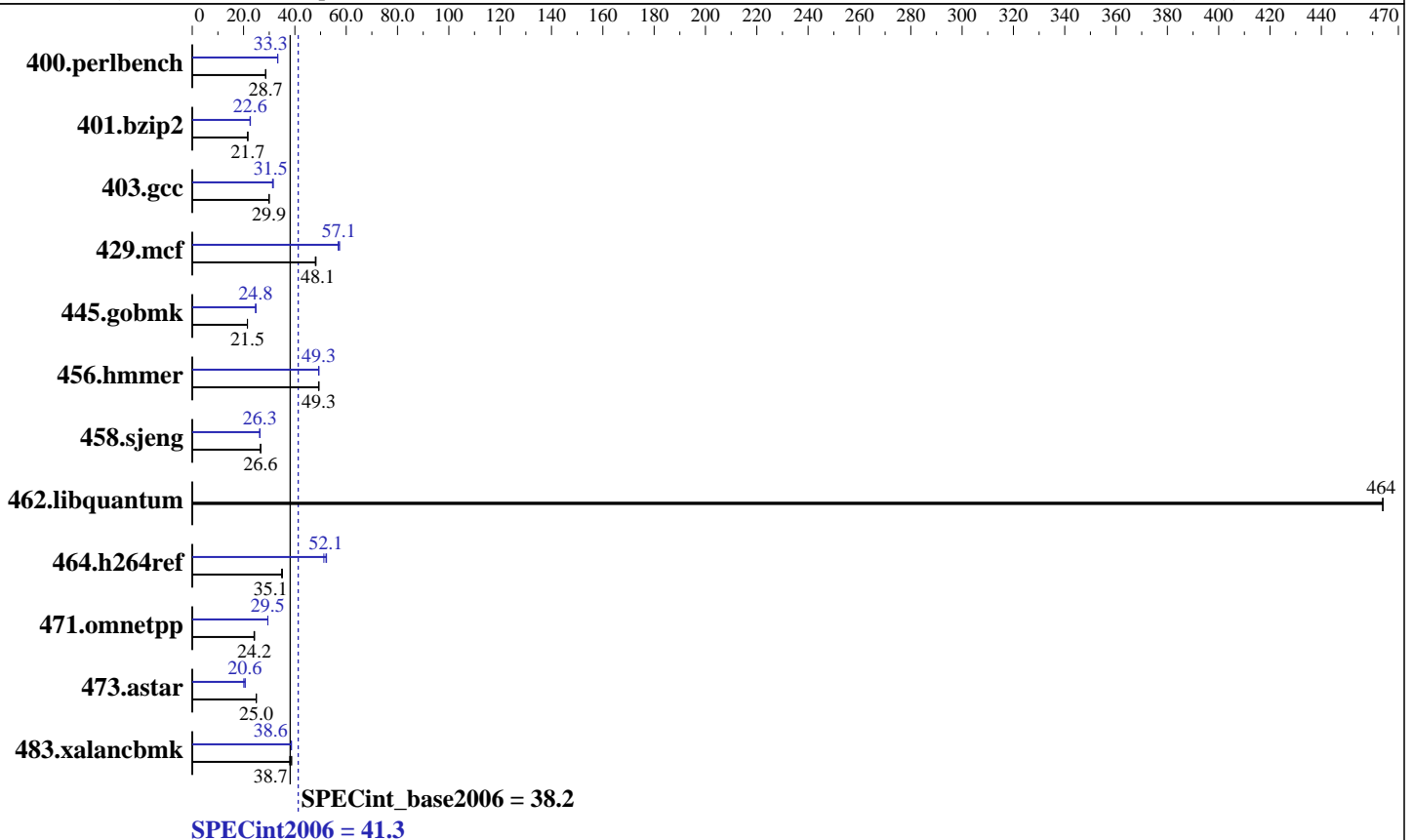
Test date: Aug-2011

Test sponsor: Acer Incorporated

Hardware Availability: Sep-2011

Tested by: Acer Incorporated

Software Availability: Jan-2011



Hardware

CPU Name: Intel Xeon E3-1260L
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB 2Rx8 PC3-10600E-9, ECC)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM SATA HDD
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32, Version 12.0.1.116 Build 20101116
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint2006 = 41.3

Acer AC100 (Intel Xeon E3-1260L, 2.40 GHz)

SPECint_base2006 = 38.2

CPU2006 license: 97

Test date: Aug-2011

Test sponsor: Acer Incorporated

Hardware Availability: Sep-2011

Tested by: Acer Incorporated

Software Availability: Jan-2011

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	342	28.6	<u>341</u>	<u>28.7</u>	341	28.7	293	33.3	<u>293</u>	<u>33.3</u>	293	33.3
401.bzip2	445	21.7	<u>445</u>	<u>21.7</u>	448	21.6	428	22.5	<u>427</u>	<u>22.6</u>	426	22.7
403.gcc	268	30.0	<u>269</u>	<u>29.9</u>	270	29.9	257	31.3	<u>256</u>	<u>31.5</u>	255	31.6
429.mcf	189	48.2	<u>190</u>	<u>48.1</u>	190	48.0	160	56.9	<u>160</u>	<u>57.1</u>	159	57.4
445.gobmk	488	21.5	488	21.5	<u>488</u>	<u>21.5</u>	<u>424</u>	<u>24.8</u>	424	24.8	424	24.8
456.hammer	190	49.2	<u>189</u>	<u>49.3</u>	189	49.4	189	49.3	189	49.3	<u>189</u>	<u>49.3</u>
458.sjeng	454	26.6	<u>454</u>	<u>26.6</u>	454	26.7	<u>459</u>	<u>26.3</u>	460	26.3	459	26.4
462.libquantum	44.7	464	<u>44.7</u>	<u>464</u>	44.7	464	44.7	464	<u>44.7</u>	<u>464</u>	44.7	464
464.h264ref	629	35.2	635	34.8	<u>631</u>	<u>35.1</u>	431	51.3	423	52.3	<u>425</u>	<u>52.1</u>
471.omnetpp	257	24.3	260	24.1	<u>258</u>	<u>24.2</u>	<u>212</u>	<u>29.5</u>	212	29.5	212	29.5
473.astar	281	25.0	281	25.0	<u>281</u>	<u>25.0</u>	349	20.1	<u>341</u>	<u>20.6</u>	340	20.6
483.xalancbmk	182	38.0	<u>178</u>	<u>38.7</u>	178	38.7	178	38.7	179	38.6	<u>179</u>	<u>38.6</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
Large pages were disabled for this run

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M
Binaries compiled on RHEL5.5

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint2006 = 41.3

Acer AC100 (Intel Xeon E3-1260L, 2.40 GHz)

SPECint_base2006 = 38.2

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Aug-2011

Hardware Availability: Sep-2011

Software Availability: Jan-2011

Base Portability Flags (Continued)

```

403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Base Optimization Flags

C benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

```

C++ benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap64
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

```

Base Other Flags

C benchmarks:

```

403.gcc: -Dalloca=_alloca

```

Peak Compiler Invocation

C benchmarks (except as noted below):

```

icc -m64

```

```

400.perlbench: icc -m32

```

```

429.mcf: icc -m32

```

```

445.gobmk: icc -m32

```

```

464.h264ref: icc -m32

```

C++ benchmarks (except as noted below):

```

icpc -m32

```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint2006 = 41.3

Acer AC100 (Intel Xeon E3-1260L, 2.40 GHz)

SPECint_base2006 = 38.2

CPU2006 license: 97

Test date: Aug-2011

Test sponsor: Acer Incorporated

Hardware Availability: Sep-2011

Tested by: Acer Incorporated

Software Availability: Jan-2011

Peak Compiler Invocation (Continued)

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
 -ansi-alias
 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div -prof-use(pass 2) -auto-ilp32 -opt-prefetch
 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div -inline-calloc
 -opt-malloc-options=3 -auto-ilp32
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

429.mcf: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
 -ansi-alias
 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
 -auto-ilp32 -ansi-alias
 -B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
 -ansi-alias
 -B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll4

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint2006 = 41.3

Acer AC100 (Intel Xeon E3-1260L, 2.40 GHz)

SPECint_base2006 = 38.2

CPU2006 license: 97

Test date: Aug-2011

Test sponsor: Acer Incorporated

Hardware Availability: Sep-2011

Tested by: Acer Incorporated

Software Availability: Jan-2011

Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

473.astar: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/smartheap -lsmartheap64

483.xalancbmk: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -ansi-alias
-Wl,-z,muldefs -L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.20110608.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Acer-Intel-Linux-Settings-flags.20110608.xml>



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint2006 = 41.3

Acer AC100 (Intel Xeon E3-1260L, 2.40 GHz)

SPECint_base2006 = 38.2

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Aug-2011

Hardware Availability: Sep-2011

Software Availability: Jan-2011

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 21:17:51 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 September 2011.