



# SPEC<sup>®</sup> CINT2006 Result

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## IBM Corporation

SPECint<sup>®</sup>\_rate2006 = 951

IBM BladeCenter HX5 (Intel Xeon E7-8867L)

SPECint\_rate\_base2006 = 884

CPU2006 license: 11

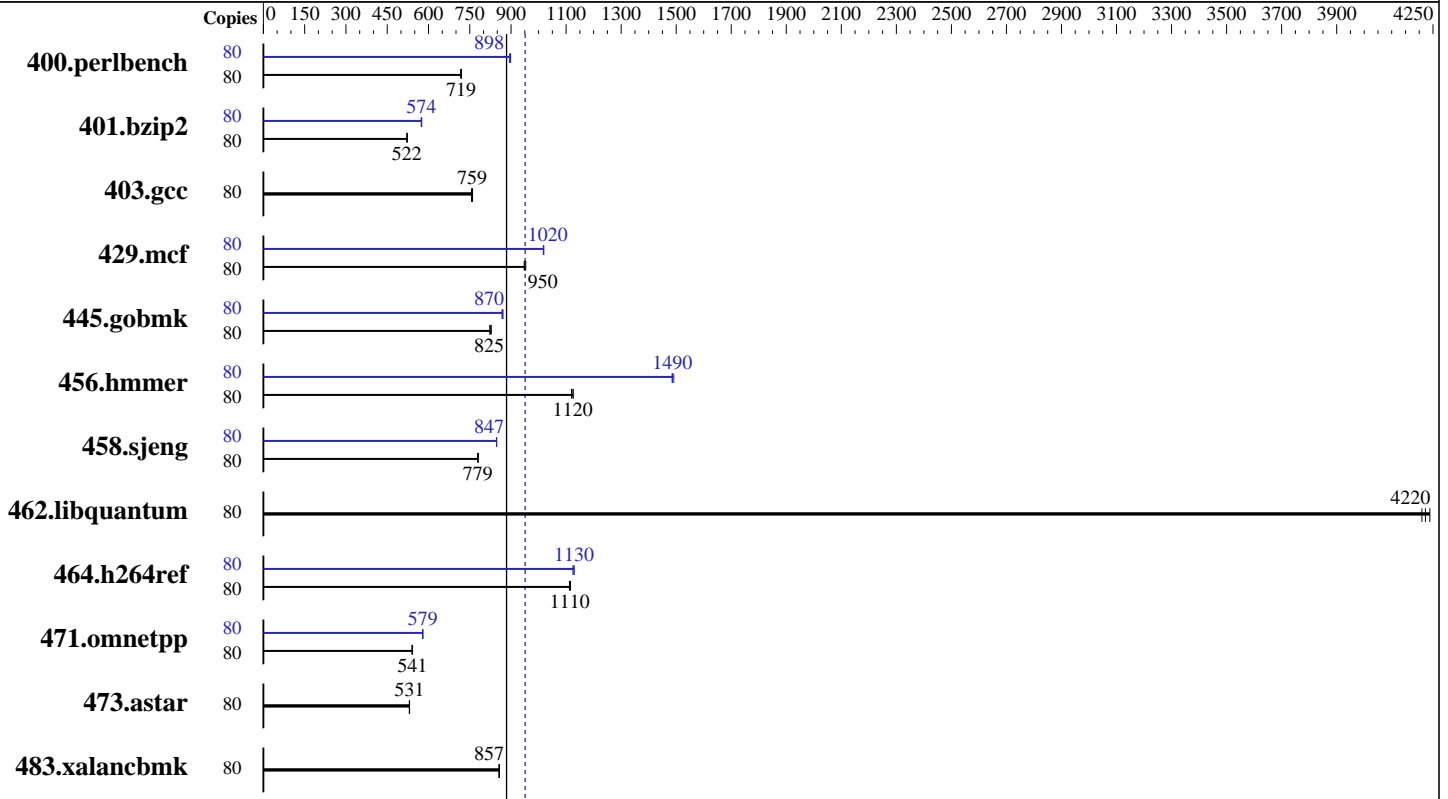
Test date: May-2011

Test sponsor: IBM Corporation

Hardware Availability: May-2011

Tested by: IBM Corporation

Software Availability: Jan-2011



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### Hardware

CPU Name: Intel Xeon E7-8867L  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz  
 CPU MHz: 2133  
 FPU: Integrated  
 CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,3,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 30 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (32 x 8 GB 4Rx8 PC3-8500R-7, ECC)  
 Disk Subsystem: 4 x 50 GB SSD, RAID 0  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V9.01



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	80	<b>1088</b>	<b>719</b>	1090	717	1086	720	80	874	894	<b>870</b>	<b>898</b>	870	898
401.bzip2	80	1480	522	<b>1480</b>	<b>522</b>	1476	523	80	1341	576	<b>1345</b>	<b>574</b>	1345	574
403.gcc	80	852	756	847	760	<b>849</b>	<b>759</b>	80	852	756	847	760	<b>849</b>	<b>759</b>
429.mcf	80	770	948	765	953	<b>768</b>	<b>950</b>	80	717	1020	<b>716</b>	<b>1020</b>	716	1020
445.gobmk	80	1020	823	<b>1018</b>	<b>825</b>	1013	828	80	<b>965</b>	<b>870</b>	965	870	968	867
456.hammer	80	667	1120	<b>663</b>	<b>1120</b>	663	1130	80	501	1490	503	1490	<b>502</b>	<b>1490</b>
458.sjeng	80	1243	779	1239	781	<b>1242</b>	<b>779</b>	80	1142	848	1143	847	<b>1142</b>	<b>847</b>
462.libquantum	80	394	4210	391	4240	<b>393</b>	<b>4220</b>	80	394	4210	391	4240	<b>393</b>	<b>4220</b>
464.h264ref	80	<b>1590</b>	<b>1110</b>	1591	1110	1586	1120	80	<b>1568</b>	<b>1130</b>	1568	1130	1574	1120
471.omnetpp	80	922	542	926	540	<b>924</b>	<b>541</b>	80	<b>863</b>	<b>579</b>	864	579	863	579
473.astar	80	1059	530	1057	531	<b>1058</b>	<b>531</b>	80	1059	530	1057	531	<b>1058</b>	<b>531</b>
483.xalancbmk	80	644	858	<b>644</b>	<b>857</b>	645	856	80	644	858	<b>644</b>	<b>857</b>	645	856

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
echo 1 > /proc/sys/vm/zone_reclaim_mode
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 52000 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

BIOS Settings:  
Turbo Boost Power Optimization set to Traditional

## General Notes

Binaries were compiled on RHEL5.5



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## Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/smartheap -lsmartheap  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

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## Peak Compiler Invocation (Continued)

C++ benchmarks:  
icpc -m32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4 -auto-ilp32  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll2 -ansi-alias

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## Peak Optimization Flags (Continued)

C++ benchmarks:

```
471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
             -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
             -L/smartheap -lsmartheap
```

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>  
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>  
<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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