



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint®_rate2006 = 974

PowerEdge R910 (Intel Xeon E7-8867L, 2.13 GHz)

SPECint_rate_base2006 = 906

CPU2006 license: 55

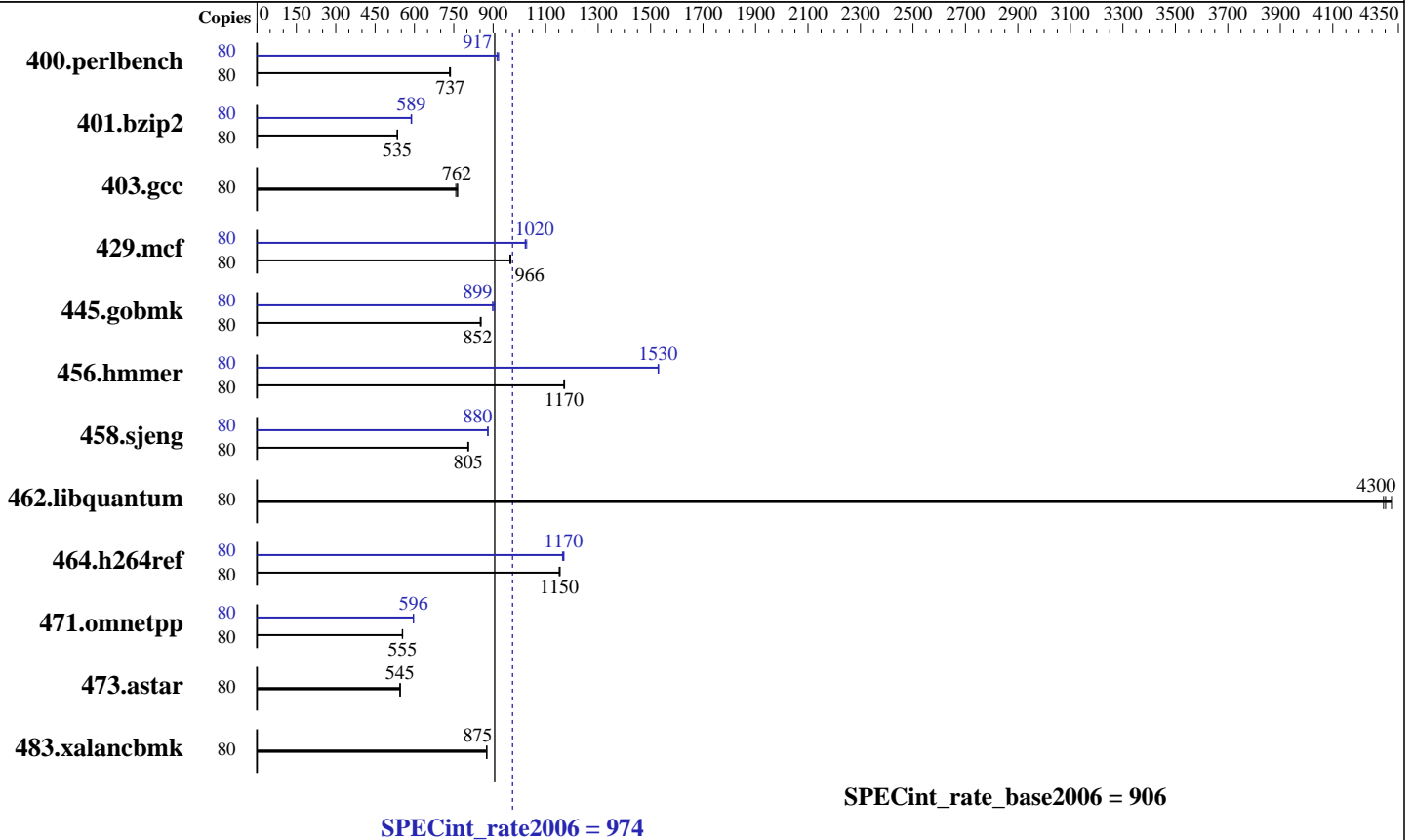
Test date: Mar-2011

Test sponsor: Dell Inc.

Hardware Availability: Apr-2011

Tested by: Dell Inc.

Software Availability: Apr-2011



Hardware

CPU Name: Intel Xeon E7-8867L
 CPU Characteristics: Intel Turbo Boost Technology up to 2.53 GHz
 CPU MHz: 2133
 FPU: Integrated
 CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip, 2 threads/core
 CPU(s) orderable: 2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 30 MB I+D on chip per chip
 Other Cache: None
 Memory: 512 GB (64 x 8 GB 4Rx8 PC3-8500R-7, ECC)
 Disk Subsystem: 1 x 500 GB 7200 RPM SAS 6Gb
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86_64), Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ Compiler XE for applications running on IA-32 Version 12.0.1.116 Build 20101116
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V9.01



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = 974

PowerEdge R910 (Intel Xeon E7-8867L, 2.13 GHz)

SPECint_rate_base2006 = 906

CPU2006 license: 55

Test date: Mar-2011

Test sponsor: Dell Inc.

Hardware Availability: Apr-2011

Tested by: Dell Inc.

Software Availability: Apr-2011

Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	80	<u>1061</u>	<u>737</u>	1066	734	1060	737	80	<u>852</u>	<u>917</u>	849	921	854	915
401.bzip2	80	<u>1443</u>	<u>535</u>	1441	536	1445	534	80	1310	589	1315	587	<u>1312</u>	<u>589</u>
403.gcc	80	850	758	842	765	<u>845</u>	<u>762</u>	80	850	758	842	765	<u>845</u>	<u>762</u>
429.mcf	80	<u>756</u>	<u>966</u>	757	964	754	967	80	<u>713</u>	<u>1020</u>	710	1030	714	1020
445.gobmk	80	982	854	986	851	<u>985</u>	<u>852</u>	80	934	898	<u>933</u>	<u>899</u>	927	906
456.hammer	80	638	1170	637	1170	<u>637</u>	<u>1170</u>	80	488	1530	<u>488</u>	<u>1530</u>	488	1530
458.sjeng	80	1205	803	1200	806	<u>1202</u>	<u>805</u>	80	<u>1100</u>	<u>880</u>	1100	880	1100	880
462.libquantum	80	386	4290	<u>385</u>	<u>4300</u>	383	4320	80	386	4290	<u>385</u>	<u>4300</u>	383	4320
464.h264ref	80	1533	1160	<u>1536</u>	<u>1150</u>	1537	1150	80	1513	1170	1521	1160	<u>1516</u>	<u>1170</u>
471.omnetpp	80	901	555	903	554	<u>901</u>	<u>555</u>	80	838	597	839	596	<u>839</u>	<u>596</u>
473.astar	80	<u>1030</u>	<u>545</u>	1031	545	1028	546	80	<u>1030</u>	<u>545</u>	1031	545	1028	546
483.xalancbmk	80	630	876	<u>631</u>	<u>875</u>	631	875	80	630	876	<u>631</u>	<u>875</u>	631	875

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 72000 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

Platform Notes

BIOS Settings:
Power Management = Maximum Performance (Default = Active Power Controller)

General Notes

The Dell PowerEdge R910 and the Bull NovaScale R480 F2 models are electronically equivalent. The results have been measured on a Dell PowerEdge R910 model. Binaries were compiled on RHEL5.5



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = 974

PowerEdge R910 (Intel Xeon E7-8867L, 2.13 GHz)

SPECint_rate_base2006 = 906

CPU2006 license: 55

Test date: Mar-2011

Test sponsor: Dell Inc.

Hardware Availability: Apr-2011

Tested by: Dell Inc.

Software Availability: Apr-2011

Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/smartheap -lsmartheap
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = 974

PowerEdge R910 (Intel Xeon E7-8867L, 2.13 GHz)

SPECint_rate_base2006 = 906

CPU2006 license: 55

Test date: Mar-2011

Test sponsor: Dell Inc.

Hardware Availability: Apr-2011

Tested by: Dell Inc.

Software Availability: Apr-2011

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32 -ansi-alias
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -auto-ilp32

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias -auto-ilp32

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4 -auto-ilp32
-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Dell Inc.

SPECint_rate2006 = 974

PowerEdge R910 (Intel Xeon E7-8867L, 2.13 GHz)

SPECint_rate_base2006 = 906

CPU2006 license: 55

Test date: Mar-2011

Test sponsor: Dell Inc.

Hardware Availability: Apr-2011

Tested by: Dell Inc.

Software Availability: Apr-2011

Peak Optimization Flags (Continued)

C++ benchmarks:

```
471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
             -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
             -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
             -L/smartheap -lsmartheap
```

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110308.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>
<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110308.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 19:27:37 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 April 2011.