



# SPEC® CINT2006 Result

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## IBM Corporation

SPECint®2006 = **Not Run**

## IBM BladeCenter HS22 (Intel Xeon X5650)

SPECint\_base2006 = **31.9**

CPU2006 license: 11

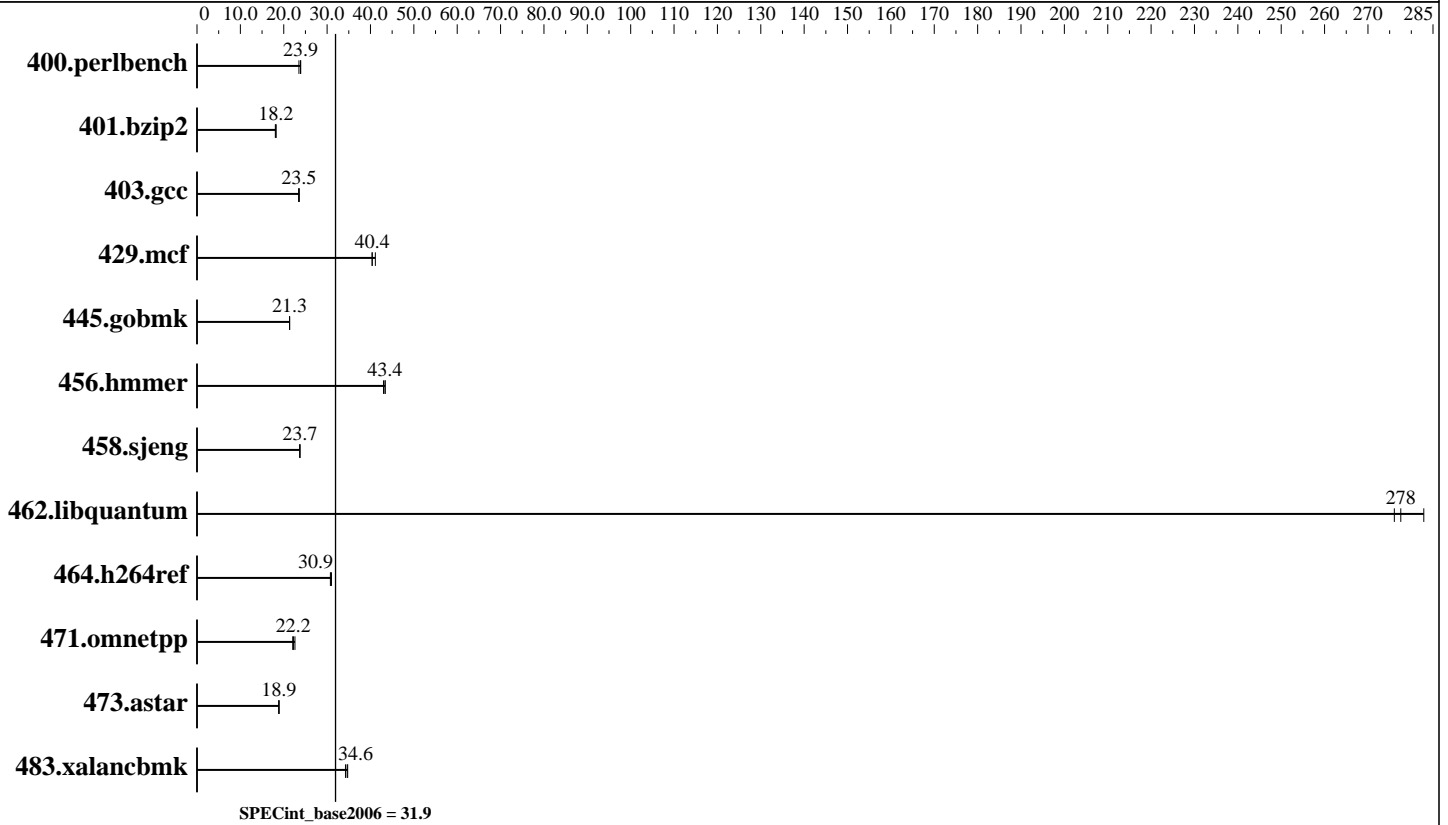
Test sponsor: IBM Corporation

Tested by: IBM Corporation

Test date: Dec-2010

Hardware Availability: Mar-2010

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon X5650  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 12 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (4 x 4 GB 2Rx8 PC3-10600R-9, ECC)  
 Disk Subsystem: 2 x 300 GB SAS, 10000 RPM, RAID 1  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	416	23.5	<b>409</b>	<b>23.9</b>	409	23.9						
401.bzip2	530	18.2	<b>530</b>	<b>18.2</b>	534	18.1						
403.gcc	341	23.6	<b>342</b>	<b>23.5</b>	343	23.4						
429.mcf	222	41.1	<b>226</b>	<b>40.4</b>	226	40.4						
445.gobmk	491	21.4	<b>492</b>	<b>21.3</b>	492	21.3						
456.hmmr	217	43.0	215	43.4	<b>215</b>	<b>43.4</b>						
458.sjeng	<b>510</b>	<b>23.7</b>	510	23.7	510	23.7						
462.libquantum	73.2	283	<b>74.6</b>	<b>278</b>	75.0	276						
464.h264ref	714	31.0	720	30.8	<b>717</b>	<b>30.9</b>						
471.omnetpp	<b>281</b>	<b>22.2</b>	277	22.6	283	22.1						
473.astar	<b>372</b>	<b>18.9</b>	371	18.9	372	18.9						
483.xalancbmk	199	34.7	<b>199</b>	<b>34.6</b>	202	34.2						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Platform Notes

Turbo Mode enabled in BIOS  
Turbo Boost set to Traditional in BIOS  
Power C-states enabled in BIOS  
Demand Scrub disabled in BIOS

## General Notes

'ulimit -s unlimited' was used to set the stack size prior to run  
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502  
OMP\_NUM\_THREADS set to number of cores  
KMP\_AFFINITY set to granularity=fine,scatter

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64



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## Base Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100929.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100929.00.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 13:47:37 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 21 December 2010.