



# SPEC<sup>®</sup> CINT2006 Result

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## Fujitsu

SPECint<sup>®</sup>\_rate2006 = 79.2

PRIMERGY RX200 S6, Intel Xeon E5507, 2.27 GHz

SPECint\_rate\_base2006 = 74.1

CPU2006 license: 19

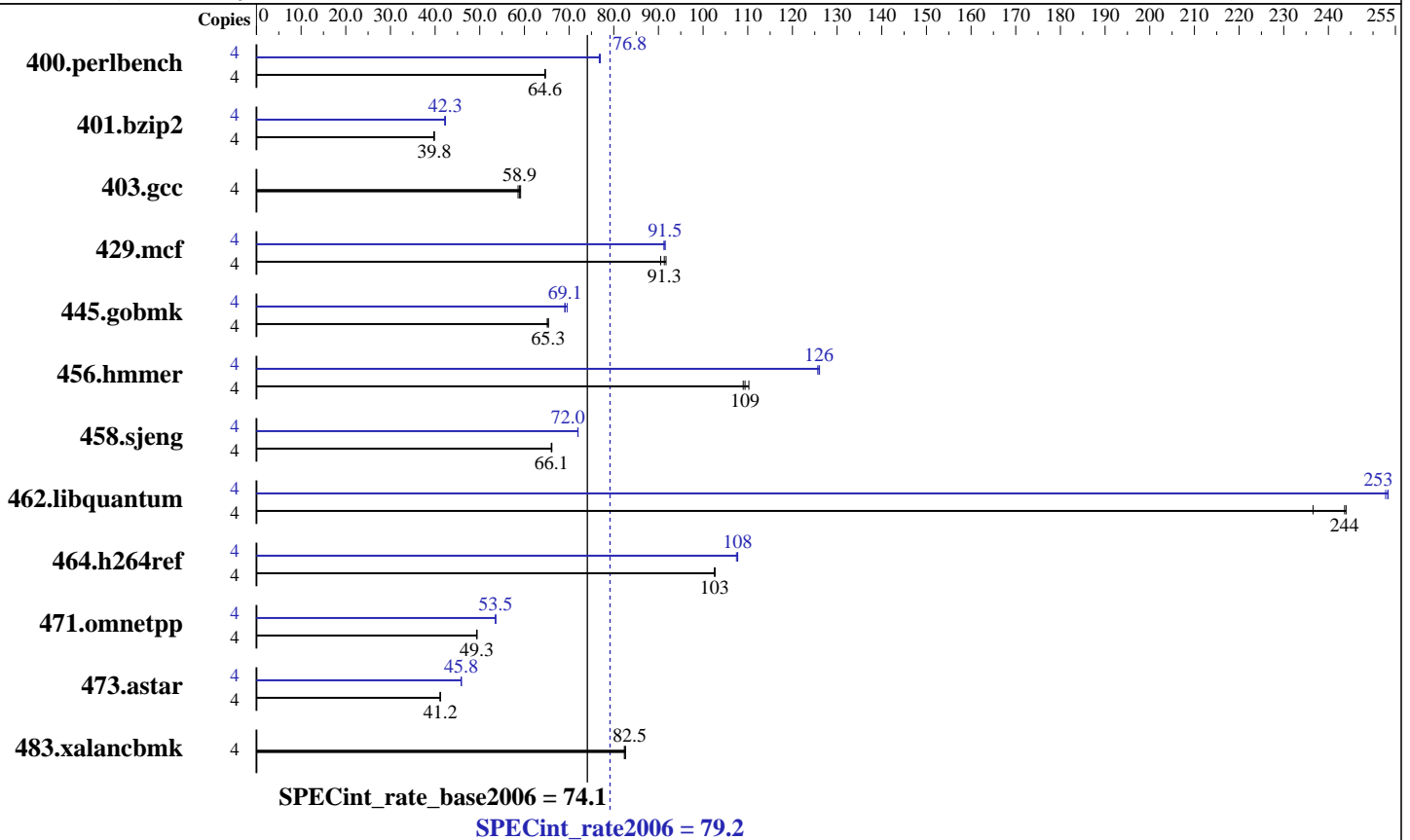
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: May-2010

Hardware Availability: May-2010

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon E5507  
 CPU Characteristics: 2267  
 CPU MHz: 2267  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 4 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)  
 Disk Subsystem: 1 x SATA, 160 GB, 5400 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	605	64.6	<b>605</b>	<b>64.6</b>	604	64.7	4	509	76.8	508	77.0	<b>509</b>	<b>76.8</b>
401.bzip2	4	<b>970</b>	<b>39.8</b>	972	39.7	970	39.8	4	912	42.3	916	42.1	<b>913</b>	<b>42.3</b>
403.gcc	4	<b>547</b>	<b>58.9</b>	550	58.6	544	59.1	4	<b>547</b>	<b>58.9</b>	550	58.6	544	59.1
429.mcf	4	403	90.5	<b>400</b>	<b>91.3</b>	398	91.7	4	<b>399</b>	<b>91.5</b>	400	91.2	399	91.5
445.gobmk	4	645	65.1	<b>642</b>	<b>65.3</b>	642	65.4	4	603	69.6	608	69.0	<b>607</b>	<b>69.1</b>
456.hammer	4	<b>341</b>	<b>109</b>	342	109	339	110	4	<b>296</b>	<b>126</b>	296	126	297	126
458.sjeng	4	732	66.1	<b>733</b>	<b>66.1</b>	733	66.0	4	672	72.0	673	72.0	<b>672</b>	<b>72.0</b>
462.libquantum	4	<b>340</b>	<b>244</b>	350	237	340	244	4	<b>327</b>	<b>253</b>	327	253	328	253
464.h264ref	4	863	103	862	103	<b>863</b>	<b>103</b>	4	<b>822</b>	<b>108</b>	824	107	821	108
471.omnetpp	4	506	49.4	507	49.3	<b>507</b>	<b>49.3</b>	4	<b>467</b>	<b>53.5</b>	467	53.6	467	53.5
473.astar	4	682	41.2	<b>682</b>	<b>41.2</b>	683	41.1	4	612	45.9	613	45.8	<b>612</b>	<b>45.8</b>
483.xalancbmk	4	334	82.6	335	82.4	<b>334</b>	<b>82.5</b>	4	334	82.6	335	82.4	<b>334</b>	<b>82.5</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

The system automatically configures the memory to run at 800 MHz.  
BIOS configuration:  
Data Reuse Optimization = Disable

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>  
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libic11.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

456.hmmcr: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias  
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32  
403.gcc: basepeak = yes  
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch  
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias  
456.hmmcr: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32  
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32  
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-prefetch  
464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap  
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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## Peak Optimization Flags (Continued)

473.astar (continued):

-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.01.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

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