



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

NEC Corporation

Express5800/E110b-1
(Intel Celeron G1101)

SPECint®2006 = 20.1

SPECint_base2006 = 18.7

CPU2006 license: 9006

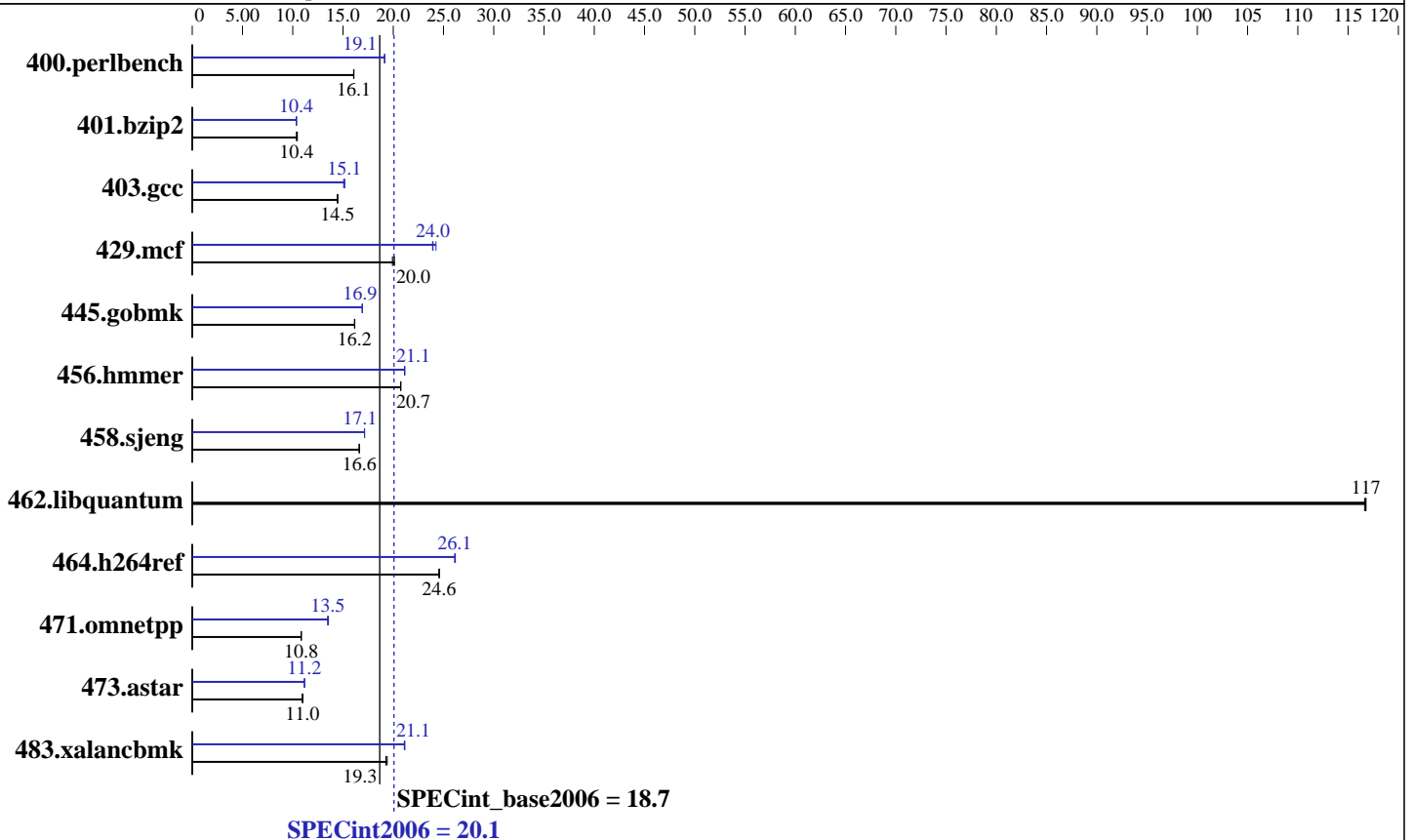
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jun-2010

Hardware Availability: Apr-2010

Software Availability: Dec-2009



Hardware

CPU Name: Intel Celeron G1101
 CPU Characteristics:
 CPU MHz: 2267
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 2 MB I+D on chip per chip
 Other Cache: None
 Memory: 8 GB (2 x 4 GB PC3-10600E, 2 rank, CL9, ECC, running at 1066 MHz)
 Disk Subsystem: 1x160 GB SATA, 7200 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1
 Build 20091130 Package ID: l_cproc_p_11.1.064
 Auto Parallel: Yes
 File System: ext3
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	607	16.1	608	16.1	608	16.1	510	19.1	510	19.2	511	19.1
401.bzip2	923	10.5	929	10.4	930	10.4	931	10.4	931	10.4	931	10.4
403.gcc	556	14.5	556	14.5	557	14.4	532	15.1	531	15.2	534	15.1
429.mcf	453	20.1	458	19.9	456	20.0	381	23.9	381	24.0	376	24.3
445.gobmk	649	16.2	649	16.2	650	16.1	621	16.9	619	16.9	620	16.9
456.hammer	449	20.8	450	20.7	450	20.7	442	21.1	442	21.1	441	21.2
458.sjeng	728	16.6	728	16.6	728	16.6	706	17.1	706	17.1	706	17.1
462.libquantum	177	117	178	117	177	117	177	117	178	117	177	117
464.h264ref	901	24.6	902	24.5	899	24.6	847	26.1	845	26.2	847	26.1
471.omnetpp	576	10.9	577	10.8	576	10.8	461	13.6	463	13.5	464	13.5
473.astar	638	11.0	639	11.0	642	10.9	629	11.2	629	11.2	629	11.2
483.xalancbmk	358	19.3	356	19.4	358	19.3	326	21.2	327	21.1	327	21.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

Default BIOS settings were used.

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64

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Base Portability Flags (Continued)

```

401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

```

Base Optimization Flags

C benchmarks:

```
-xSSSE3 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

C++ benchmarks:

```
-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib64 -lsmartheap64
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m64
```

```
429.mcf: icc -m32
```

C++ benchmarks (except as noted below):

```
icpc -m32
```

```
473.astar: icpc -m64
```



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Peak Portability Flags

```

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

```

Peak Optimization Flags

C benchmarks:

```

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
               -no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
           -no-prec-div -static -auto-ilp32 -opt-prefetch
           -ansi-alias

403.gcc: -xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc
         -opt-malloc-options=3

429.mcf: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -O2 -ipo
           -no-prec-div -ansi-alias

456.hmmmer: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2
           -ansi-alias -auto-ilp32

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
           -no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
           -no-prec-div -static -unroll2 -ansi-alias

```

C++ benchmarks:

```

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
            -no-prec-div -ansi-alias -opt-ra-region-strategy=block
            -Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

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Peak Optimization Flags (Continued)

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib64 -lsmarheap64

483.xalancbmk: -xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100609.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100609.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 13:19:22 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 8 July 2010.