



# SPEC<sup>®</sup> CINT2006 Result

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## Cisco Systems

SPECint<sup>®</sup>\_rate2006 = 771

Cisco UCS B440 M1 (Intel Xeon X7560, 2.27 GHz)

SPECint\_rate\_base2006 = 720

CPU2006 license: 9019

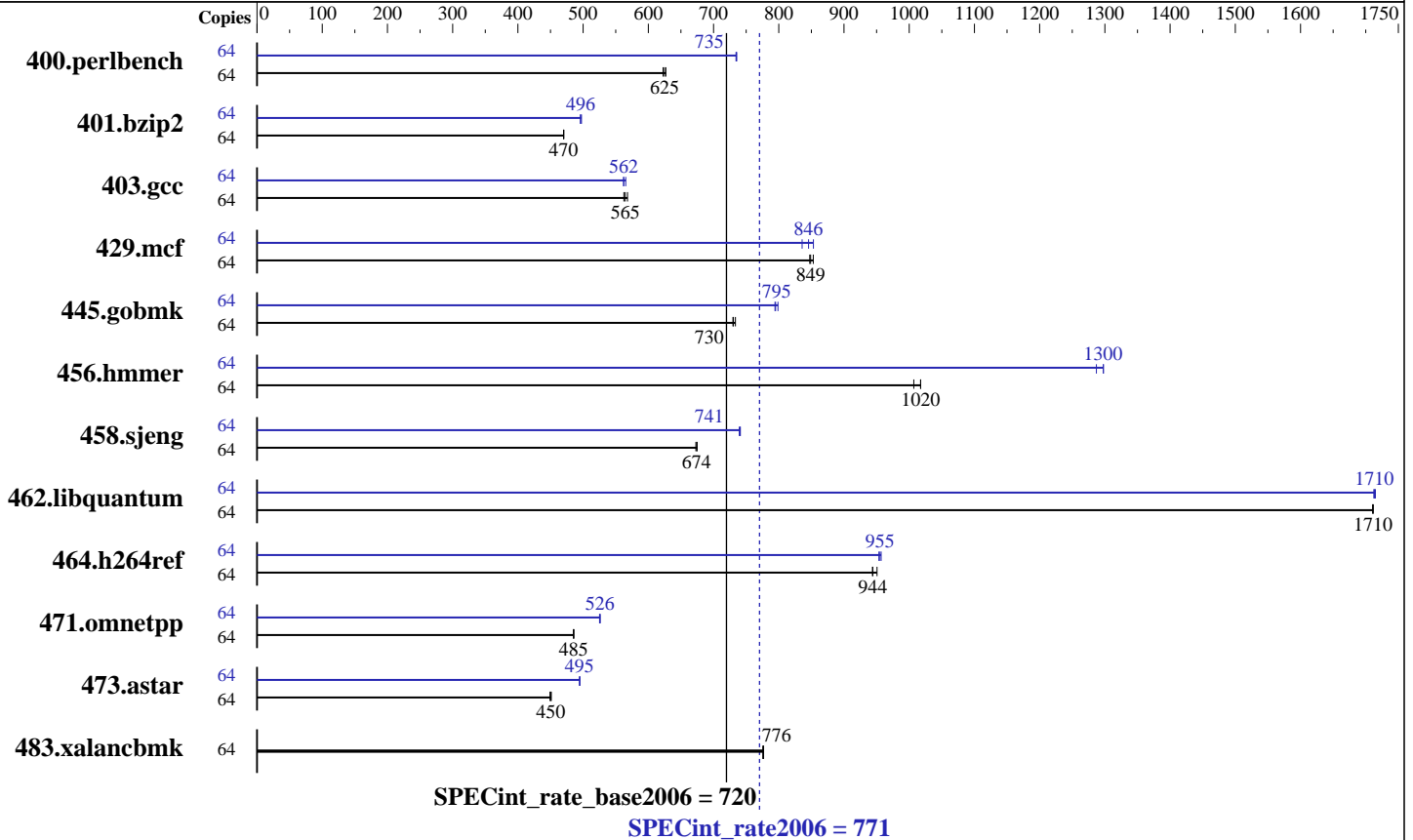
Test date: May-2010

Test sponsor: Cisco Systems

Hardware Availability: Jun-2010

Tested by: Cisco Systems

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon X7560  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz  
 CPU MHz: 2266  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 4 chips, 8 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2,3,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 24 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (32x8GB, PC3-8500R, CL9, Quad Rank, ECC)  
 Disk Subsystem: 146 GB SAS, 15K RPM  
 Other Hardware: None

### Software

Operating System: SuSe Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27-19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap Library V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	64	998	627	1004	623	<u>1001</u>	<u>625</u>	64	851	735	850	736	<u>851</u>	<u>735</u>
401.bzip2	64	1314	470	1313	470	<u>1314</u>	<u>470</u>	64	<u>1244</u>	<u>496</u>	1246	496	1241	498
403.gcc	64	916	562	<u>913</u>	<u>565</u>	907	568	64	<u>916</u>	<u>562</u>	911	565	917	562
429.mcf	64	684	853	689	847	<u>688</u>	<u>849</u>	64	<u>690</u>	<u>846</u>	684	853	698	836
445.gobmk	64	915	734	<u>919</u>	<u>730</u>	920	730	64	845	794	<u>844</u>	<u>795</u>	841	799
456.hammer	64	<u>587</u>	<u>1020</u>	587	1020	593	1010	64	<u>460</u>	<u>1300</u>	464	1290	460	1300
458.sjeng	64	<u>1149</u>	<u>674</u>	1148	675	1151	673	64	1047	739	1045	741	<u>1046</u>	<u>741</u>
462.libquantum	64	775	1710	<u>775</u>	<u>1710</u>	775	1710	64	<u>774</u>	<u>1710</u>	773	1710	774	1710
464.h264ref	64	<u>1500</u>	<u>944</u>	1490	951	1501	944	64	1485	954	<u>1483</u>	<u>955</u>	1480	957
471.omnetpp	64	823	486	<u>824</u>	<u>485</u>	824	485	64	762	525	<u>761</u>	<u>526</u>	760	526
473.astar	64	<u>998</u>	<u>450</u>	1000	449	996	451	64	908	495	908	495	<u>908</u>	<u>495</u>
483.xalancbmk	64	569	776	<u>569</u>	<u>776</u>	569	777	64	569	776	<u>569</u>	<u>776</u>	569	777

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Invocation Notes

Binaries compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

ulimit -s unlimited was used to set the stacksize to unlimited prior to run

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

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## Base Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmarheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
 -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmarheap64

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## Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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