



SPEC® CINT2006 Result

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Hewlett-Packard Company

ProLiant ML370 G6
(2.4 GHz, Intel Xeon E5530)

SPECint_rate2006 = 206

SPECint_rate_base2006 = 192

CPU2006 license: 3

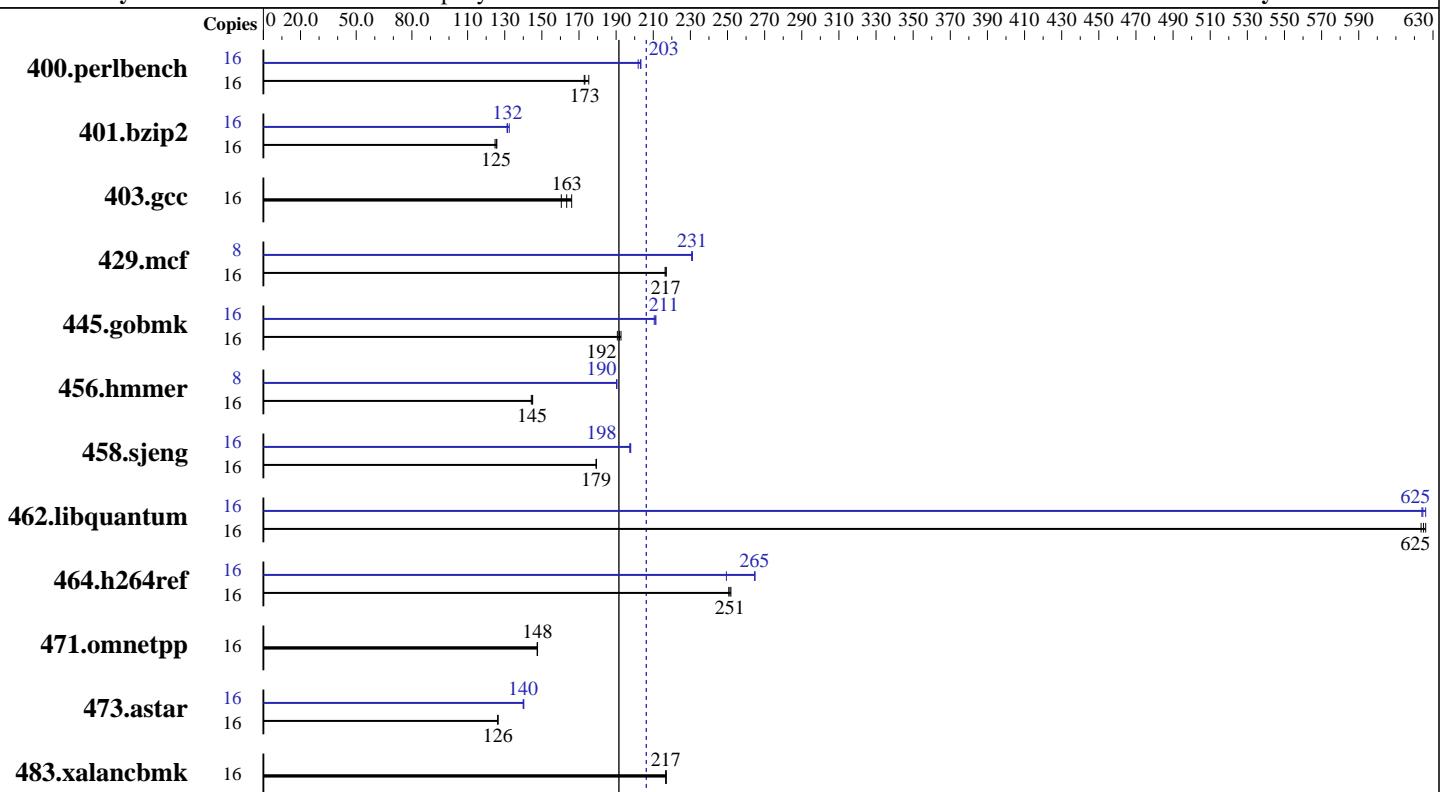
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Jun-2009

Hardware Availability: Mar-2009

Software Availability: Feb-2009



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Hardware

CPU Name: Intel Xeon E5530
CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6x4 GB PC3-10600R CL9)
Disk Subsystem: 1x146 GB 10 K RPM SAS
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 5.3, Advanced Platform, Kernel 2.6.18-128.el5
Compiler: Intel C++ Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080
Auto Parallel: No
File System: ext3
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	904	173	892	175	903	173	16	774	202	770	203	769	203
401.bzip2	16	1237	125	1228	126	1231	125	16	1174	132	1176	131	1166	132
403.gcc	16	776	166	788	163	803	160	16	776	166	788	163	803	160
429.mcf	16	673	217	674	216	672	217	8	316	231	316	231	316	231
445.gobmk	16	871	193	880	191	875	192	16	795	211	794	211	797	211
456.hammer	16	1034	144	1029	145	1031	145	8	392	190	392	190	392	190
458.sjeng	16	1079	179	1080	179	1080	179	16	978	198	980	198	981	197
462.libquantum	16	532	624	531	625	530	626	16	531	625	530	626	531	624
464.h264ref	16	1406	252	1409	251	1413	251	16	1338	265	1337	265	1420	249
471.omnetpp	16	678	148	677	148	678	147	16	678	148	677	148	678	147
473.astar	16	889	126	890	126	889	126	16	801	140	802	140	802	140
483.xalancbmk	16	509	217	509	217	509	217	16	509	217	509	217	509	217

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Platform Notes

BIOS configuration:

HP Power Regulator set to Static High Performance Mode
HP Power Profile set to Maximum Performance
Thermal Configuration set to Increased Cooling

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

462.libquantum: -DSPEC_CPU_LINUX

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Base Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc

458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc

C++ benchmarks (except as noted below):

icpc

473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

456.hmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll12
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll14 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -auto-ilp32
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap64

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=__alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090710.html>
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/HP-Intel-Linux-Settings-flags.20090710.xml>
<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090901.00.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.

Report generated on Wed Jul 23 02:10:15 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 1 September 2009.