SPEC® CINT2006 Result

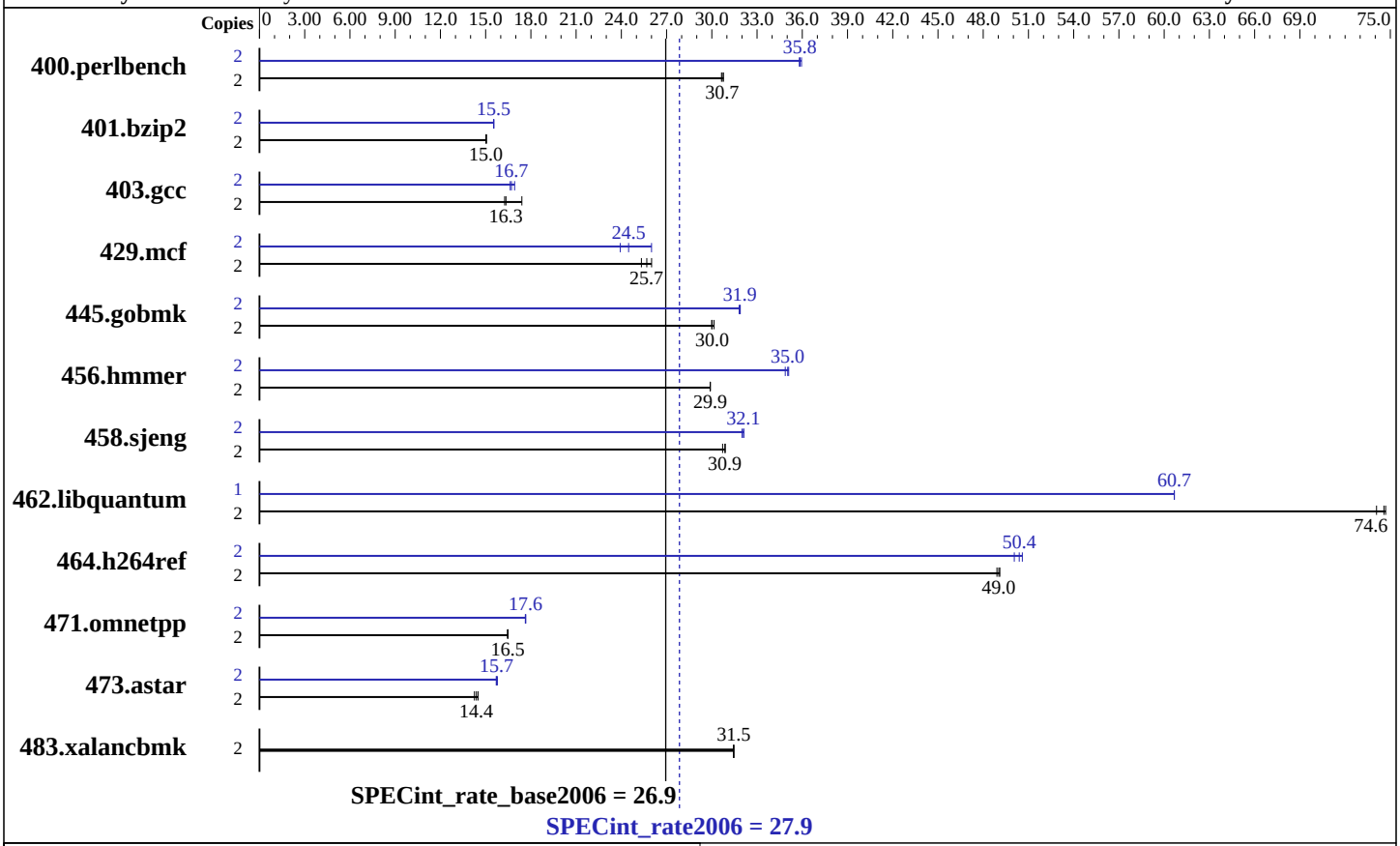
NTT System S. A.
NTT Business W 907G

SPECint®_rate2006 = 27.9

SPECint_rate_base2006 = 26.9

CPU2006 license: 9013
Test sponsor: NTT System S. A.
Tested by: NTT System S. A.

Test date: Jul-2009
Hardware Availability: Jul-2009
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Celeron E3200
CPU Characteristics:	
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB) DDR2 800Mhz
Disk Subsystem:	250 GB SATA, 7200RPM
Other Hardware:	None

Software

Operating System:	SuSe Linux SLES10 SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.066
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

SPEC CINT2006 Result

NTT System S. A.
NTT Business W 907G

SPECint_rate2006 = 27.9

SPECint_rate_base2006 = 26.9

CPU2006 license: 9013
Test sponsor: NTT System S. A.
Tested by: NTT System S. A.

Test date: Jul-2009
Hardware Availability: Jul-2009
Software Availability: Nov-2008

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**637**	**30.7**	638	30.6	635	30.8	2	544	35.9	**545**	**35.8**	546	35.8
401.bzip2	2	**1284**	**15.0**	1285	15.0	1282	15.1	2	1243	15.5	**1242**	**15.5**	1241	15.5
403.gcc	2	990	16.3	**985**	**16.3**	925	17.4	2	968	16.6	**963**	**16.7**	951	16.9
429.mcf	2	720	25.3	**710**	**25.7**	701	26.0	2	**745**	**24.5**	701	26.0	762	23.9
445.gobmk	2	700	30.0	**699**	**30.0**	696	30.1	2	658	31.9	660	31.8	**658**	**31.9**
456.hmmer	2	624	29.9	**624**	**29.9**	624	29.9	2	532	35.1	535	34.9	**533**	**35.0**
458.sjeng	2	**784**	**30.9**	788	30.7	783	30.9	2	**754**	**32.1**	756	32.0	753	32.1
462.libquantum	2	**556**	**74.6**	555	74.7	559	74.1	1	**342**	**60.7**	342	60.7	342	60.7
464.h264ref	2	905	48.9	**903**	**49.0**	901	49.1	2	875	50.6	884	50.1	**878**	**50.4**
471.omnetpp	2	760	16.5	**759**	**16.5**	758	16.5	2	709	17.6	708	17.7	**708**	**17.6**
473.astar	2	969	14.5	**976**	**14.4**	985	14.3	2	894	15.7	890	15.8	**893**	**15.7**
483.xalancbmk	2	439	31.5	439	31.4	**439**	**31.5**	2	439	31.5	439	31.4	**439**	**31.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

General Notes

```
numactl was used to bind processes to cores
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
`-xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3 -opt-prefetch`

C++ benchmarks:
`-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

401.bzip2: `/opt/intel/Compiler/11.0/066/bin/intel64/icc`

456.hmmer: `/opt/intel/Compiler/11.0/066/bin/intel64/icc`

C++ benchmarks:
`icpc`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSSE3 -ipo -O3 -no-prec-div -static -ansi-alias -opt-prefetch`

Continued on next page

Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -O2 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll4

462.libquantum: -xSSSE3 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -parallel -par-runtime-control -opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revF.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.00.html

You can also download the XML flags sources by saving the following links:

http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revF.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090710.00.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 02:13:56 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 27 August 2009.