



SPEC® CINT2006 Result

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Intel Corporation

SPECint®_rate2006 = 44.7

Intel DQ45CB motherboard (Intel Core 2 Duo E8400)

SPECint_rate_base2006 = 42.5

CPU2006 license: 13

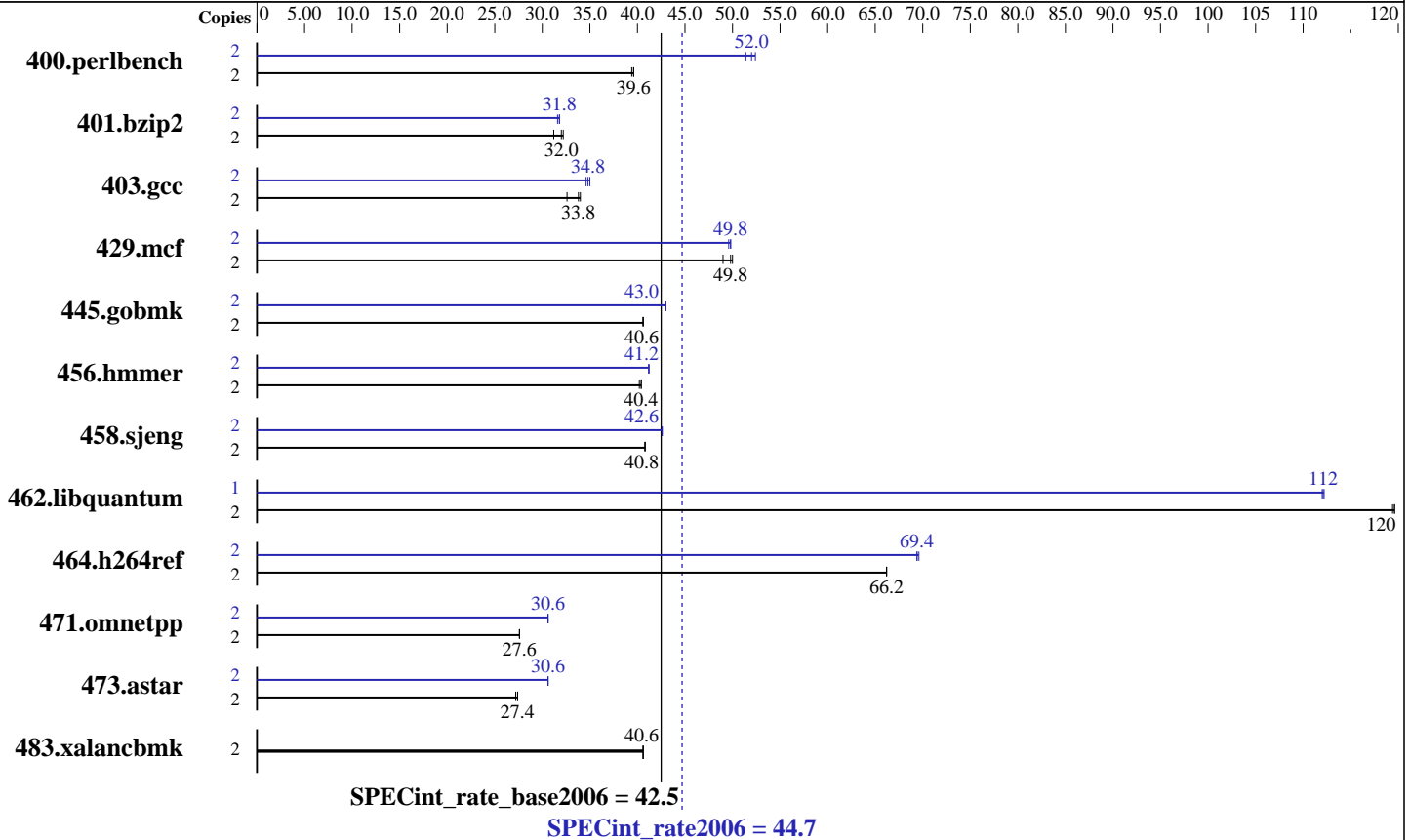
Test date: Apr-2009

Test sponsor: Intel Corporation

Hardware Availability: May-2009

Tested by: Intel Corporation

Software Availability: Nov-2008



Hardware

CPU Name: Intel Core 2 Duo E8400
 CPU Characteristics:
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 4 GB (4x1GB DDR2-800 CL5)
 Disk Subsystem: Seagate 320 GB SATA, 7200RPM
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
 Compiler: Intel C++ Compiler Professional 11.0 for IA32
 Build 20080930 Package ID: w_cproc_p_11.0.054
 Microsoft Visual Studio 2008 (for libraries)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.1 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	496	39.4	493	39.6	<u>493</u>	<u>39.6</u>	2	380	51.4	374	52.4	<u>376</u>	<u>52.0</u>
401.bzip2	2	618	31.2	<u>604</u>	<u>32.0</u>	599	32.2	2	612	31.6	607	31.8	<u>609</u>	<u>31.8</u>
403.gcc	2	495	32.6	<u>477</u>	<u>33.8</u>	474	34.0	2	<u>463</u>	<u>34.8</u>	461	35.0	466	34.6
429.mcf	2	372	49.0	365	50.0	<u>366</u>	<u>49.8</u>	2	<u>367</u>	<u>49.8</u>	366	49.8	367	49.6
445.gobmk	2	517	40.6	<u>516</u>	<u>40.6</u>	516	40.6	2	<u>488</u>	<u>43.0</u>	488	43.0	488	43.0
456.hammer	2	464	40.2	<u>463</u>	<u>40.4</u>	463	40.4	2	<u>453</u>	<u>41.2</u>	454	41.2	453	41.2
458.sjeng	2	<u>594</u>	<u>40.8</u>	594	40.8	594	40.8	2	568	42.6	568	42.6	<u>568</u>	<u>42.6</u>
462.libquantum	2	<u>347</u>	<u>120</u>	347	119	346	120	1	185	112	<u>185</u>	<u>112</u>	185	112
464.h264ref	2	669	66.2	<u>668</u>	<u>66.2</u>	668	66.2	2	637	69.4	<u>637</u>	<u>69.4</u>	637	69.6
471.omnetpp	2	452	27.6	452	27.6	<u>452</u>	<u>27.6</u>	2	<u>409</u>	<u>30.6</u>	409	30.6	409	30.6
473.astar	2	515	27.2	<u>514</u>	<u>27.4</u>	514	27.4	2	459	30.6	<u>459</u>	<u>30.6</u>	459	30.6
483.xalancbmk	2	341	40.6	341	40.6	<u>341</u>	<u>40.6</u>	2	341	40.6	341	40.6	<u>341</u>	<u>40.6</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

General Notes

Tested systems can be used with Shin-G ATX case,
 Antec Truepower Trio power supply TP3-650
 Binaries were built on Windows Vista Ultimate (32-bit)
 Binaries were built on Windows Vista Ultimate (32-bit)
 OMP_NUM_THREADS set to number of logical processors as seen by the OS
 KMP_AFFINITY set to physical,0
 submit disabled for 462.libquantum peak

Base Compiler Invocation

C benchmarks:
 icl -Qvc9 -Qc99

C++ benchmarks:
 icl -Qvc9



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Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

C++ benchmarks:

-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qc99

C++ benchmarks:

icl -Qvc9

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

401.bzp2: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
/F512000000

403.gcc: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
/F512000000

445.gobmk: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias
/F512000000

458.sjeng: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch
-Qparallel -Qpar-runtime-control -Qvec-guard-write
/F512000000

464.h264ref: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=block /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

473.astar: -QxSSE4.1(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias
-Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-win32-revA.20090710.xml>

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For other inquiries, please contact webmaster@spec.org.

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