



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = 45.6

PowerEdge T300 (Intel Xeon E3113, 3.00 GHz)

SPECint_rate_base2006 = 42.5

CPU2006 license: 55

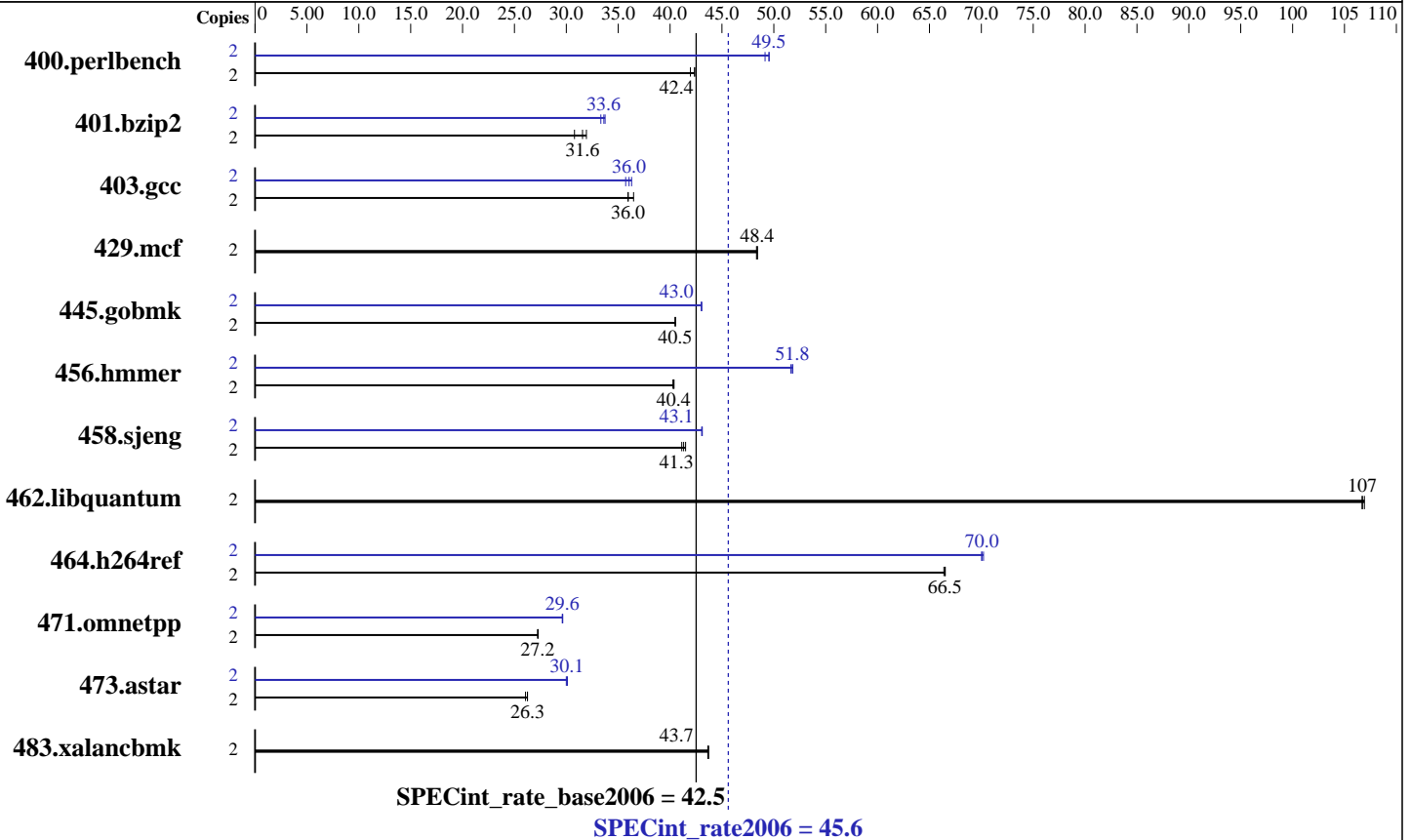
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Oct-2008

Hardware Availability: Mar-2008

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon E3113
 CPU Characteristics:
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4 x 4 GB DDR2-667)
 Disk Subsystem: 1 x 300 GB SAS 15000 RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
 Auto Parallel: No
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	466	42.0	<u>461</u>	<u>42.4</u>	461	42.4	2	394	49.6	<u>395</u>	<u>49.5</u>	398	49.2
401.bzip2	2	604	31.9	<u>612</u>	<u>31.6</u>	627	30.8	2	<u>574</u>	<u>33.6</u>	572	33.7	579	33.3
403.gcc	2	448	35.9	<u>448</u>	<u>36.0</u>	441	36.5	2	444	36.3	451	35.7	<u>447</u>	<u>36.0</u>
429.mcf	2	377	48.3	377	48.4	<u>377</u>	<u>48.4</u>	2	377	48.3	377	48.4	<u>377</u>	<u>48.4</u>
445.gobmk	2	518	40.5	518	40.5	<u>518</u>	<u>40.5</u>	2	<u>488</u>	<u>43.0</u>	487	43.0	488	43.0
456.hammer	2	462	40.4	<u>462</u>	<u>40.4</u>	463	40.3	2	361	51.6	<u>360</u>	<u>51.8</u>	360	51.8
458.sjeng	2	588	41.1	583	41.5	<u>586</u>	<u>41.3</u>	2	562	43.0	561	43.1	<u>562</u>	<u>43.1</u>
462.libquantum	2	388	107	388	107	<u>388</u>	<u>107</u>	2	388	107	388	107	<u>388</u>	<u>107</u>
464.h264ref	2	667	66.4	<u>666</u>	<u>66.5</u>	665	66.5	2	<u>632</u>	<u>70.0</u>	630	70.2	632	70.0
471.omnetpp	2	<u>459</u>	<u>27.2</u>	458	27.3	459	27.2	2	422	29.6	<u>422</u>	<u>29.6</u>	421	29.7
473.astar	2	539	26.1	<u>535</u>	<u>26.3</u>	535	26.3	2	468	30.0	466	30.1	<u>467</u>	<u>30.1</u>
483.xalancbmk	2	<u>316</u>	<u>43.7</u>	316	43.7	316	43.6	2	<u>316</u>	<u>43.7</u>	316	43.7	316	43.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

General Notes

taskset was used to bind processes to cores except for 462.libquantum peak

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

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Base Portability Flags (Continued)

462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalanbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalanbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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