



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®\_rate2006 = Not Run

PowerEdge 1950 III (Intel Xeon X5460, 3.16 GHz)

SPECint\_rate\_base2006 = 135

CPU2006 license: 55

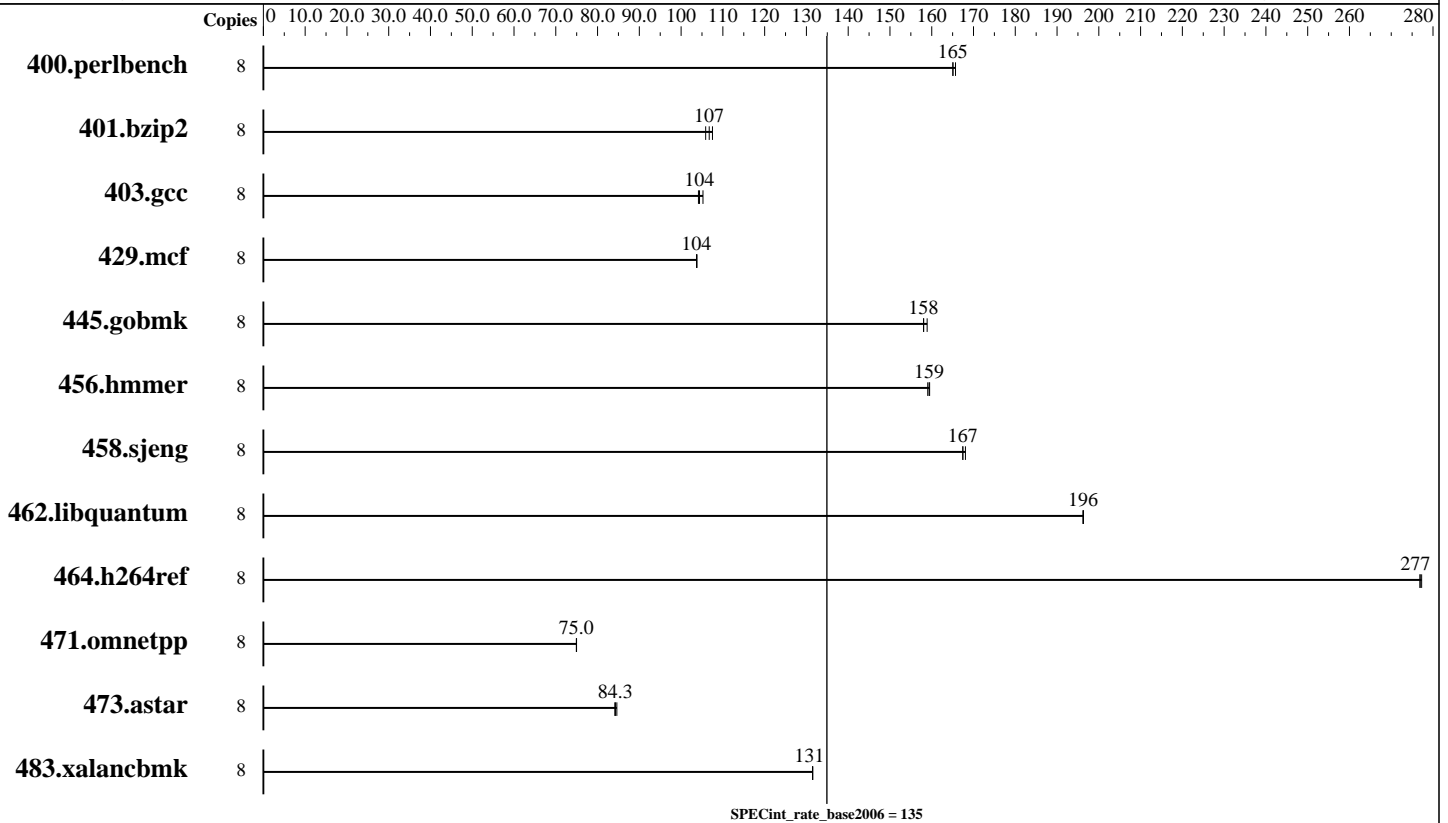
Test date: Sep-2008

Test sponsor: Dell Inc.

Hardware Availability: Sep-2008

Tested by: Dell Inc.

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon X5460  
 CPU Characteristics:  
 CPU MHz: 3166  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2 GB 667 MHz ECC CL5 FB-DIMM)  
 Disk Subsystem: 1 x 73 GB 15000 RPM SAS  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 10 (x86\_64) SP2, Kernel 2.6.16-60.0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l\_cproc\_b\_11.0.042  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	474	165	472	166	<b>473</b>	<b>165</b>							
401.bzip2	8	729	106	<b>723</b>	<b>107</b>	718	108							
403.gcc	8	<b>617</b>	<b>104</b>	618	104	612	105							
429.mcf	8	704	104	<b>703</b>	<b>104</b>	703	104							
445.gobmk	8	528	159	<b>531</b>	<b>158</b>	531	158							
456.hammer	8	<b>468</b>	<b>159</b>	469	159	468	159							
458.sjeng	8	578	167	576	168	<b>578</b>	<b>167</b>							
462.libquantum	8	<b>845</b>	<b>196</b>	845	196	844	196							
464.h264ref	8	638	277	<b>639</b>	<b>277</b>	639	277							
471.omnetpp	8	667	75.0	<b>667</b>	<b>75.0</b>	667	74.9							
473.astar	8	<b>666</b>	<b>84.3</b>	664	84.6	667	84.1							
483.xalancbmk	8	420	132	<b>420</b>	<b>131</b>	420	131							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
taskset was used to bind processes to cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

## Platform Notes

BIOS Settings:  
Hardware Prefetcher = Disabled (Default = Enabled)  
Adjacent Cache Line Prefetch = Disabled (Default = Enabled)

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.1.

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