



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = 148

PowerEdge 1950 III (Intel Xeon X5470, 3.33 GHz)

SPECint_rate_base2006 = 135

CPU2006 license: 55

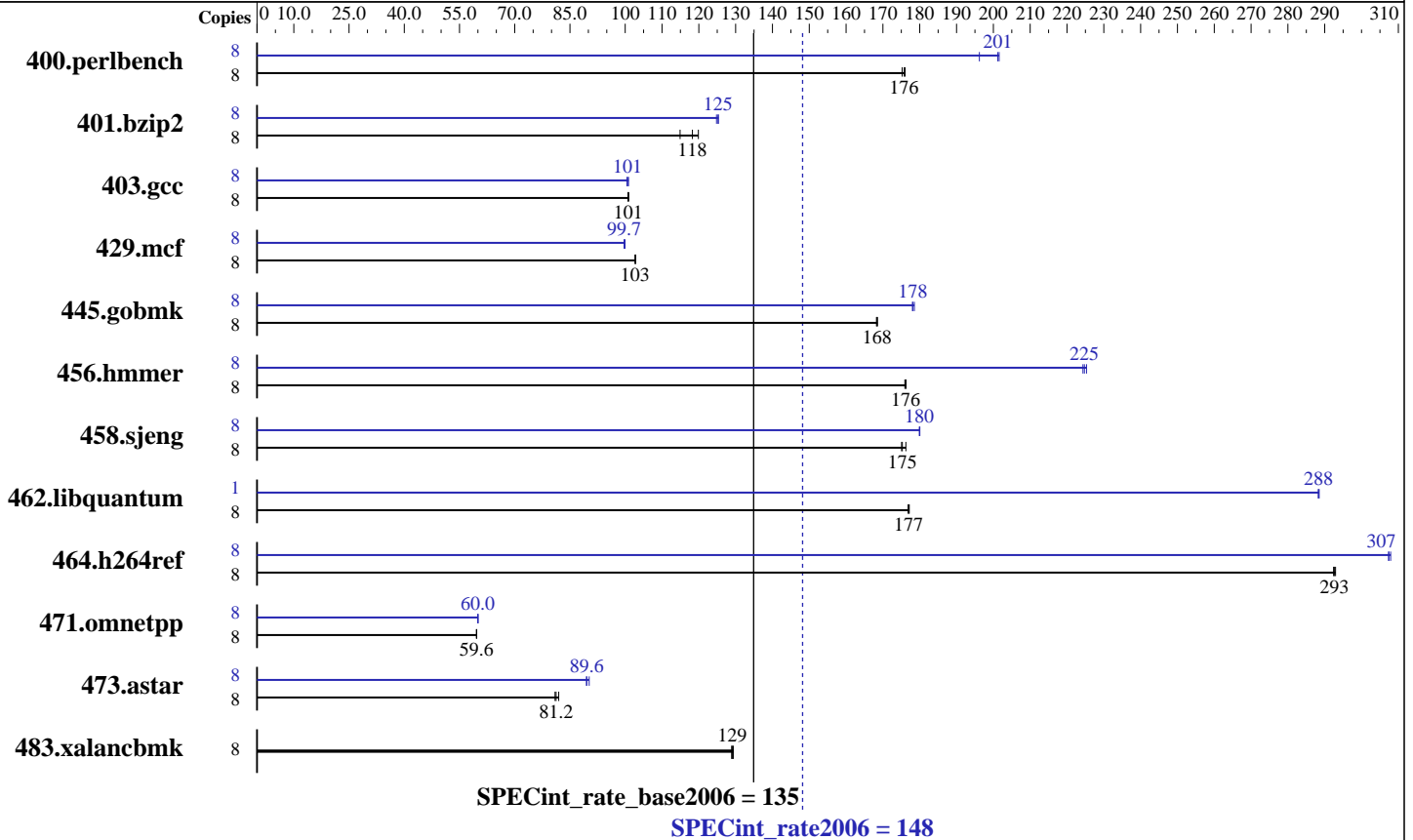
Test date: Sep-2008

Test sponsor: Dell Inc.

Hardware Availability: Sep-2008

Tested by: Dell Inc.

Software Availability: Nov-2008



Hardware

CPU Name: Intel Xeon X5470
 CPU Characteristics: 3333
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2GB 667 MHz ECC CL5 FB-DIMM)
 Disk Subsystem: 1 x 73 GB 15000 RPM SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
 Compiler: Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Binutils 2.18.50.0.7.20080502
 Microquill SmartHeap V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	446	175	444	176	445	176	8	388	201	388	202	398	196
401.bzip2	8	653	118	672	115	644	120	8	616	125	617	125	619	125
403.gcc	8	638	101	639	101	639	101	8	641	100	640	101	638	101
429.mcf	8	709	103	710	103	710	103	8	731	99.7	730	100	731	99.7
445.gobmk	8	498	168	498	169	499	168	8	471	178	471	178	470	179
456.hmmmer	8	423	176	424	176	424	176	8	332	225	331	225	333	224
458.sjeng	8	553	175	549	176	552	175	8	538	180	538	180	538	180
462.libquantum	8	937	177	936	177	937	177	1	71.8	288	71.9	288	71.9	288
464.h264ref	8	604	293	605	292	605	293	8	576	307	575	308	576	307
471.omnetpp	8	839	59.6	838	59.6	838	59.7	8	833	60.0	833	60.0	833	60.0
473.astar	8	694	80.9	691	81.2	685	81.9	8	628	89.5	627	89.6	622	90.2
483.xalancbmk	8	428	129	427	129	427	129	8	428	129	427	129	427	129

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
taskset was used to bind processes to cores except
for 462.libquantum peak

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

Platform Notes

BIOS Settings:
Adjacent Cache Line Prefetch = Disabled (Default = Enabled)

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmmer,
for peak, are compiled in 64-bit mode
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M



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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
-L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
-I/opt/intel/Compiler/11.0/042/ipp/em64t/include

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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias
403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-alloc
-opt-malloc-options=3
429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo
-no-prec-div -ansi-alias
456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias
458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4
462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static
-opt-malloc-options=3 -parallel -par-runtime-control
-opt-prefetch
464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

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Peak Optimization Flags (Continued)

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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