SPEC® CFP2006 Result

Sun Microsystems

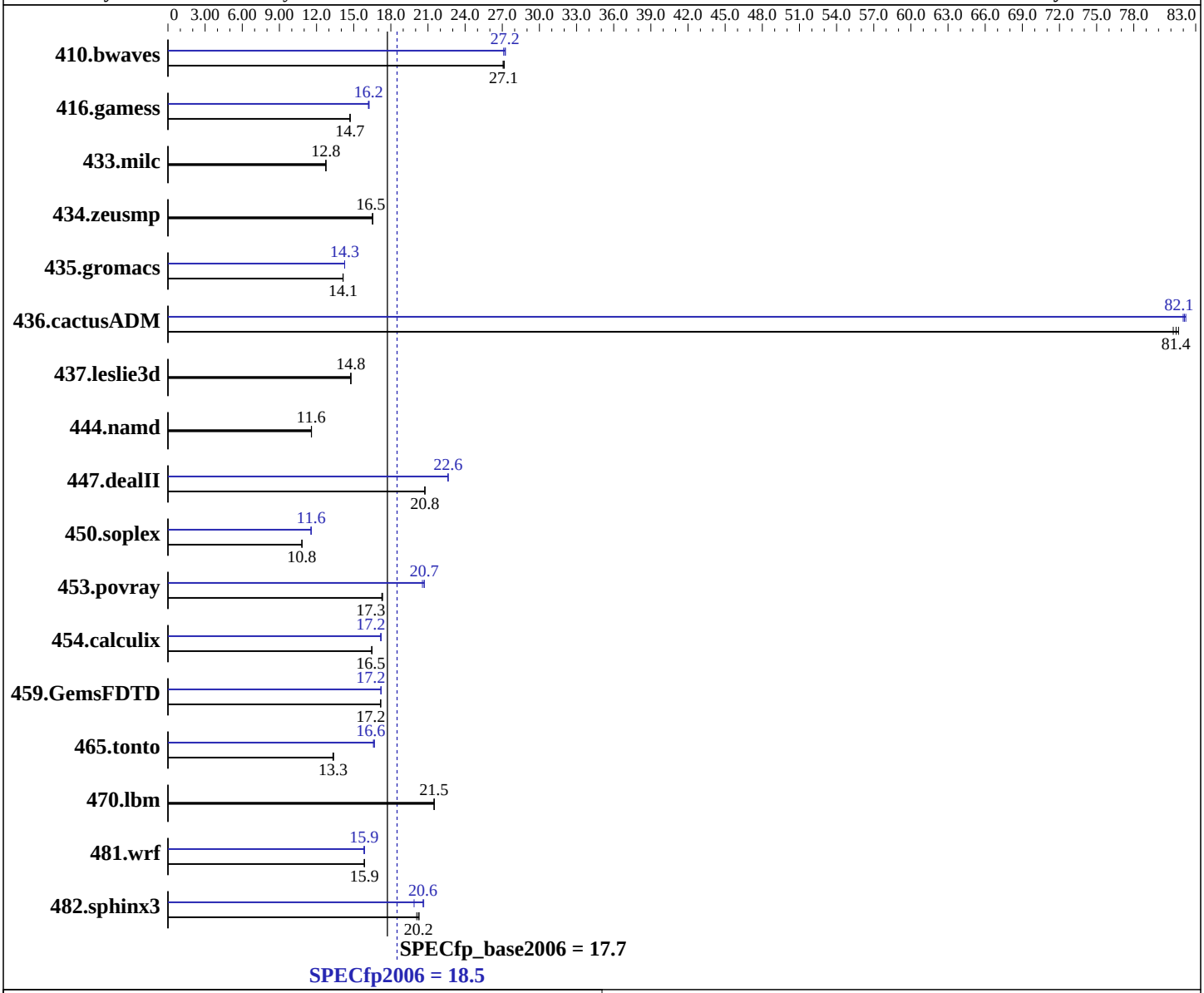
Sun Netra X4250 (Intel Xeon L5408 2.13GHz)

SPECfp®2006 = 18.5

SPECfp_base2006 = 17.7

CPU2006 license: 6
Test sponsor: Sun Microsystems
Tested by: Sun Microsystems

Test date: Sep-2008
Hardware Availability: Aug-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon L5408
CPU Characteristics:	
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	64-bit

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L3 Cache: None
Other Cache: None
Memory: 64 GB (16x4 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: SAS, 146GB, 10K RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	502	27.1	500	27.2	**501**	**27.1**	**499**	**27.2**	501	27.1	499	27.3
416.gamess	1330	14.7	1333	14.7	**1331**	**14.7**	**1208**	**16.2**	1205	16.2	1209	16.2
433.milc	**720**	**12.8**	720	12.7	719	12.8	**720**	**12.8**	720	12.7	719	12.8
434.zeusmp	550	16.5	551	16.5	**550**	**16.5**	550	16.5	551	16.5	**550**	**16.5**
435.gromacs	**505**	**14.1**	505	14.1	505	14.1	500	14.3	**500**	**14.3**	500	14.3
436.cactusADM	146	81.6	**147**	**81.4**	147	81.2	146	82.0	**146**	**82.1**	145	82.2
437.leslie3d	636	14.8	**636**	**14.8**	637	14.8	636	14.8	**636**	**14.8**	637	14.8
444.namd	**692**	**11.6**	692	11.6	692	11.6	**692**	**11.6**	692	11.6	692	11.6
447.dealII	**551**	**20.8**	551	20.7	551	20.8	506	22.6	**506**	**22.6**	506	22.6
450.soplex	770	10.8	**771**	**10.8**	771	10.8	721	11.6	**721**	**11.6**	722	11.5
453.povray	308	17.3	**307**	**17.3**	307	17.3	259	20.6	257	20.7	**257**	**20.7**
454.calculix	501	16.5	501	16.5	**501**	**16.5**	480	17.2	479	17.2	**480**	**17.2**
459.GemsFDTD	618	17.2	618	17.2	**618**	**17.2**	617	17.2	616	17.2	**617**	**17.2**
465.tonto	735	13.4	739	13.3	**737**	**13.3**	590	16.7	593	16.6	**591**	**16.6**
470.lbm	639	21.5	**639**	**21.5**	639	21.5	639	21.5	**639**	**21.5**	639	21.5
481.wrf	705	15.8	**704**	**15.9**	704	15.9	704	15.9	**705**	**15.9**	706	15.8
482.sphinx3	970	20.1	**964**	**20.2**	961	20.3	**946**	**20.6**	981	19.9	944	20.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited
OMP_NUM_THREADS set to number of cores.
KMP_STACKSIZE set to 200M
KMP_AFFINITY set to physical,0
```

Platform Notes

```
BIOS configuration:
Hardware Prefetch = Enable; Adjacent Sector Prefetch = Enable
```

General Notes

```
All benchmarks compiled in 64-bit mode except  450.soplex
and 482.sphinx3, at peak, are compiled in 32-bit mode
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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Base Optimization Flags (Continued)

Fortran benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

482.sphinx3: `/opt/intel/Compiler/11.0/042/bin/ia32/icc -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib -I/opt/intel/Compiler/11.0/042/ipp/ia32/include`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/Compiler/11.0/042/bin/ia32/icpc -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib -I/opt/intel/Compiler/11.0/042/ipp/ia32/include`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32

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Peak Optimization Flags (Continued)

454.calculix: `-xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32`

481.wrf: `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel -auto-ilp32`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.10.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.10.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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