



SPEC® CINT2006 Result

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NEC Corporation

Express5800/i120Ra-e1
(Intel Xeon L5240)

SPECint®_rate2006 = 40.1

SPECint_rate_base2006 = 35.1

CPU2006 license: 9006

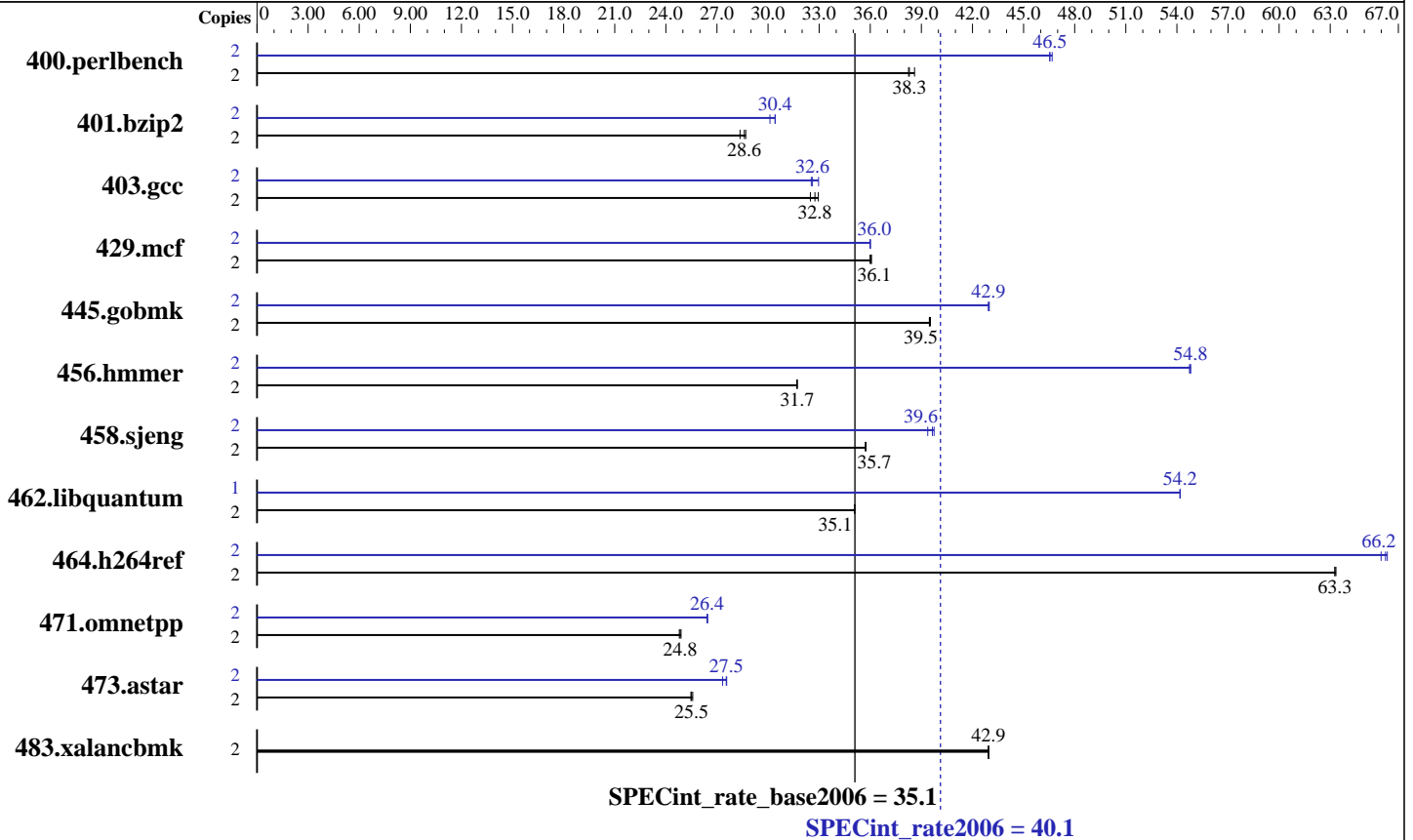
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jul-2008

Hardware Availability: May-2008

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon L5240
 CPU Characteristics: 3.00 GHz, 6 MB L2, 1333 MHz bus
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 6 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (4x4 GB PC2-5300P, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: 1x80.0 GB SATAII, 7200RPM
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: L_cc_p_10.1.008
 Auto Parallel: Yes
 File System: ReiserFS
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



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Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	511	38.2	506	38.6	<u>510</u>	<u>38.3</u>	2	<u>420</u>	<u>46.5</u>	420	46.5	419	46.7
401.bzip2	2	673	28.7	681	28.4	<u>675</u>	<u>28.6</u>	2	<u>635</u>	<u>30.4</u>	635	30.4	641	30.1
403.gcc	2	489	33.0	<u>492</u>	<u>32.8</u>	496	32.5	2	<u>494</u>	<u>32.6</u>	495	32.5	488	33.0
429.mcf	2	506	36.1	<u>506</u>	<u>36.1</u>	507	36.0	2	507	36.0	<u>507</u>	<u>36.0</u>	507	36.0
445.gobmk	2	531	39.5	532	39.5	<u>531</u>	<u>39.5</u>	2	488	43.0	<u>488</u>	<u>42.9</u>	489	42.9
456.hmmer	2	<u>588</u>	<u>31.7</u>	589	31.7	588	31.7	2	340	54.8	341	54.7	<u>341</u>	<u>54.8</u>
458.sjeng	2	677	35.7	678	35.7	<u>677</u>	<u>35.7</u>	2	<u>610</u>	<u>39.6</u>	609	39.7	615	39.4
462.libquantum	2	1182	35.1	1181	35.1	<u>1181</u>	<u>35.1</u>	1	382	54.2	382	54.2	<u>382</u>	<u>54.2</u>
464.h264ref	2	699	63.3	<u>699</u>	<u>63.3</u>	700	63.3	2	<u>668</u>	<u>66.2</u>	671	66.0	667	66.4
471.omnetpp	2	502	24.9	<u>503</u>	<u>24.8</u>	504	24.8	2	<u>473</u>	<u>26.4</u>	472	26.5	473	26.4
473.astar	2	<u>550</u>	<u>25.5</u>	549	25.6	551	25.5	2	514	27.3	509	27.6	<u>510</u>	<u>27.5</u>
483.xalancbmk	2	321	43.0	321	42.9	<u>321</u>	<u>42.9</u>	2	321	43.0	321	42.9	<u>321</u>	<u>42.9</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores

Platform Notes

Bios settings:
Hardware Prefetcher: Enabled
Adjacent Cache Line Prefetch: Enabled
Intel SpeedStep Technology: Disabled

General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2,
456.hmmer, for peak, are compiled in 64-bit mode

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/opt/SmartHeap_8.1/lib -lsmartheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 20:03:17 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 22 July 2008.