



# SPEC<sup>®</sup> CINT2006 Result

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## IBM Corporation

## SPECint<sup>®</sup>\_rate2006 = Not Run

## IBM System x3400 (Intel Xeon E5430)

## SPECint\_rate\_base2006 = 102

CPU2006 license: 11

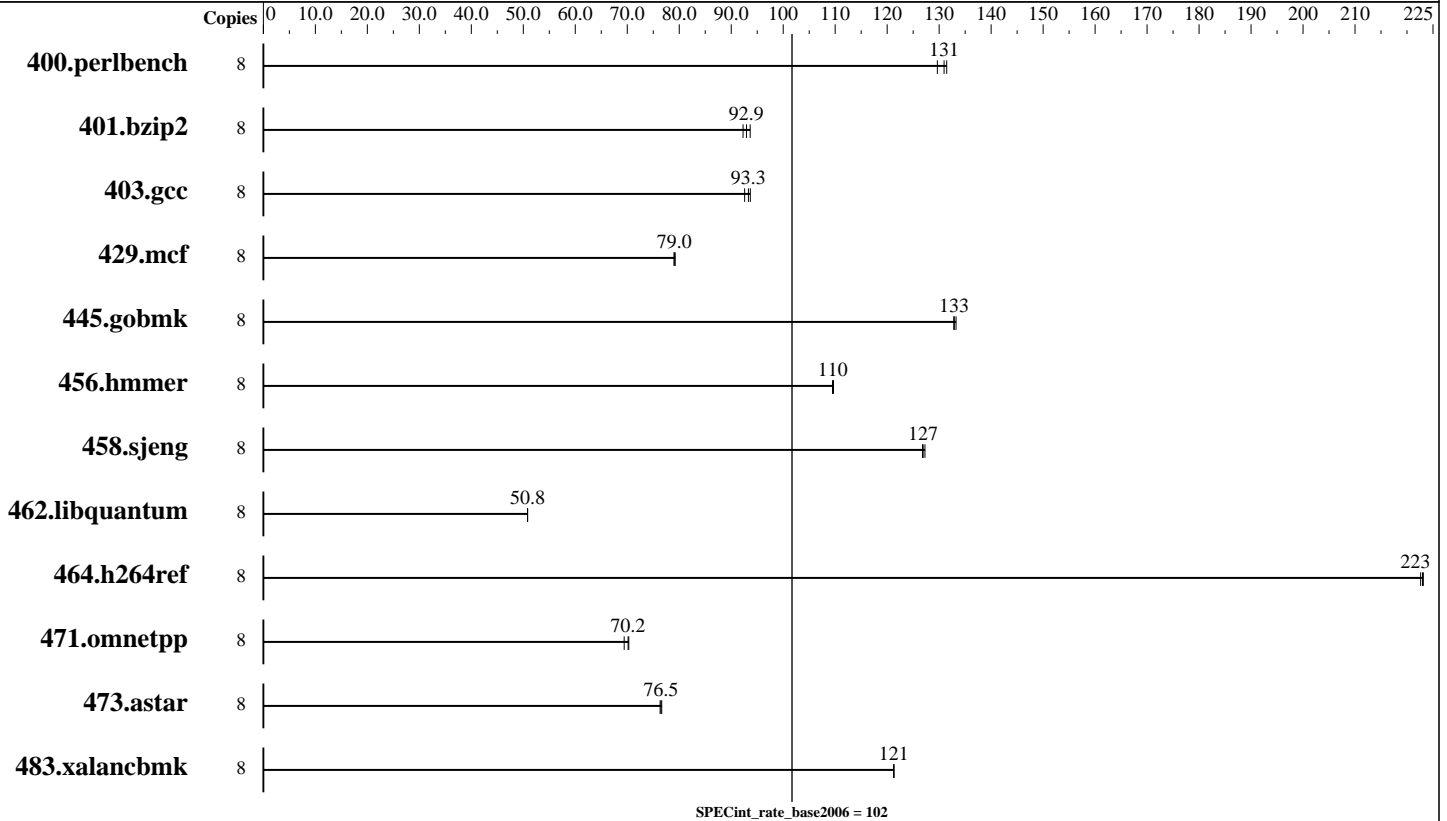
Test date: Apr-2008

Test sponsor: IBM Corporation

Hardware Availability: Jan-2008

Tested by: IBM Corporation

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon E5430  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)  
 Disk Subsystem: 1 x 80 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64), Kernel 2.6.16.21-0.8-smp  
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	603	130	595	131	<b>597</b>	<b>131</b>							
401.bzip2	8	<b>831</b>	<b>92.9</b>	837	92.3	824	93.6							
403.gcc	8	696	92.6	<b>690</b>	<b>93.3</b>	688	93.7							
429.mcf	8	<b>923</b>	<b>79.0</b>	923	79.0	921	79.2							
445.gobmk	8	632	133	630	133	<b>631</b>	<b>133</b>							
456.hammer	8	682	109	681	110	<b>681</b>	<b>110</b>							
458.sjeng	8	761	127	763	127	<b>763</b>	<b>127</b>							
462.libquantum	8	3261	50.8	<b>3262</b>	<b>50.8</b>	3262	50.8							
464.h264ref	8	795	223	793	223	<b>794</b>	<b>223</b>							
471.omnetpp	8	720	69.4	711	70.3	<b>713</b>	<b>70.2</b>							
473.astar	8	736	76.3	<b>734</b>	<b>76.5</b>	733	76.6							
483.xalancbmk	8	455	121	455	121	<b>455</b>	<b>121</b>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

All benchmarks compiled in 32-bit mode  
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled  
taskset utility used to bind CPU(s) to processes  
This result is measured on an IBM System x 3500 Server. Note  
that the IBM System x 3500 and IBM System x 3400 are electrically  
equivalent.

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revC.20090713.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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