SPEC® CINT2006 Result
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IBM Corporation

SPECint®_rate2006 = Not Run

IBM System x3850 M2 (Intel Xeon E7320)
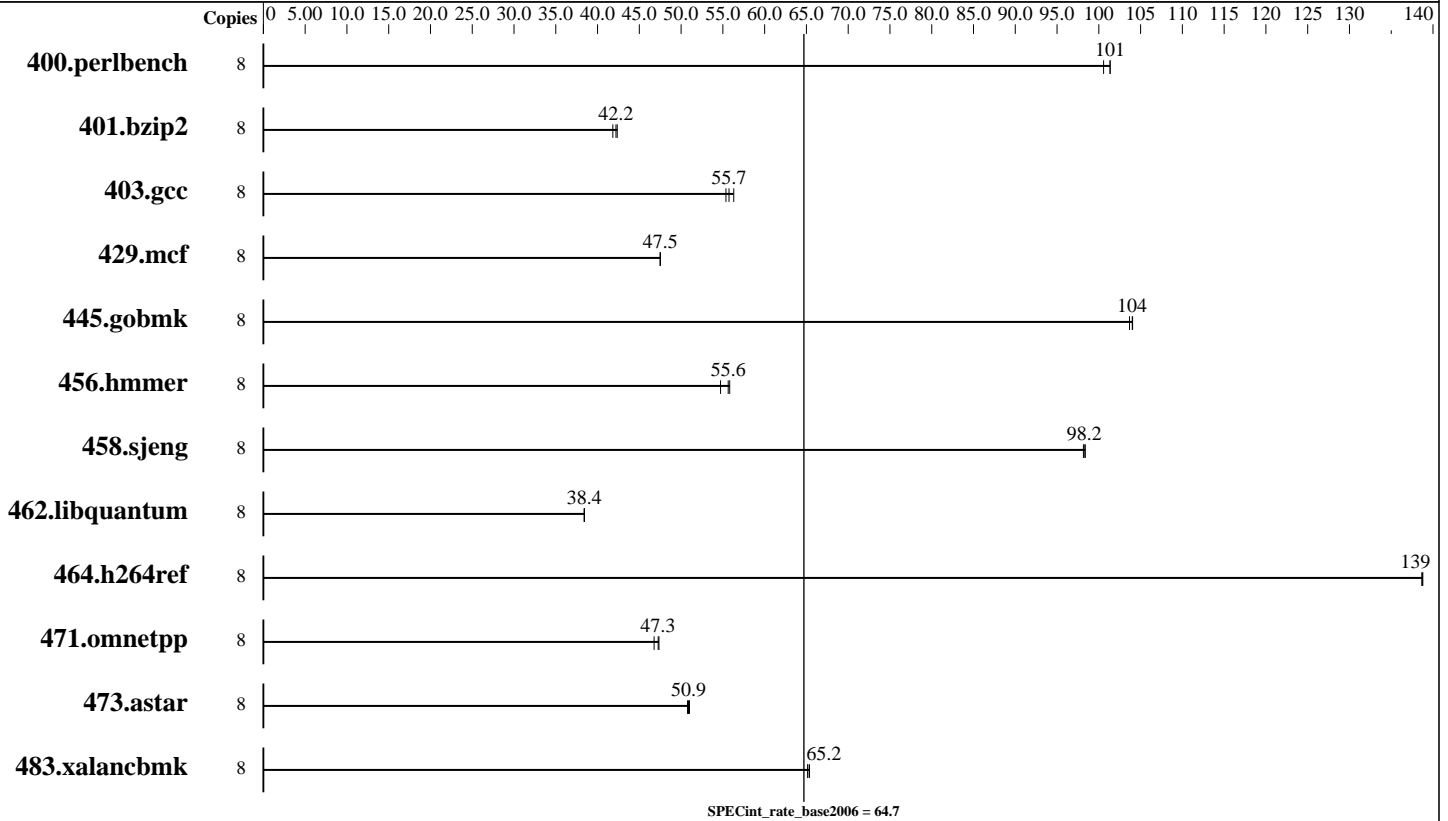
SPECint_rate_base2006 = 64.7

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Mar-2008
Hardware Availability: Nov-2007
Software Availability: Nov-2007

Benchmark	Copies	Result
400.perlbench	8	101
401.bzip2	8	42.2
403.gcc	8	55.7
429.mcf	8	47.5
445.gobmk	8	104
456.hmmer	8	55.6
458.sjeng	8	98.2
462.libquantum	8	38.4
464.h264ref	8	139
471.omnetpp	8	47.3
473.astar	8	50.9
483.xalancbmk	8	65.2

SPECint_rate_base2006 = 64.7

Hardware

CPU Name:	Intel Xeon E7320
CPU Characteristics:	Quad Core, 2.13 GHz
CPU MHz:	2133
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip, 2 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	64 GB (32 x 2 GB DDR2-5300 ECC)
Disk Subsystem:	1 x 146 GB SAS, 10000 RPM
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15

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Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	777	101	771	101	**771**	**101**							
401.bzip2	8	1823	42.4	**1829**	**42.2**	1846	41.8							
403.gcc	8	1144	56.3	1163	55.4	**1156**	**55.7**							
429.mcf	8	**1535**	**47.5**	1536	47.5	1535	47.5							
445.gobmk	8	807	104	809	104	**807**	**104**							
456.hmmer	8	**1342**	**55.6**	1364	54.7	1337	55.8							
458.sjeng	8	984	98.4	**985**	**98.2**	986	98.2							
462.libquantum	8	4313	38.4	4317	38.4	**4313**	**38.4**							
464.h264ref	8	1277	139	**1276**	**139**	1276	139							
471.omnetpp	8	**1058**	**47.3**	1056	47.3	1069	46.8							
473.astar	8	1101	51.0	1106	50.8	**1104**	**50.9**							
483.xalancbmk	8	844	65.4	**847**	**65.2**	847	65.2							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
    All benchmarks compiled in 32-bit mode
    taskset utility used to bind CPU(s) processes
```

Base Compiler Invocation

C benchmarks:
```
     icc
```

C++ benchmarks:
```
     icpc
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
     -fast -inline-calloc -opt-malloc-options=3
```

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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