



# SPEC® CINT2006 Result

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## IBM Corporation

## SPECint®\_rate2006 = Not Run

## IBM System x3500 (Intel Xeon X5450)

## SPECint\_rate\_base2006 = 109

CPU2006 license: 11

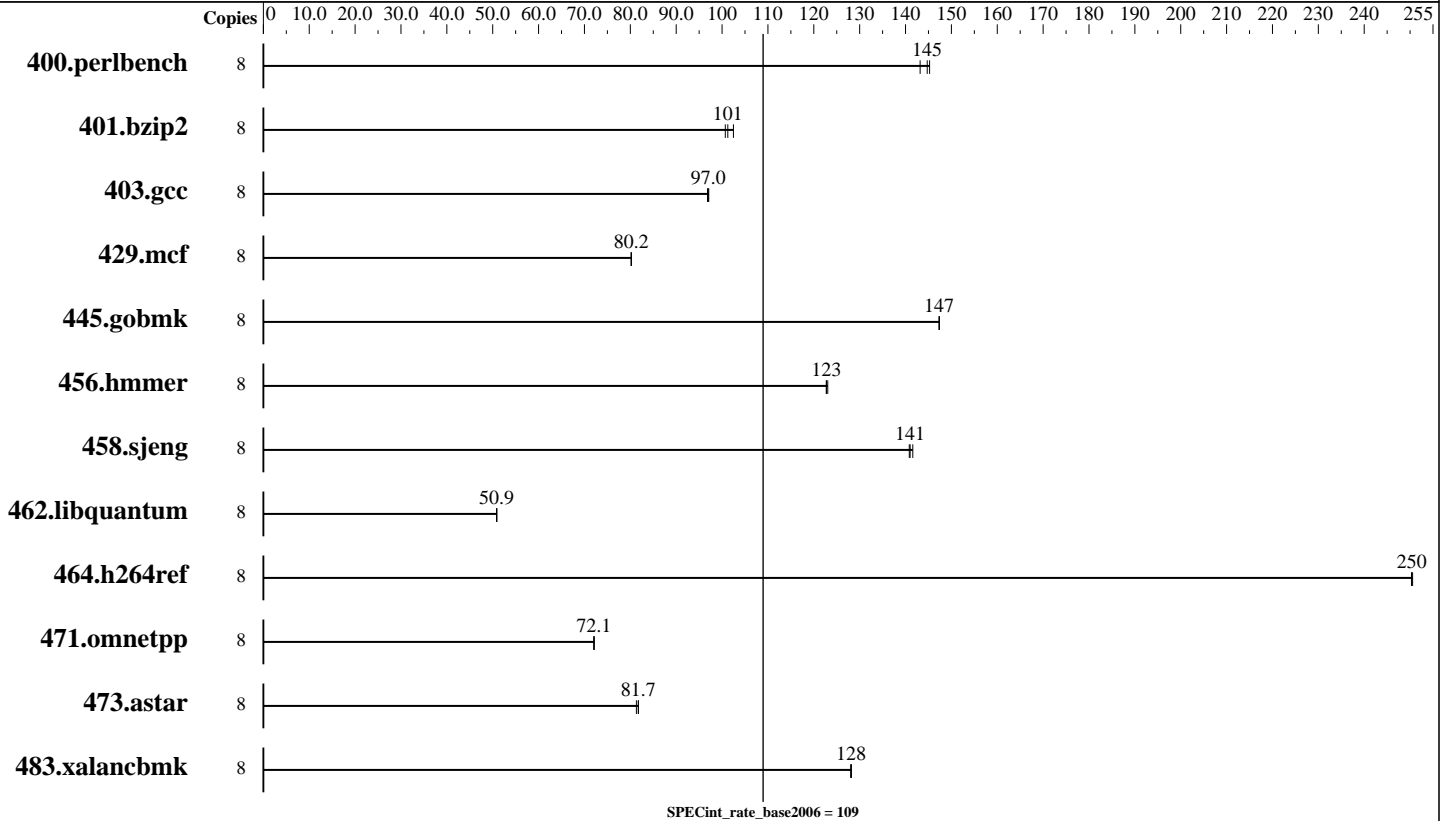
Test date: Feb-2008

Test sponsor: IBM Corporation

Hardware Availability: Jan-2008

Tested by: IBM Corporation

Software Availability: Nov-2007



### Hardware

CPU Name: Intel Xeon X5450  
 CPU Characteristics: 1333MHz system bus  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)  
 Disk Subsystem: 1 x 80 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SuSE Linux Enterprise Server 10 (x86\_64), Kernel 2.6.16.21-0.8-smp  
 Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l\_cc\_p\_10.1.008  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: Not Applicable  
 Other Software: MicroQuill SmartHeap 8.1 Binutils 2.17.50.0.15



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	546	143	538	145	<b><u>540</u></b>	<b><u>145</u></b>							
401.bzip2	8	767	101	753	102	<b><u>762</u></b>	<b><u>101</u></b>							
403.gcc	8	663	97.1	665	96.8	<b><u>664</u></b>	<b><u>97.0</u></b>							
429.mcf	8	910	80.2	909	80.3	<b><u>910</u></b>	<b><u>80.2</u></b>							
445.gobmk	8	<b><u>569</u></b>	<b><u>147</u></b>	569	147	570	147							
456.hammer	8	<b><u>608</u></b>	<b><u>123</u></b>	607	123	608	123							
458.sjeng	8	687	141	684	142	<b><u>686</u></b>	<b><u>141</u></b>							
462.libquantum	8	<b><u>3259</u></b>	<b><u>50.9</u></b>	3258	50.9	3259	50.9							
464.h264ref	8	<b><u>707</u></b>	<b><u>250</u></b>	707	250	707	251							
471.omnetpp	8	<b><u>694</u></b>	<b><u>72.1</u></b>	693	72.1	694	72.1							
473.astar	8	687	81.8	691	81.3	<b><u>687</u></b>	<b><u>81.7</u></b>							
483.xalancbmk	8	431	128	<b><u>431</u></b>	<b><u>128</u></b>	431	128							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

All benchmarks compiled in 32-bit mode  
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled  
taskset utility used to bind CPU(s) to processes

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast -inline-calloc -opt-malloc-options=3

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/users/rahul/cpu2006.1.0/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090714.05.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090714.05.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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