



SPEC® CINT2006 Result

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Supermicro Motherboard PDSM4+

SPECint®2006 = 11.5
SPECint_base2006 = 10.9

CPU2006 license: 001176

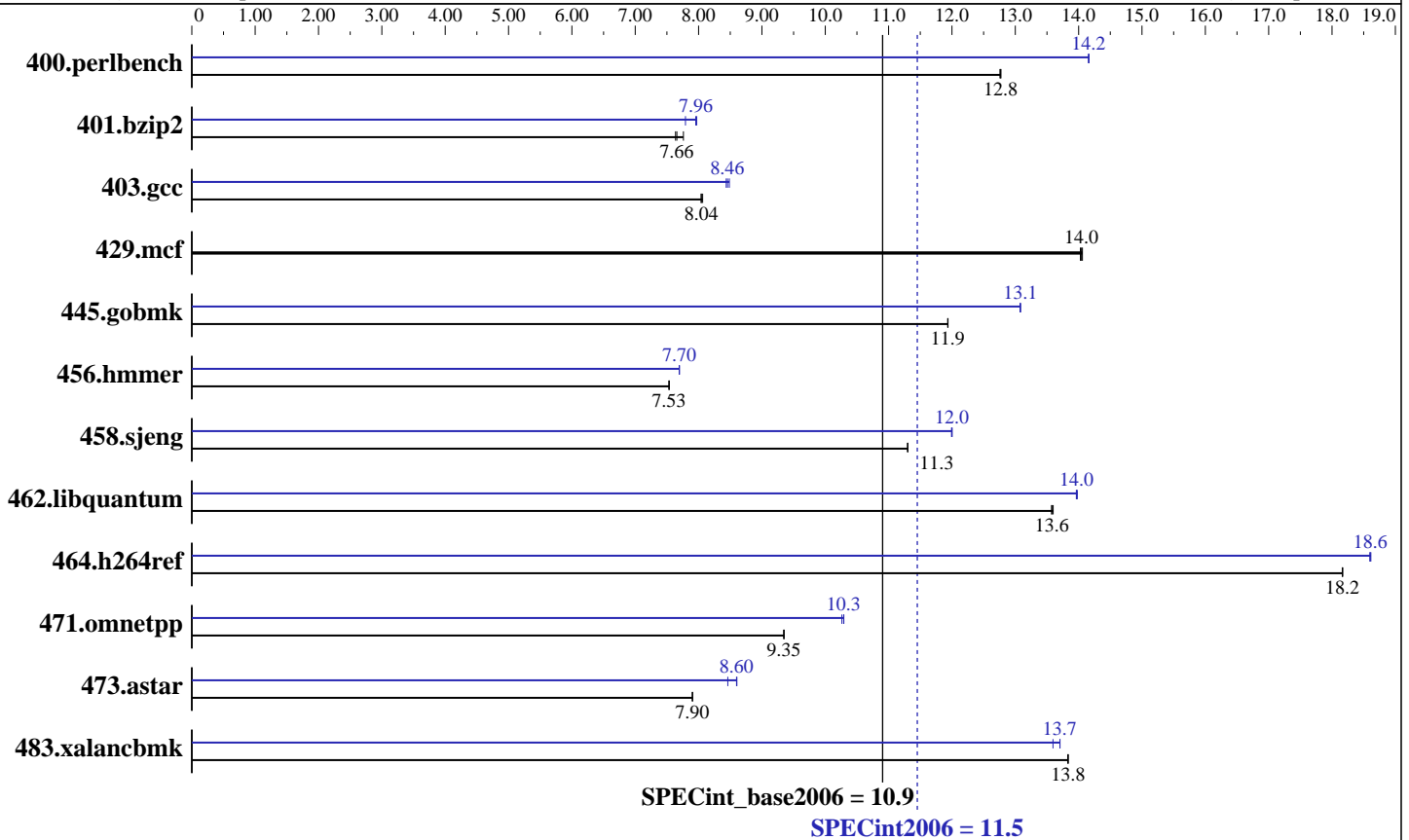
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Apr-2007

Hardware Availability: Apr-2007

Software Availability: Apr-2007



Hardware

CPU Name: Intel Core 2 Duo E4300
 CPU Characteristics: 1.8GHz 800MHz bus
 CPU MHz: 1800
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 2 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 2 GB (4 X 512MB, DDR2 667MHz, CL5, ECC)
 Disk Subsystem: WD2500YS-01SHB1 250GB SATA II, 7200RPM
 Other Hardware: None

Software

Operating System: Windows XP Professional w/ SP2
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Build no 20070322Z
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: SmartHeap Library Version 8.0 from
<http://www.microquill.com/>



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	766	12.8	765	12.8	765	12.8	690	14.2	690	14.2	690	14.2
401.bzip2	1243	7.76	1260	7.66	1264	7.64	1211	7.97	1238	7.79	1213	7.96
403.gcc	998	8.06	1001	8.04	1001	8.04	949	8.49	954	8.44	952	8.46
429.mcf	650	14.0	649	14.0	649	14.1	650	14.0	649	14.0	649	14.1
445.gobmk	879	11.9	879	11.9	879	11.9	802	13.1	802	13.1	802	13.1
456.hammer	1238	7.54	1239	7.53	1238	7.53	1212	7.70	1212	7.70	1213	7.69
458.sjeng	1071	11.3	1070	11.3	1071	11.3	1009	12.0	1008	12.0	1008	12.0
462.libquantum	1524	13.6	1527	13.6	1526	13.6	1482	14.0	1484	14.0	1484	14.0
464.h264ref	1218	18.2	1218	18.2	1218	18.2	1190	18.6	1189	18.6	1190	18.6
471.omnetpp	668	9.35	669	9.35	669	9.34	609	10.3	607	10.3	607	10.3
473.astar	888	7.91	888	7.90	888	7.90	830	8.46	816	8.60	816	8.60
483.xalancbmk	499	13.8	499	13.8	499	13.8	508	13.6	503	13.7	504	13.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

Tested systems can be used with CSE-823S-R500LP case,
For a general system, a 420W (minimum) ATX12V power supply [8-pin +12V AND 24-pin is recommended to assure system stability].
Product description located as of <http://www.supermicro.com/products/motherboard/Xeon3000/3010/PDSM4+.cfm>
The system bus runs at 800 MHz

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Peak Portability Flags

```
403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE
```

```
401.bzip2: Same as 400.perlbench
```

```
403.gcc: Same as 400.perlbench
```

```
429.mcf: basepeak = yes
```

```
445.gobmk: Same as 400.perlbench
```

```
456.hmmmer: Same as 400.perlbench
```

```
458.sjeng: Same as 400.perlbench
```

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Peak Optimization Flags (Continued)

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -O2 -Qipo
-Qprec-div- -Qunroll14 -Ob2 -Qsfa16 -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: Same as 471.omnetpp

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.xml>

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