



SPEC[®] CINT2006 Result

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Dell Inc.

PowerEdge 1950 (Intel Xeon processor 5160, 3.00GHz)

SPECint[®]_rate2006 = Not Run

SPECint_rate_base2006 = 53.7

CPU2006 license: 55

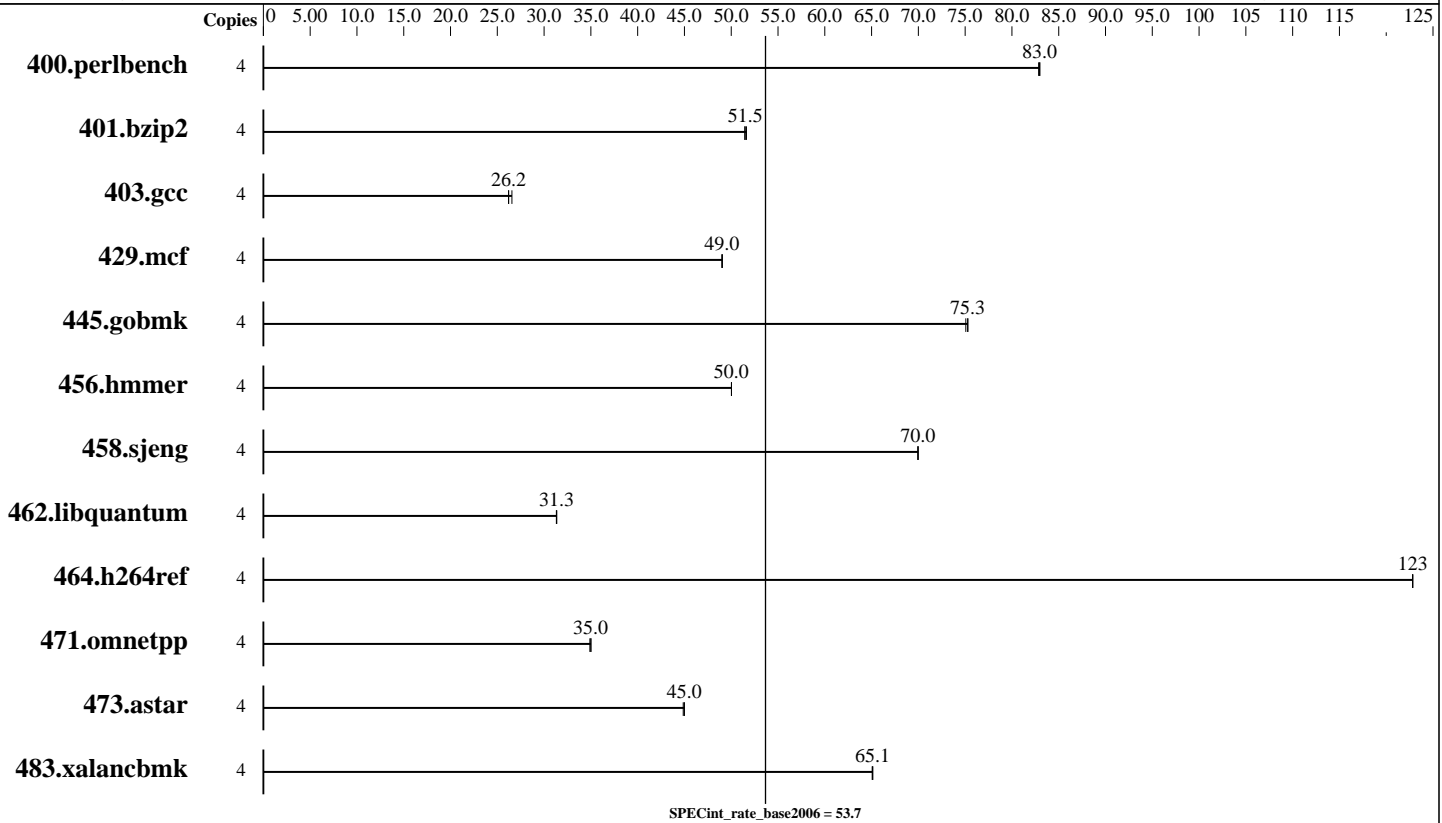
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Mar-2007

Hardware Availability: Jul-2006

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon 5160
 CPU Characteristics: 1333 MHz Bus Speed
 CPU MHz: 3000
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2 GB 667MHz CL5 DDR2 FB-DIMM SDRAM)
 Disk Subsystem: 1 x 73GB SAS 15k RPM
 Other Hardware: None

Software

Operating System: Microsoft Windows Server 2003 Enterprise x64 Edition + SP1(64-bit)
 Compiler: Intel C++ Compiler 9.1 for IA32 (20061103Z) Microsoft Visual Studio 2005
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: Not Applicable
 Other Software: MicroQuill SmartHeap Library 8.0



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|------------|-------------|-------------|-------------|-------------|-------------|--------|---------|-------|---------|-------|---------|-------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 4 | <u>471</u> | <u>83.0</u> | 471 | 83.0 | 472 | 82.9 | | | | | | | |
| 401.bzip2 | 4 | 748 | 51.6 | 750 | 51.4 | <u>750</u> | <u>51.5</u> | | | | | | | |
| 403.gcc | 4 | 1212 | 26.6 | 1229 | 26.2 | <u>1228</u> | <u>26.2</u> | | | | | | | |
| 429.mcf | 4 | <u>744</u> | <u>49.0</u> | 744 | 49.0 | 744 | 49.0 | | | | | | | |
| 445.gobmk | 4 | 559 | 75.1 | <u>557</u> | <u>75.3</u> | 557 | 75.3 | | | | | | | |
| 456.hammer | 4 | 746 | 50.0 | 746 | 50.0 | <u>746</u> | <u>50.0</u> | | | | | | | |
| 458.sjeng | 4 | 692 | 70.0 | 692 | 70.0 | <u>692</u> | <u>70.0</u> | | | | | | | |
| 462.libquantum | 4 | 2647 | 31.3 | <u>2645</u> | <u>31.3</u> | 2644 | 31.3 | | | | | | | |
| 464.h264ref | 4 | <u>721</u> | <u>123</u> | 721 | 123 | 721 | 123 | | | | | | | |
| 471.omnetpp | 4 | 716 | 34.9 | 714 | 35.0 | <u>714</u> | <u>35.0</u> | | | | | | | |
| 473.astar | 4 | 624 | 45.0 | 626 | 44.9 | <u>624</u> | <u>45.0</u> | | | | | | | |
| 483.xalancbmk | 4 | <u>424</u> | <u>65.1</u> | 424 | 65.1 | 424 | 65.1 | | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Snoop Filter enabled in BIOS

Base Compiler Invocation

C benchmarks:
icl -Qc99

C++ benchmarks:
icl

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32M.lib -link /FORCE:MULTIPLE

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlW32M.lib
-link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/dell.cpu2006.ic91.flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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