SPEC® CINT2006 Result

Fujitsu Siemens Computers

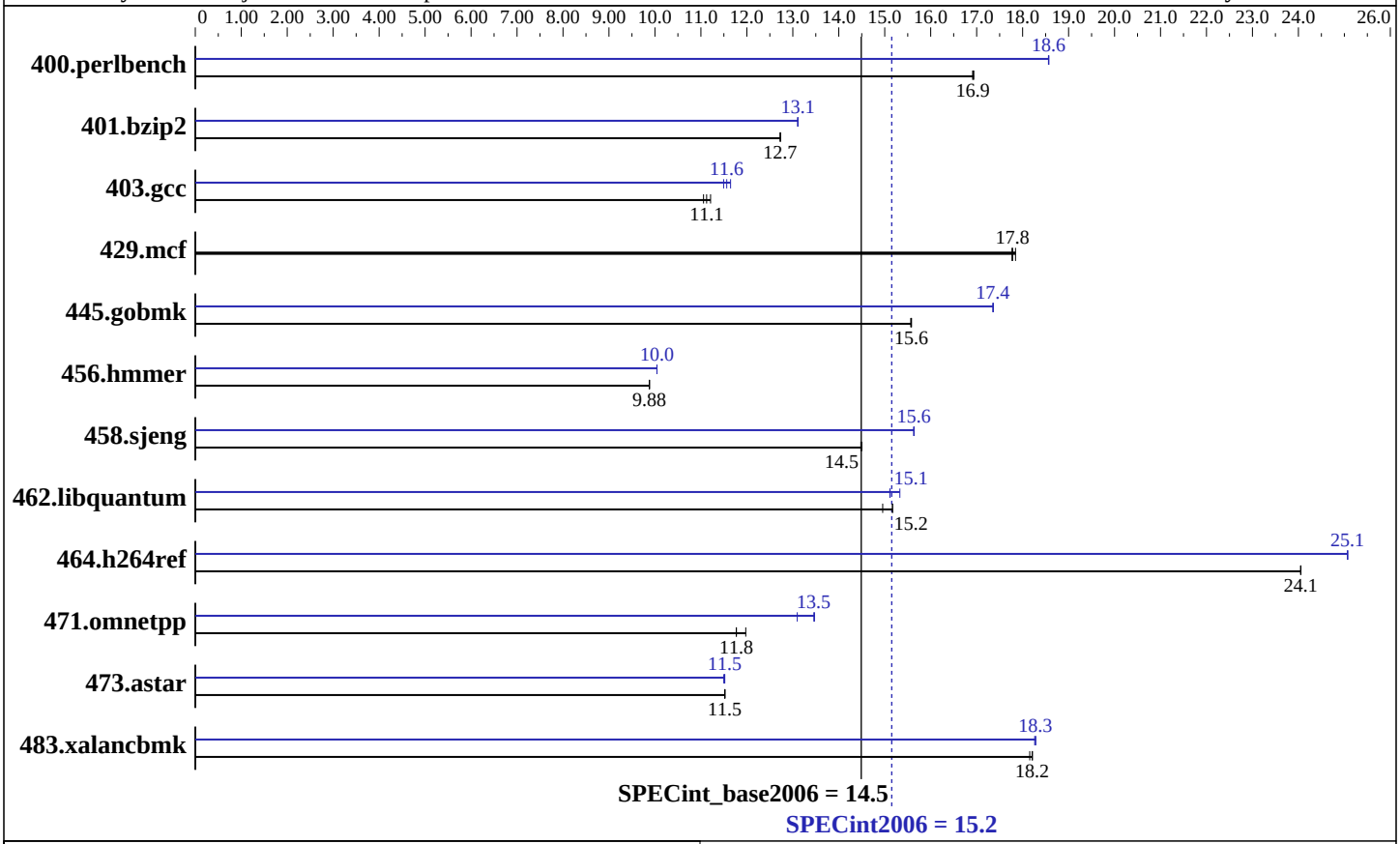
CELSIUS R540, Intel Xeon processor E5345

SPECint®2006 = 15.2

SPECint_base2006 = 14.5

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Mar-2007
Hardware Availability: Nov-2006
Software Availability: Jan-2007



Hardware

CPU Name:	Intel Xeon E5345
CPU Characteristics:	E5345
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (4x2 GB DDR2 5300F, 2 rank, CL5-5-5,with ECC)
Disk Subsystem:	SATA II 7200 rpm
Other Hardware:	None

Software

Operating System:	Windows XP, 64 bit Edition
Compiler:	Intel C++ Compiler for 32-bit applications - Version 9.1, Build 20070109Z Microsoft Visual Studio .NET 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.0

SPEC CINT2006 Result

Fujitsu Siemens Computers

CELSIUS R540, Intel Xeon processor E5345

SPECint2006 = 15.2

SPECint_base2006 = 14.5

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Mar-2007
Hardware Availability: Nov-2006
Software Availability: Jan-2007

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**577**	**16.9**	577	16.9	578	16.9	526	18.6	526	18.6	**526**	**18.6**
401.bzip2	758	12.7	758	12.7	**758**	**12.7**	736	13.1	**736**	**13.1**	736	13.1
403.gcc	**724**	**11.1**	728	11.1	718	11.2	**696**	**11.6**	700	11.5	691	11.6
429.mcf	**513**	**17.8**	513	17.8	511	17.8	**513**	**17.8**	513	17.8	511	17.8
445.gobmk	674	15.6	**673**	**15.6**	673	15.6	**604**	**17.4**	604	17.4	604	17.4
456.hmmer	944	9.88	944	9.88	**944**	**9.88**	**929**	**10.0**	929	10.0	929	10.0
458.sjeng	834	14.5	836	14.5	**835**	**14.5**	**774**	**15.6**	774	15.6	774	15.6
462.libquantum	**1366**	**15.2**	1385	15.0	1366	15.2	1352	15.3	1371	15.1	**1371**	**15.1**
464.h264ref	920	24.1	920	24.0	**920**	**24.1**	883	25.1	**883**	**25.1**	883	25.1
471.omnetpp	522	12.0	531	11.8	**531**	**11.8**	**464**	**13.5**	464	13.5	477	13.1
473.astar	**610**	**11.5**	609	11.5	610	11.5	611	11.5	**609**	**11.5**	609	11.5
483.xalancbmk	380	18.2	**379**	**18.2**	379	18.2	378	18.3	**378**	**18.3**	377	18.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
BIOS default settings have been used, except:
Snoop Filter Enabled
  (The Snoop Filter is designed to reduce system bus utilization coming
  from cache misses. It can lead to significant memory performance
  improvements for several workstation applications on suitable memory
  configurations.)
Adjacent Cache Line Prefetch Enabled
  (Prefetches a cache line pair of data instead of only the cache line
  that contains the requested data. This helps to improve data access time
  for some applications, especially for single processors.)
```

General Notes

```
For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
`-fast -F512000000 shlW32M.lib -link -FORCE:MULTIPLE`

C++ benchmarks:
`-fast -Qcxx-features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -F512000000 shlW32M.lib -link -FORCE:MULTIPLE`

401.bzip2: Same as 400.perlbench

Continued on next page

Peak Optimization Flags (Continued)

403.gcc: `Same as 400.perlbench`

429.mcf: `basepeak = yes`

445.gobmk: `Same as 400.perlbench`

456.hmmer: `Same as 400.perlbench`

458.sjeng: `Same as 400.perlbench`

462.libquantum: `Same as 400.perlbench`

464.h264ref: `Same as 400.perlbench`

C++ benchmarks:
`-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qcxx-features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.18.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.18.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 11:52:38 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 3 April 2007.