



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint®_rate2006 = 70.5

CELSIUS R640, Intel Xeon E5335 processor

SPECint_rate_base2006 = 68.2

CPU2006 license: 22

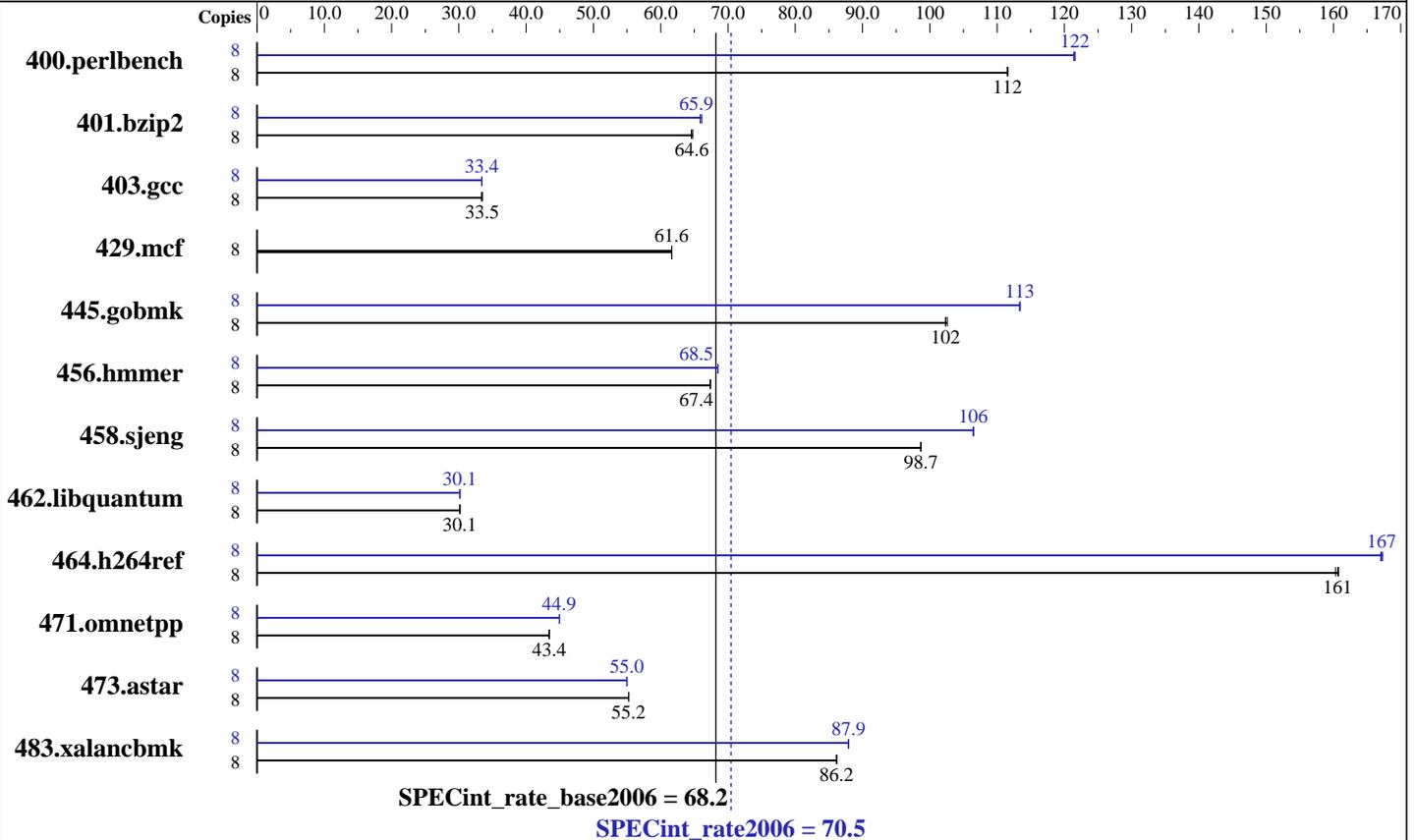
Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Feb-2007

Hardware Availability: Nov-2006

Software Availability: Jan-2007



Hardware

CPU Name: Intel Xeon E5335
 CPU Characteristics: E5335
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
 Disk Subsystem: SATA II 7200 rpm
 Other Hardware: None

Software

Operating System: Windows XP, 64 bit Edition
 Compiler: Intel C++ Compiler for 32-bit applications
 - Version 9.1, Build 20070109Z
 Microsoft Visual Studio .NET 2003 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 8.0



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint_rate2006 = 70.5

CELSIUS R640, Intel Xeon E5335 processor

SPECint_rate_base2006 = 68.2

CPU2006 license: 22

Test date: Feb-2007

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Nov-2006

Tested by: Fujitsu Siemens Computers

Software Availability: Jan-2007

Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	701	112	701	112	<u>701</u>	<u>112</u>	8	644	121	643	122	<u>643</u>	<u>122</u>
401.bzip2	8	1192	64.8	<u>1195</u>	<u>64.6</u>	1196	64.6	8	<u>1171</u>	<u>65.9</u>	1168	66.1	1172	65.8
403.gcc	8	1930	33.4	1922	33.5	<u>1925</u>	<u>33.5</u>	8	<u>1926</u>	<u>33.4</u>	1926	33.4	1929	33.4
429.mcf	8	1183	61.7	1184	61.6	<u>1184</u>	<u>61.6</u>	8	1183	61.7	1184	61.6	<u>1184</u>	<u>61.6</u>
445.gobmk	8	820	102	818	103	<u>820</u>	<u>102</u>	8	741	113	740	113	<u>740</u>	<u>113</u>
456.hammer	8	1108	67.4	1108	67.4	<u>1108</u>	<u>67.4</u>	8	1090	68.5	1090	68.5	<u>1090</u>	<u>68.5</u>
458.sjeng	8	981	98.6	981	98.7	<u>981</u>	<u>98.7</u>	8	909	106	909	106	<u>909</u>	<u>106</u>
462.libquantum	8	5499	30.1	<u>5498</u>	<u>30.1</u>	5497	30.2	8	5498	30.2	5498	30.1	<u>5498</u>	<u>30.1</u>
464.h264ref	8	1104	160	1101	161	<u>1102</u>	<u>161</u>	8	1060	167	1058	167	<u>1059</u>	<u>167</u>
471.omnetpp	8	1152	43.4	<u>1151</u>	<u>43.4</u>	1151	43.4	8	1112	45.0	<u>1113</u>	<u>44.9</u>	1113	44.9
473.astar	8	1017	55.2	<u>1017</u>	<u>55.2</u>	1016	55.3	8	<u>1022</u>	<u>55.0</u>	1021	55.0	1022	55.0
483.xalancbmk	8	<u>641</u>	<u>86.2</u>	640	86.2	641	86.1	8	628	88.0	<u>628</u>	<u>87.9</u>	628	87.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

BIOS default settings have been used, except:
High Bandwith Option Enabled

General Notes

'start /b /wait /affinity' command is used to bind CPU(s) to processes

For information about Fujitsu Siemens Computers in your country please see:
<http://www.fujitsu-siemens.com/countries>

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99
C++ benchmarks:
icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint_rate2006 = 70.5

CELSIUS R640, Intel Xeon E5335 processor

SPECint_rate_base2006 = 68.2

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Feb-2007

Hardware Availability: Nov-2006

Software Availability: Jan-2007

Base Portability Flags (Continued)

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:

-fast -F51200000 shlw32M.lib -link -FORCE:MULTIPLE

C++ benchmarks:

-fast -Qcxx-features -F51200000 shlw32M.lib -link -FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:

400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F51200000
shlw32M.lib -link -FORCE:MULTIPLE

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

SPECint_rate2006 = 70.5

CELSIUS R640, Intel Xeon E5335 processor

SPECint_rate_base2006 = 68.2

CPU2006 license: 22

Test sponsor: Fujitsu Siemens Computers

Tested by: Fujitsu Siemens Computers

Test date: Feb-2007

Hardware Availability: Nov-2006

Software Availability: Jan-2007

Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
-F512000000 shlw32M.lib -link -FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.19.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.19.xml

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

Report generated on Tue Jul 22 10:43:41 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 20 March 2007.