



# CINT2000 Result

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## SGI

### SGI Altix 350 (1400MHz/1.5MB, Itanium 2)

SPECint\_rate2000 = --

SPECint\_rate\_base2000 = 44.4

SPEC license #: 4 | Tested by: SGI | Test date: Dec-2003 | Hardware Avail: Jan-2004 | Software Avail: Dec-2003

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	162	40.1			
175.vpr	4	199	32.7			
176.gcc	4	106	48.0			
181.mcf	4	250	33.4			
186.crafty	4	90.2	51.5			
197.parser	4	249	33.6			
252.eon	4	91.9	65.6			
253.perlbnk	4	171	49.0			
254.gap	4	142	36.0			
255.vortex	4	122	72.1			
256.bzip2	4	185	37.7			
300.twolf	4	270	51.6			

#### Hardware

CPU: Intel Itanium 2  
CPU MHz: 1400  
FPU: Integrated  
CPU(s) enabled: 4 cores, 4 chips, 1 core/chip  
CPU(s) orderable: 2-16  
Parallel: No  
Primary Cache: 16KBI + 16KBD (on chip) per CPU  
Secondary Cache: 256KB (on chip) per CPU  
L3 Cache: 1.5MB (on chip) per CPU  
Other Cache: N/A  
Memory: 8 GB (8\*512MB PC2700 DIMMS per 2cpu module)  
Disk Subsystem: 1 x 36 GB SCSI (Seagate Cheetah 15k rpm)  
Other Hardware: None

#### Software

Operating System: SGI ProPack(TM) v2.3  
Compiler: Intel(R) C++ Compiler for Linux 8.0 (Build 20031017)  
MicroQuill SmartHeap Library 7.01 (www.microquill.com)  
File System: xfs  
System State: Single-user

### Notes/Tuning Information

+FDO: PASS1=-prof\_gen PASS2=-prof\_use

Baseline optimization flags:

C programs: -fast -auto\_ilp32 +FDO  
C++ programs: -fast -ansi\_alias +FDO  
Extra Libraries: libsmartheap64.a

Portability Flags:

176.gcc: -DSPEC\_CPU2000\_LP64 -Dalloca=\_builtin\_alloca -D\_LIBC  
186.crafty: -DLINUX\_i386  
252.eon: -DSPEC\_CPU2000\_LP64 -DHAS\_ERRLIST -DFMAX\_IS\_DOUBLE  
253.perlbnk: -DSPEC\_CPU2000\_LP64 -DSPEC\_CPU2000\_NEED\_BOOL  
-DSPEC\_CPU2000\_LINUX\_IA64 -DSPEC\_CPU2000\_GLIBC22  
254.gap: -DSPEC\_CPU2000\_LP64 -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_IS\_USG  
-DSYS\_HAS\_IOCTL\_PROTO -DSYS\_HAS\_TIME\_PROTO -DSYS\_HAS\_SIGNAL\_PROTO  
255.vortex: -DSPEC\_CPU2000\_LP64

Processes were bound to CPUs using dplace.